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Circuits, Devices and Systems A First Course in Electrical Engineering John Wiley & Sons

Presents a multi-objective design approach to the many powermagnetic devices in use today Power Magnetic Devices: A Multi-Objective Design Approach addresses the design of power magnetic devices—including inductors, transformers, electromagnets, and rotating electric machinery—using a structured design approach based on formal single- and multi-objective optimization. The book opens with a discussion of evolutionary-computing-based optimization. Magnetic analysis techniques useful to the design of all the devices considered in the book are then set forth. This material is then used for inductor design so readers can start the design process. Core loss is next considered; this material is used to support transformer design. A chapter on force and torque production feeds into a chapter on electromagnet design. This is followed by chapters on rotating machinery and the design of a permanent magnet AC machine. Finally, enhancements to the design process including thermal analysis and AC conductor losses due to skin and proximity effects are set forth. Power Magnetic Devices: Focuses on the design process as it relates to power magnetic devices such as inductors, transformers, electromagnets, and rotating machinery Offers a structured design approach based on single- and multi-objective optimization Helps experienced designers take advantage of new techniques which can yield superior designs with less engineering time Provides numerous case studies throughout the book to facilitate readers' comprehension of the analysis and design process Includes Powerpoint-slide-based student and instructor lecture notes and MATLAB-based examples, toolboxes, and design codes Designed to support the educational needs of students, Power Magnetic Devices: A Multi-Objective Design Approach also serves as a valuable reference tool for practicing engineers and designers. MATLAB examples are available via the book support site.

Up-to-date coverage of the analysis and applications of coplanar waveguides to microwave circuits and antennas The unique feature of coplanar waveguides, as opposed to more conventional waveguides, is their uniplanar construction, in which all of the conductors are aligned on the same side of the substrate. This feature simplifies manufacturing and allows faster and less expensive characterization using on-wafer techniques. Coplanar Waveguide Circuits, Components, and Systems is an engineer's complete resource, collecting all of the available data on the subject. Rainee Simons thoroughly discusses propagation parameters for conventional coplanar waveguides and includes valuable details such as the derivation of the fundamental equations, physical explanations, and numerical examples. Coverage also includes: Discontinuities and circuit elements Transitions to other transmission media Directional couplers, hybrids, and magic T Microelectromechanical systems based switches and phase shifters Tunable devices using ferroelectric materials

Photonic bandgap structures Printed circuit antennas

The increasing demand for electronic devices for private and industrial purposes lead designers and researchers to explore new electronic devices and circuits that can perform several tasks efficiently with low IC area and low power consumption. In addition, the increasing demand for portable devices intensifies the call from industry to design sensor elements, an efficient storage cell, and large capacity memory elements. Several industry-related issues have also forced a redesign of basic electronic components for certain specific applications. The researchers, designers, and students working in the area of electronic devices, circuits, and materials sometimes need standard examples with certain specifications. This breakthrough work presents this knowledge of standard electronic device and circuit design analysis, including advanced technologies and materials. This outstanding new volume presents the basic concepts and fundamentals behind devices, circuits, and systems. It is a valuable reference for the veteran engineer and a learning tool for the student, the practicing engineer, or an engineer from another field crossing over into electrical engineering. It is a must-have for any library.

Emerging Nanoelectronic Devices focuses on the future direction of semiconductor and emerging nanoscale device technology. As the dimensional scaling of CMOS approaches its limits, alternate information processing devices and microarchitectures are being explored to sustain increasing functionality at decreasing cost into the indefinite future. This is driving new paradigms of information processing enabled by innovative new devices, circuits, and architectures, necessary to support an increasingly interconnected world through a rapidly evolving internet. This original title provides a fresh perspective on emerging research devices in 26 up to date chapters written by the leading researchers in their respective areas. It supplements and extends the work performed by the Emerging Research Devices working group of the International Technology Roadmap for Semiconductors (ITRS). Key features: Serves as an authoritative tutorial on innovative devices and architectures that populate the dynamic world of "Beyond CMOS" technologies. Provides a realistic assessment of the strengths, weaknesses and key unknowns associated with each technology. Suggests guidelines for the directions of future development of each technology. Emphasizes physical concepts over mathematical development. Provides an essential resource for students, researchers and practicing engineers.

Electrical Overstress (EOS) continues to impact semiconductor manufacturing, semiconductor components and systems as technologies scale from micro- to nano-electronics. This book teaches the fundamentals of electrical overstress and how to minimize and mitigate EOS failures. The text provides a clear picture of EOS phenomena, EOS origins, EOS sources, EOS physics, EOS failure mechanisms, and EOS on-chip and system design. It provides an illuminating insight into the sources of EOS in manufacturing, integration of on-chip, and system level EOS protection networks, followed by examples in specific technologies, circuits, and chips. The book is unique in covering the EOS manufacturing issues from on-chip design and electronic design automation to factory-level EOS program management in today's modern world. Look inside for extensive coverage on: Fundamentals of electrical overstress, from EOS physics, EOS time scales, safe operating area (SOA), to physical models for EOS phenomena EOS sources in today's semiconductor manufacturing environment, and EOS program management, handling and EOS auditing processing to avoid EOS failures EOS failures in both semiconductor devices, circuits and system

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Discussion of how to distinguish between EOS events, and electrostatic discharge (ESD) events (e.g. such as human body model (HBM), charged device model (CDM), cable discharge events (CDE), charged board events (CBE), to system level IEC 61000-4-2 test events) EOS protection on-chip design practices and how they differ from ESD protection networks and solutions Discussion of EOS system level concerns in printed circuit boards (PCB), and manufacturing equipment Examples of EOS issues in state-of-the-art digital, analog and power technologies including CMOS, LDMOS, and BCD EOS design rule checking (DRC), LVS, and ERC electronic design automation (EDA) and how it is distinct from ESD EDA systems EOS testing and qualification techniques, and Practical off-chip ESD protection and system level solutions to provide more robust systems Electrical Overstress (EOS): Devices, Circuits and Systems is a continuation of the author's series of books on ESD protection. It is an essential reference and a useful insight into the issues that confront modern technology as we enter the nano-electronic era.

This book is also available through the Introductory Engineering Custom Publishing System. If you are interested in creating a course-pack that includes chapters from this book, you can get further information by calling 212-850-6272 or sending email inquiries to engineerjwiley.com. The authors offer a set of objectives at the beginning of each chapter plus a clear, concise description of abstract concepts. Focusing on preparing students to solve practical problems, it includes numerous colorful illustrative examples. Along with updated material on MOSFETS, the CRO for use in lab work, a thorough treatment of digital electronics and rapidly developing areas of electronics, it contains an expansive glossary of new terms and ideas.

On a daily basis, our requirements for technology become more innovative and creative and the field of electronics is helping to lead the way to more advanced appliances. This book gathers and evaluates the materials, designs, models, and technologies that enable the fabrication of fully elastic electronic devices that can tolerate high strain. Written by some of the most outstanding scientists in the field, it lays down the undisputed knowledge on how to make electronics withstand stretching. This monograph provides a review of the specific applications that directly benefit from highly compliant electronics, including transistors, photonic devices, and sensors. In addition to stretchable devices, the topic of ultraflexible electronics is treated, highlighting its upcoming significance for the industrial-scale production of electronic goods for the consumer. Divided into four parts covering: * Theory * Materials and Processes * Circuit Boards * Devices and Applications An unprecedented overview of this thriving area of research that nobody in the field - or intending to enter it - can afford to miss.

The fundamentals and implementation of digital electronics are essential to understanding the design and working of consumer/industrial electronics, communications, embedded systems, computers, security and military equipment. Devices used in applications such as these are constantly decreasing in size and employing more complex technology. It is therefore essential for engineers and students to understand the fundamentals, implementation and application principles of digital electronics, devices and integrated circuits. This is so that they can use the most appropriate and effective technique to suit their technical need. This book provides practical and comprehensive coverage of digital electronics, bringing together information on fundamental theory, operational aspects and potential applications. With

worked problems, examples, and review questions for each chapter, Digital Electronics includes: information on number systems, binary codes, digital arithmetic, logic gates and families, and Boolean algebra; an in-depth look at multiplexers, de-multiplexers, devices for arithmetic operations, flip-flops and related devices, counters and registers, and data conversion circuits; up-to-date coverage of recent application fields, such as programmable logic devices, microprocessors, microcontrollers, digital troubleshooting and digital instrumentation. A comprehensive, must-read book on digital electronics for senior undergraduate and graduate students of electrical, electronics and computer engineering, and a valuable reference book for professionals and researchers.

The book introduces flexible and stretchable wearable electronic systems and covers in detail the technologies and materials required for healthcare and medical applications. A team of excellent authors gives an overview of currently available flexible devices and thoroughly describes their physical mechanisms that enable sensing human conditions. In dedicated chapters, crucial components needed to realize flexible and wearable devices are discussed which include transistors and sensors and deal with memory, data handling and display. Additionally, suitable power sources based on photovoltaics, thermoelectric energy and supercapacitors are reviewed. A special chapter treats implantable flexible sensors for neural recording. The book editor concludes with a perspective on this rapidly developing field which is expected to have a great impact on healthcare in the 21st century.

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