

Solution Microelectronics Behzad Razavi

This market-leading textbook continues its standard of excellence and innovation built on the solid pedagogical foundation that instructors expect from Adel S. Sedra and Kenneth C. Smith. All material in the international sixth edition of Microelectronic Circuits is thoroughly updated to reflect changes in technology-CMOS technology in particular. These technological changes have shaped the book's organization and topical coverage, making it the most current resource available for teaching tomorrow's engineers how to analyze and design electronic circuits. In addition, end-of-chapter problems unique to this version of the text help preserve the integrity of instructor assignments.

Power consumption has become a major design consideration for battery-operated, portable systems as well as high-performance, desktop systems. Strict limitations on power dissipation must be met by the designer while still meeting ever higher computational requirements. A comprehensive approach is thus required at all levels of system design, ranging from algorithms and architectures to the logic styles and the underlying technology. Potentially one of the most important techniques involves combining architecture optimization with voltage scaling, allowing a trade-off between silicon area and low-power operation. Architectural optimization enables supply voltages of the order of 1 V using standard CMOS technology. Several techniques can

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also be used to minimize the switched capacitance, including representation, optimizing signal correlations, minimizing spurious transitions, optimizing sequencing of operations, activity-driven power down, etc. The high- efficiency of DC-DC converter circuitry required for efficient, low-voltage and low-current level operation is described by Stratakos, Sullivan and Sanders. The application of various low-power techniques to a chip set for multimedia applications shows that orders-of-magnitude reduction in power consumption is possible. The book also features an analysis by Professor Meindl of the fundamental limits of power consumption achievable at all levels of the design hierarchy. Svensson, of ISI, describes emerging adiabatic switching techniques that can break the CV^2f barrier and reduce the energy per computation at a fixed voltage. Srivastava, of AT&T, presents the application of aggressive shut-down techniques to microprocessor applications.

Pozar's new edition of Microwave Engineering includes more material on active circuits, noise, nonlinear effects, and wireless systems. Chapters on noise and nonlinear distortion, and active devices have been added along with the coverage of noise and more material on intermodulation distortion and related nonlinear effects. On active devices, there's more updated material on bipolar junction and field effect transistors. New and updated material on wireless communications systems, including link budget, link margin, digital modulation methods, and bit error rates is also part of the new edition. Other new material includes a section on transients on transmission lines, the

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theory of power waves, a discussion of higher order modes and frequency effects for microstrip line, and a discussion of how to determine unloaded.

The Acclaimed RF Microelectronics Best-Seller, Expanded and Updated for the Newest Architectures, Circuits, and Devices Wireless communication has become almost as ubiquitous as electricity, but RF design continues to challenge engineers and researchers. In the 15 years since the first edition of this classic text, the demand for higher performance has led to an explosive growth of RF design techniques. In RF Microelectronics, Second Edition, Behzad Razavi systematically teaches the fundamentals as well as the state-of-the-art developments in the analysis and design of RF circuits and transceivers. Razavi has written the second edition to reflect today's RF microelectronics, covering key topics in far greater detail. At nearly three times the length of the first edition, the second edition is an indispensable tome for both students and practicing engineers. With his lucid prose, Razavi now Offers a stronger tutorial focus along with hundreds of examples and problems Teaches design as well as analysis with the aid of step-by-step design procedures and a chapter dedicated to the design of a dual-band WiFi transceiver Describes new design paradigms and analysis techniques for circuits such as low-noise amplifiers, mixers, oscillators, and frequency dividers This edition's extensive coverage includes brand new chapters on mixers, passive devices, integer-N synthesizers, and fractional-N synthesizers. Razavi's teachings culminate in a new chapter that begins with WiFi's radio specifications and,

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step by step, designs the transceiver at the transistor level. Coverage includes Core RF principles, including noise and nonlinearity, with ties to analog design, microwave theory, and communication systems An intuitive treatment of modulation theory and wireless standards from the standpoint of the RF IC designer Transceiver architectures such as heterodyne, sliding-IF, directconversion, image-reject, and low-IF topologies. Low-noise amplifiers, including cascode common-gate and commonsource topologies, noise-cancelling schemes, and reactance-cancelling configurations Passive and active mixers, including their gain and noise analysis and new mixer topologies Voltage-controlled oscillators, phase noise mechanisms, and various VCO topologies dealing with noisepower-tuning trade-offs All-new coverage of passive devices, such as integrated inductors, MOS varactors, and transformers A chapter on the analysis and design of phase-locked loops with emphasis on low phase noise and low spur levels Two chapters on integer-N and fractional-N synthesizers, including the design of frequency dividers Power amplifier principles and circuit topologies along with transmitter architectures, such as polar modulation and outphasing Praise for CMOS: Circuit Design, Layout, and Simulation Revised Second Edition from the Technical Reviewers "A refreshing industrial flavor. Design concepts are presented as they are needed for 'just-in-time' learning. Simulating and designing circuits using SPICE is emphasized with literally hundreds of examples. Very few textbooks contain as much detail as this one. Highly recommended!" --Paul M. Furth, New Mexico State

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University "This book builds a solid knowledge of CMOS circuit design from the ground up. With coverage of process integration, layout, analog and digital models, noise mechanisms, memory circuits, references, amplifiers, PLLs/DLLs, dynamic circuits, and data converters, the text is an excellent reference for both experienced and novice designers alike." --Tyler J. Gomm, Design Engineer, Micron Technology, Inc. "The Second Edition builds upon the success of the first with new chapters that cover additional material such as oversampled converters and non-volatile memories. This is becoming the de facto standard textbook to have on every analog and mixed-signal designer's bookshelf." --Joe Walsh, Design Engineer, AMI Semiconductor CMOS circuits from design to implementation CMOS: Circuit Design, Layout, and Simulation, Revised Second Edition covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. Features include: Updated materials to reflect CMOS technology's movement into nanometer sizes Discussions on phase- and delay-locked loops, mixed-signal circuits, data converters, and circuit noise More than 1,000 figures, 200 examples, and over 500 end-of-chapter problems In-depth coverage of both analog and digital circuit-

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level design techniques Real-world process parameters and design rules The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid learning

Signals and Systems Using MATLAB, Third Edition, features a pedagogically rich and accessible approach to what can commonly be a mathematically dry subject. Historical notes and common mistakes combined with applications in controls, communications and signal processing help students understand and appreciate the usefulness of the techniques described in the text. This new edition features more end-of-chapter problems, new content on two-dimensional signal processing, and discussions on the state-of-the-art in signal processing. Introduces both continuous and discrete systems early, then studies each (separately) in-depth Contains an extensive set of worked examples and homework assignments, with applications for controls, communications, and signal processing Begins with a review on all the background math necessary to study the subject Includes MATLAB® applications in every chapter

This book provides the most comprehensive and in-depth coverage of the latest circuit design developments in RF CMOS technology. It is a practical and cutting-edge guide, packed with proven circuit techniques and innovative design methodologies for solving challenging problems associated with RF integrated circuits and systems. This

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invaluable resource features a collection of the finest design practices that may soon drive the system-on-chip revolution. Using this book's state-of-the-art design techniques, one can apply existing technologies in novel ways and to create new circuit designs for the future.

Analog Circuit Design

Focussing on micro- and nanoelectronics design and technology, this book provides thorough analysis and demonstration, starting from semiconductor devices to VLSI fabrication, designing (analog and digital), on-chip interconnect modeling culminating with emerging non-silicon/ nano devices. It gives detailed description of both theoretical as well as industry standard HSPICE, Verilog, Cadence simulation based real-time modeling approach with focus on fabrication of bulk and nano-devices. Each chapter of this proposed title starts with a brief introduction of the presented topic and ends with a summary indicating the futuristic aspect including practice questions. Aimed at researchers and senior undergraduate/graduate students in electrical and electronics engineering, microelectronics, nanoelectronics and nanotechnology, this book: Provides broad and comprehensive coverage from Microelectronics to Nanoelectronics including design in analog and digital electronics. Includes HDL, and VLSI design going into the nanoelectronics arena. Discusses devices, circuit analysis, design

methodology, and real-time simulation based on industry standard HSPICE tool. Explores emerging devices such as FinFETs, Tunnel FETs (TFETs) and CNTFETs including their circuit co-designing. Covers real time illustration using industry standard Verilog, Cadence and Synopsys simulations.

For Electrical Engineering courses in analog layout or professional layout designers. This text covers the issues involved in successfully laying out analog integrated circuits. Hastings provides clear guidance and does not stress theoretical physics or mathematical analysis of layouts. He emphasizes cross-sections of devices and carrier-based models of device operation as compared to the more common geometric and schematic representation of devices.

As the availability of powerful computer resources has grown over the last three decades, the art of computation of electromagnetic (EM) problems has also grown - exponentially. Despite this dramatic growth, however, the EM community lacked a comprehensive text on the computational techniques used to solve EM problems. The first edition of Numerical Techniques in Electromagnetics filled that gap and became the reference of choice for thousands of engineers, researchers, and students. The Second Edition of this bestselling text reflects the continuing increase in awareness and use of numerical techniques and incorporates advances and refinements made in recent years. Most notable

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among these are the improvements made to the standard algorithm for the finite difference time domain (FDTD) method and treatment of absorbing boundary conditions in FDTD, finite element, and transmission-line-matrix methods. The author also added a chapter on the method of lines. Numerical Techniques in Electromagnetics continues to teach readers how to pose, numerically analyze, and solve EM problems, give them the ability to expand their problem-solving skills using a variety of methods, and prepare them for research in electromagnetism. Now the Second Edition goes even further toward providing a comprehensive resource that addresses all of the most useful computation methods for EM problems.

Learn Linear Circuits by Actually Designing Them! With more examples, problems, applications, and tools, the Third Edition of Thomas and Rosa's The Analysis and Design of Linear Circuits presents an effective learn-by-doing approach to linear circuits. The authors not only discuss Laplace transforms, new passive and active elements, time-varying circuits, and fundamental analysis and design concepts, they also provide valuable skill-building exercises and tools. Here's how Thomas and Rosa's learn-by-doing approach works: * Apply concepts to practical problems. Throughout the text, the authors maintain a steady focus circuit design and include a greatly revised set of design examples,

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exercises, and homework problems. * Master the most modern software tools. The new edition now covers five of today's most widely used programs: Excel (r), Matlab(r), Electronics Workbench(r), and PSpice(r). * Explore real-world applications. The Third Edition now features many new real-world applications that are especially relevant to computer engineering, instrumentation, electronics, and signals. * Build circuits you can use. The text's early coverage of the Ideal Op-Amp will help readers design practical interface circuits, instrumentation systems, and cascade filters. * Evaluate competing designs. Thomas and Rosa show how to evaluate and select the best design from several correct approaches. * Develop circuit analysis and design skills. The text provides many opportunities to apply Laplace and related tools such as pole-zero diagrams, Bode diagrams, and Fourier series. This constant exposure to analysis and design tools will build practical skills.

Applicable for bookstore catalogue

Essential reading for experts in the field of RF circuit design and engineers needing a good reference. This book provides complete design procedures for multiple-pole Butterworth, Chebyshev, and Bessel filters. It also covers capacitors, inductors, and other components with their behavior at RF frequencies discussed in detail. Provides complete design procedures for

multiple-pole Butterworth, Chebyshev, and Bessel filters Covers capacitors, inductors, and other components with their behavior at RF frequencies discussed in detail

With the exponential growth of the number of Internet nodes, the volume of the data transported on the backbone has increased with the same trend. The load of the global Internet backbone will soon increase to tens of terabits per second. This indicates that the backbone bandwidth requirements will increase by a factor of 50 to 100 every seven years. Transportation of such high volumes of data requires suitable media with low loss and high bandwidth. Among the available transmission media, optical fibers achieve the best performance in terms of loss and bandwidth. High-speed data can be transported over hundreds of kilometers of single-mode fiber without significant loss in signal integrity. These fibers progressively benefit from reduction of cost and improvement of performance. Meanwhile, the electronic interfaces used in an optical network are not capable of exploiting the ultimate bandwidth of the fiber, limiting the throughput of the network. Different solutions at both the system and the circuit levels have been proposed to increase the data rate of the backbone. System-level solutions are based on the utilization of wave-division multiplexing (WDM), using different colors of light to transmit several sequences simultaneously. In parallel with that, a

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great deal of effort has been put into increasing the operating rate of the electronic transceivers using highly-developed fabrication processes and novel circuit techniques.

Fundamentals of Microelectronics, 2nd Edition is designed to build a strong foundation in both design and analysis of electronic circuits this text offers conceptual understanding and mastery of the material by using modern examples to motivate and prepare readers for advanced courses and their careers. The book's unique problem-solving framework enables readers to deconstruct complex problems into components that they are familiar with which builds the confidence and intuitive skills needed for success.

Very successful introductory electronics book. Features include effective pedagogical use of second color, flexible organization, devices fully covered in one place so that circuit characteristics are developed early. Hallmarks of the previous edition, such as breadth and depth of coverage, current and practical information, and coordination of the physical understanding of electronics with a theoretical, mathematical basis, have been retained. Provides practical examples of circuit design and analysis using PSpice, MATLAB, and the Smith Chart This book presents the three technologies used to deal with electronic circuits: MATLAB, PSpice, and Smith chart. It gives students, researchers, and practicing engineers the necessary design and modelling tools for validating electronic design concepts involving bipolar junction transistors (BJTs), field-effect transistors (FET), OP Amp circuits, and analog

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filters. Electronic Circuits with MATLAB®, PSpice®, and Smith Chart presents analytical solutions with the results of MATLAB analysis and PSpice simulation. This gives the reader information about the state of the art and confidence in the legitimacy of the solution, as long as the solutions obtained by using the two software tools agree with each other. For representative examples of impedance matching and filter design, the solution using MATLAB and Smith chart (Smith V4.1) are presented for comparison and crosscheck. This approach is expected to give the reader confidence in, and a deeper understanding of, the solution. In addition, this text: Increases the reader's understanding of the underlying processes and related equations for the design and analysis of circuits Provides a stepping stone to RF (radio frequency) circuit design by demonstrating how MATLAB can be used for the design and implementation of microstrip filters Features two chapters dedicated to the application of Smith charts and two-port network theory Electronic Circuits with MATLAB®, PSpice®, and Smith Chart will be of great benefit to practicing engineers and graduate students interested in circuit theory and RF circuits.

Microelectronic Circuits by Sedra and Smith has served generations of electrical and computer engineering students as the best and most widely-used text for this required course. Respected equally as a textbook and reference, "Sedra/Smith" combines a thorough presentation of fundamentals with an introduction to present-day IC technology. It remains the best text for helping students progress from circuit analysis to circuit design, developing design skills and insights that are essential to successful practice in the field. Significantly revised with the input of two new coauthors, slimmed down, and updated with the latest innovations, Microelectronic Circuits, Eighth Edition, remains the gold standard in providing the most

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comprehensive, flexible, accurate, and design-oriented treatment of electronic circuits available today.

Designed to accompany Microelectronic Circuits, Eighth Edition, by Adel S. Sedra, K. C. Smith, Tony Chan Carusone and Vincent Gaudet, Laboratory Explorations invites students to explore the realm of real-world engineering through practical, hands-on experimentation. Taking a learning-by-doing approach, it presents labs that focus on the development of practical engineering skills and design practices. Experiments start from concepts and hand analysis, and include simulation, measurement, and post-measurement discussion components. A complete solutions manual is also available for adopting instructors.

Suitable for undergraduates, postgraduates and professionals, this is a comprehensive text on physical and chemical equilibrium. De Nevers is also the author of Fluid Mechanics for Chemical Engineers.

Fundamentals of Microelectronics, 3rd Edition, is a comprehensive introduction to the design and analysis of electrical circuits, enabling students to develop the practical skills and engineering intuition necessary to succeed in their future careers. Through an innovative “analysis by inspection” framework, students learn to deconstruct complex problems into familiar components and reach solutions using basic principles. A step-by-step synthesis approach to microelectronics demonstrates the role of each device in a circuit while helping students build “design-oriented” mindsets. The revised third edition covers basic semiconductor physics, diode models and circuits, bipolar transistors and amplifiers, oscillators, frequency response, and more. In-depth chapters feature illustrative examples and numerous problems of varying levels of difficulty, including design problems that challenge

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students to select the bias and component values to satisfy particular requirements. The text contains a wealth of pedagogical tools, such as application sidebars, chapter summaries, self-tests with answers, and Multisim and SPICE software simulation problems. Now available in enhanced ePub format, Fundamentals of Microelectronics is ideal for single- and two-semester courses in the subject.

Nichols and Lekkas uncover the threats and vulnerabilities unique to the wireless communication, telecom, broadband, and satellite markets. They provide an overview of current commercial security solutions available on the open market.

Combining solid state devices with electronic circuits for an introductory-level microelectronics course, this textbook offers an integrated approach so that students can truly understand how a circuit works. A concise writing style is employed, with the right level of detail and physics to help students understand how a device works. Other features include an emphasis on modelling of electronic devices, and analysis of non-linear circuits. Spice problems, worked examples and end-of-chapter problems are included.

A comprehensive introduction to CMOS and bipolar analog IC design. The book presumes no prior knowledge of linear design, making it comprehensible to engineers with a non-analog back-ground. The emphasis is on practical design, covering the entire field with hundreds of examples to explain the choices. Concepts are presented following the history of their discovery. Content: 1. Devices Semiconductors, The Bipolar Transistor, The Integrated Circuit, Integrated NPN Transistors, The Case of the Lateral PNP Transistor, CMOS Transistors, The Substrate PNP Transistor, Diodes, Zener Diodes, Resistors, Capacitors, CMOS vs. Bipolar; 2. Simulation, DC Analysis, AC Analysis, Transient Analysis, Variations, Models, Diode Model,

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Bipolar Transistor Model, Model for the Lateral PNP Transistor, MOS Transistor Models, Resistor Models, Models for Capacitors; 3. Current Mirrors; 4. Differential Pairs; 5. Current Sources; 6. Time Out: Analog Measures, dB, RMS, Noise, Fourier Analysis, Distortion, Frequency Compensation; 7. Bandgap References; 8. Op Amps; 9. Comparators; 10. Transimpedance Amplifiers; 11. Timers and Oscillators; 12. Phase-Locked Loops; 13. Filters; 14. Power, Linear Regulators, Low Drop-Out Regulators, Switching Regulators, Linear Power Amplifiers, Switching Power Amplifiers; 15. A to D and D to A, The Delta-Sigma Converter; 16. Odds and Ends, Gilbert Cell, Multipliers, Peak Detectors, Rectifiers and Averaging Circuits, Thermometers, Zero-Crossing Detectors; 17. Layout.

By helping students develop an intuitive understanding of the subject, Microelectronics teaches them to think like engineers. The second edition of Razavi's Microelectronics retains its hallmark emphasis on analysis by inspection and building students' design intuition, and it incorporates a host of new pedagogical features that make it easier to teach and learn from, including: application sidebars, self-check problems with answers, simulation problems with SPICE and MULTISIM, and an expanded problem set that is organized by degree of difficulty and more clearly associated with specific chapter sections.

A large part of the research currently being conducted in the fields of materials science and engineering mechanics is devoted to carbon nanotubes and their applications. In this process, modeling is a very attractive investigation tool due to the difficulties in manufacturing and testing of nanomaterials. Continuum modeling offers significant advantages over atomistic modeling. Furthermore, the lack of accuracy in continuum methods can be overtaken by incorporating input data either from experiments or atomistic methods. This book reviews the

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recent progress in continuum modeling of carbon nanotubes and their composites. The advantages and disadvantages of continuum methods over atomistic methods are comprehensively discussed. Numerical models, mainly based on the finite element method, as well as analytical models are presented in a comparative way starting from the simulation of isolated pristine and defected nanotubes and proceeding to nanotube-based composites. The ability of continuum methods to bridge different scales is emphasized. Recommendations for future research are given by focusing on what still continuum methods have to learn from the nano-scale. The scope of the book is to provide current knowledge aiming to support researchers entering the scientific area of carbon nanotubes to choose the appropriate modeling tool for accomplishing their study and place their efforts to further improve continuum methods.

Fundamentals of Microelectronics John Wiley & Sons

This modern, pedagogic textbook from leading author Behzad Razavi provides a comprehensive and rigorous introduction to CMOS PLL design, featuring intuitive presentation of theoretical concepts, extensive circuit simulations, over 200 worked examples, and 250 end-of-chapter problems. The perfect text for senior undergraduate and graduate students.

Microelectronic Circuit Design is known for being a technically excellent text. The new edition has been revised to make the material more motivating and accessible to students while retaining a student-friendly approach. Jaeger has added more pedagogy and an emphasis on design through the use of design examples and design notes. Some pedagogical elements include chapter opening vignettes, chapter objectives, "Electronics in Action" boxes, a problem solving methodology, and "design note" boxes. The number of examples, including new design

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examples, has been increased, giving students more opportunity to see problems worked out. Additionally, some of the less fundamental mathematical material has been moved to the ARIS website. In addition this edition comes with a Homework Management System called ARIS, which includes 450 static problems.

"The increasing demand for high-speed transport of data has revitalized optical communications, leading to extensive work on high-speed device and circuit design. This book deals with the design of high-speed integrated circuits for optical communication transceivers. Building upon a detailed understanding of optical devices, the book describes the analysis and design of critical building blocks, such as transimpedance and limiting amplifiers, laser drivers, phase-locked loops, oscillators, clock and data recovery circuits, and multiplexers. This second edition of this best selling textbook has been updated to provide information on the latest developments in the field"--

For courses in DC/AC circuits: conventional flow The Latest Insights in Circuit Analysis Introductory Circuit Analysis, the number one acclaimed text in the field for over three decades, is a clear and interesting information source on a complex topic. The Thirteenth Edition contains updated insights on the highly technical subject, providing students with the most current information in circuit analysis. With updated software components and challenging review questions at the end of each chapter, this text engages students in a profound understanding of Circuit Analysis.

A concise introduction to circuit analysis designed to meet the needs of faculty who want to teach this material in a one semester course. Chapters have been carefully selected from Irwin, Basic Engineering Circuit Analysis, 7E.

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This advanced text and reference covers the design and implementation of integrated circuits for analog-to-digital and digital-to-analog conversion. It begins with basic concepts and systematically leads the reader to advanced topics, describing design issues and techniques at both circuit and system level. Gain a system-level perspective of data conversion units and their trade-offs with this state-of-the-art book. Topics covered include: sampling circuits and architectures, D/A and A/D architectures; comparator and op amp design; calibration techniques; testing and characterization; and more!

This book makes comprehension of material a top priority and encourages readers to be active participants in the learning process. The conventional-flow version of this book provides a readable and thorough approach to electronic devices and circuits, and support discussions with an abundance of learning aids to motivate and assist readers at every turn. The seventh edition of this well-established book features new internet link identifiers which bring the user to supplemental on-line resources. Covered topics include fundamental solid-state principles, common diode applications, amplifiers, oscillators and transistors. For professionals in the field of Electronics Technology.

Discover a fresh approach to efficient and insight-driven analog integrated circuit design in nanoscale-CMOS with this hands-on guide. Expert authors present a sizing methodology that employs SPICE-generated lookup tables, enabling close agreement between hand analysis and simulation. This enables the exploration of analog circuit tradeoffs using the gm/ID ratio as a central variable in script-based design flows, and eliminates time-consuming iterations in a circuit simulator. Supported by downloadable MATLAB code, and including over forty detailed worked examples, this book will provide professional analog circuit designers, researchers,

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and graduate students with the theoretical know-how and practical tools needed to acquire a systematic and re-use oriented design style for analog integrated circuits in modern CMOS. Intuitive Analog Circuit Design outlines ways of thinking about analog circuits and systems that let you develop a feel for what a good, working analog circuit design should be. This book reflects author Marc Thompson's 30 years of experience designing analog and power electronics circuits and teaching graduate-level analog circuit design, and is the ideal reference for anyone who needs a straightforward introduction to the subject. In this book, Dr. Thompson describes intuitive and "back-of-the-envelope" techniques for designing and analyzing analog circuits, including transistor amplifiers (CMOS, JFET, and bipolar), transistor switching, noise in analog circuits, thermal circuit design, magnetic circuit design, and control systems. The application of some simple rules of thumb and design techniques is the first step in developing an intuitive understanding of the behavior of complex electrical systems. Introducing analog circuit design with a minimum of mathematics, this book uses numerous real-world examples to help you make the transition to analog design. The second edition is an ideal introductory text for anyone new to the area of analog circuit design. Design examples are used throughout the text, along with end-of-chapter examples Covers real-world parasitic elements in circuit design and their effects

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