

## Power Amplifiers For The S C X And Ku Bands An Eda Perspective Signals And Communication Technology

Circuits for Emerging Technologies Beyond CMOS New exciting opportunities are abounding in the field of body area networks, wireless communications, data networking, and optical imaging. In response to these developments, top-notch international experts in industry and academia present Circuits at the Nanoscale: Communications, Imaging, and Sensing. This volume, unique in both its scope and its focus, addresses the state-of-the-art in integrated circuit design in the context of emerging systems. A must for anyone serious about circuit design for future technologies, this book discusses emerging materials that can take system performance beyond standard CMOS. These include Silicon on Insulator (SOI), Silicon Germanium (SiGe), and Indium Phosphide (InP). Three-dimensional CMOS integration and co-integration with Microelectromechanical (MEMS) technology and radiation sensors are described as well. Topics in the book are divided into comprehensive sections on emerging design techniques, mixed-signal CMOS circuits, circuits for communications, and circuits for imaging and sensing. Dr. Krzysztof Iniewski is a director at CMOS Emerging Technologies, Inc., a consulting company in Vancouver, British Columbia. His current research interests are in VLSI circuits for medical applications. He has published over 100 research papers in international journals and conferences, and he holds 18 international patents granted in the United States, Canada, France, Germany, and Japan. In this volume, he has assembled the contributions of over 60 world-reknown experts who are at the top of their field in the world of circuit design, advancing the bank of knowledge for all who work in this exciting and burgeoning area.

This book provides an overview of current efficiency enhancement and linearization techniques for silicon power amplifier designs. It examines the latest state of the art technologies and design techniques to address challenges for RF cellular mobile, base stations, and RF and mmW WLAN applications. Coverage includes material on current silicon (CMOS, SiGe) RF and mmW power amplifier designs, focusing on advantages and disadvantages compared with traditional GaAs implementations. With this book you will learn: The principles of linearization and efficiency improvement techniques The architectures allowing the optimum design of multimode Si RF and mmW power amplifiers How to make designs more efficient by employing new design techniques such as linearization and efficiency improvement Layout considerations Examples of schematic, layout, simulation and measurement results Addresses the problems of high power generation, faithful construction of non-constant envelope constellations, and efficient and well control power radiation from integrated silicon chips Demonstrates how silicon technology can solve problems and trade-offs of power amplifier design, including price, size, complexity and efficiency Written and edited by the top contributors to the field

Broadband RF and Microwave Amplifiers provides extensive coverage of broadband radio frequency (RF) and microwave power amplifier design, including well-known historical and recent novel schematic configurations, theoretical approaches, circuit simulation results, and practical implementation strategies. The text begins by introducing two-port networks to illustrate the behavior of linear and nonlinear circuits, explaining the basic principles of power amplifier design, and discussing impedance matching and broadband power amplifier design using lumped and distributed parameters. The book then: Shows how dissipative or lossy gain-compensation-matching circuits can offer an important trade-off between power gain, reflection coefficient, and operating frequency bandwidth Describes the design of broadband RF and microwave amplifiers using real frequency techniques (RFTs), supplying numerous examples based on the MATLAB® programming process Examines Class-E power amplifiers, Doherty

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amplifiers, low-noise amplifiers, microwave gallium arsenide field-effect transistor (GaAs FET)-distributed amplifiers, and complementary metal-oxide semiconductor (CMOS) amplifiers for ultra-wideband (UWB) applications Broadband RF and Microwave Amplifiers combines theoretical analysis with practical design to create a solid foundation for innovative ideas and circuit design techniques.

Power Amplifiers for the S-, C-, X- and Ku-bands An EDA Perspective Springer

This much-anticipated volume builds on the author's best selling and classic work, RF Power Amplifiers for Wireless Communications (Artech House, 1999), offering experienced engineers a more in-depth understanding of the theory and design of RF power amplifiers. An invaluable reference tool for RF, digital and system level designers, the book includes discussions on the most critical topics for professionals in the field, including envelope power management schemes and linearization.

Advanced Design Techniques for RF Power Amplifiers provides a deep analysis of theoretical aspects, modelling, and design strategies of RF high-efficiency power amplifiers. The book can be used as a guide by scientists and engineers dealing with the subject and as a text book for graduate and postgraduate students. Although primarily intended for skilled readers, it provides an excellent quick start for beginners.

Doherty Power Amplifiers: From Fundamentals to Advanced Design Methods is a great resource for both RF and microwave engineers and graduate students who want to understand and implement the technology into future base station and mobile handset systems. The book introduces the very basic operational principles of the Doherty Amplifier and its non-ideal behaviors. The different transconductance requirements for carrier and peaking amplifiers, reactive element effect, and knee voltage effect are described. In addition, several methods to correct imperfections are introduced, such as uneven input drive, gate bias adaptation, dual input drive and the offset line technique. Advanced design methods of Doherty Amplifiers are also explained, including multistage/multiway Doherty power amplifiers which can enhance the efficiency of the amplification of a highly-modulated signal. Other covered topics include signal tracking operation which increases the dynamic range, highly efficient saturated amplifiers, and broadband amplifiers, amongst other comprehensive, related topics. Specifically written on the Doherty Power Amplifier by the world's leading expert, providing an in-depth presentation of principles and design techniques Includes detailed analysis on correcting non-ideal behaviors of Doherty Power Amplifiers Presents advanced Doherty Power Amplifier architectures Envelope tracking technology is seen as the most promising efficiency enhancement technology for RF power amplifiers for 4G and beyond wireless communications. More and more organizations are investing and researching on this topic with huge potential in academic and commercial areas. This is the first book on the market to offer complete introduction, theory, and design considerations on envelope tracking for wireless communications. This resource presents you with a full introduction to the subject and covers underlying theory and practical design considerations.

RF CMOS Power Amplifiers: Theory Design and Implementation focuses on the design procedure and the testing issues of CMOS RF power amplifiers. This is the first monograph addressing RF CMOS power amplifier design for emerging wireless standards. The focus on power amplifiers for short is distance wireless personal and local area networks (PAN and LAN), however the design techniques are also applicable to emerging wide area networks (WAN) infrastructure using micro or pico cell networks. The book discusses CMOS power amplifier design principles and theory and describes the architectures and tradeoffs in designing linear and nonlinear power amplifiers. It then details design examples of RF CMOS power amplifiers for short distance wireless applications (e, g., Bluetooth, WLAN) including designs for multi-standard platforms. Design aspects of RF circuits in deep submicron CMOS are also discussed. RF CMOS Power Amplifiers: Theory Design and Implementation serves as

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a reference for RF IC design engineers and RD and R&D managers in industry, and for graduate students conducting research in wireless semiconductor IC design in general and with CMOS technology in particular.

Achieve higher levels of performance, integration, compactness, and cost-effectiveness in the design and modeling of radio-frequency (RF) power amplifiers RF power amplifiers are important components of any wireless transmitter, but are often the limiting factors in achieving better performance and lower cost in a wireless communication system—presenting the RF IC design community with many challenges. The next-generation technological advances presented in this book are the result of cutting-edge research in the area of large-signal device modeling and RF power amplifier design at the Georgia Institute of Technology, and have the potential to significantly address issues of performance and cost-effectiveness in this area. Richly complemented with hundreds of figures and equations, Modeling and Design Techniques for RF Power Amplifiers introduces and explores the most important topics related to RF power amplifier design under one concise cover. With a focus on efficiency enhancement techniques and the latest advances in the field, coverage includes: Device modeling for CAD Empirical modeling of bipolar devices Scalable modeling of RF MOSFETs Power amplifier IC design Power amplifier design in silicon Efficiency enhancement of RF power amplifiers The description of state-of-the-art techniques makes this book a valuable and handy reference for practicing engineers and researchers, while the breadth of coverage makes it an ideal text for graduate- and advanced undergraduate-level courses in the area of RF power amplifier design and modeling.

Here is a thorough treatment of distortion in RF power amplifiers. This unique resource offers expert guidance in designing easily linearizable systems that have low memory effects. It offers you a detailed understanding of how the matching impedances of a power amplifier and other RF circuits can be tuned to minimize overall distortion. What's more, you see how to build models that can be used for distortion simulations.

This text presents a full account of RF amplifiers and provides a thorough understanding of power amplifier principles and their applications. This comprehensive book covers all important design techniques for power amplifiers and includes mathematical derivations and the assumptions used to develop design rules.

Switchmode RF and Microwave Power Amplifiers, Third Edition is an essential reference book on developing RF and microwave switchmode power amplifiers. The book combines theoretical discussions with practical examples, allowing readers to design high-efficiency RF and microwave power amplifiers on different types of bipolar and field-effect transistors, design any type of high-efficiency switchmode power amplifiers operating in Class D or E at lower frequencies and in Class E or F and their subclasses at microwave frequencies with specified output power, also providing techniques on how to design multiband and broadband Doherty amplifiers using different bandwidth extension techniques and implementation technologies. This book provides the necessary information to understand the theory and practical implementation of load-network design techniques based on lumped and transmission-line elements. It brings a unique focus on switchmode RF and microwave power amplifiers that are widely used in cellular/wireless, satellite and radar communication systems which offer major power consumption savings. Provides a complete history of high-efficiency Class E and Class F techniques Presents a new chapter on Class E with shunt capacitance and shunt filter to simplify the design of high-efficiency power amplifier with broader frequency bandwidths Covers different Doherty architectures, including integrated and monolithic implementations, which are and will be, used in modern communication systems to save power consumption and to reduce size and costs Includes extended coverage of multiband and broadband Doherty amplifiers with different frequency ranges and output powers using different bandwidth extension techniques Balances theory with practical implementation, avoiding a cookbook

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approach and enabling engineers to develop better designs, including hybrid, integrated and monolithic implementations

RF power amplifiers are implemented in communication, semiconductor wafer processing, magnetic resonance imaging (MRI), and radar systems to produce RF signal with the desired characteristics to perform several critical tasks in the entire system. They can be designed to operate in linear or switch-mode, depending on the specific application. This book explores the design and implementation methods for both linear and switch-mode amplifiers with real world engineering problems. The text discusses phased controlled switch-mode amplifiers and distortion and modulation effects in RF amplifiers. It illustrates the interface and integration of components and sub-systems for RF amplifiers. The material is further reinforced with MATLAB design files.

This practical resource offers expert guidance on the most critical aspects of microwave power amplifier design. This comprehensive book provides descriptions of all the major active devices, discusses large signal characterization, explains all the key circuit design procedures. Moreover you gain keen insight on the link between design parameters and technological implementation, helping you achieve optimal solutions with the most efficient utilization of available technologies. The book covers a broad range of essential topics, from requirements for high-power amplifiers, device models, phase noise and power combiners. to high-efficiency amplifiers, linear amplifier design, bias circuits, and thermal design.

Do you want to know how to design high efficiency RF and microwave solid state power amplifiers? Read this book to learn the main concepts that are fundamental for optimum amplifier design. Practical design techniques are set out, stating the pros and cons for each method presented in this text. In addition to novel theoretical discussion and workable guidelines, you will find helpful running examples and case studies that demonstrate the key issues involved in power amplifier (PA) design flow. Highlights include: Clarification of topics which are often misunderstood and misused, such as bias classes and PA nomenclatures. The consideration of both hybrid and monolithic microwave integrated circuits (MMICs).

Discussions of switch-mode and current-mode PA design approaches and an explanation of the differences. Coverage of the linearity issue in PA design at circuit level, with advice on low distortion power stages. Analysis of the hot topic of Doherty amplifier design, plus a description of advanced techniques based on multi-way and multi-stage architecture solutions. High Efficiency RF and Microwave Solid State Power Amplifiers is: an ideal tutorial for MSc and postgraduate students taking courses in microwave electronics and solid state circuit/device design; a useful reference text for practising electronic engineers and researchers in the field of PA design and microwave and RF engineering. With its unique unified vision of solid state amplifiers, you won't find a more comprehensive publication on the topic.

Advances in electronics have pushed mankind to create devices, ranging from - credible gadgets to medical equipment to spacecraft instruments. More than that, modern society is getting used to—if not dependent on—the comfort, solutions, and astonishing amount of information brought by these devices. One ?eld that has continuously bene?tted from those advances is the radio frequency integrated c- cuit (RFIC) design, which in its turn has promoted countless bene?ts to the mankind as a payback. Wireless communications is one prominent example of what the - vances in electronics have enabled and their consequences to our daily life. How could anyone back in the eighties think of the possibilities opened by the wireless local area networks (WLANs) that can be found today in a host of places, such as public libraries, coffee shops, trains, to name just a few? How can a youngster, who lives this true WLAN experience nowadays, imagine a world without it? This book dealswith the design oflinearCMOS RF PowerAmpli?ers(PAs). The RF PA is a very important part of the RF transceiver, the device that enables wireless communications. Two important aspects that are key to keep the advances in RF PA design at an accelerate pace are treated: ef?ciency

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enhancement and frequency-tunable capability. For this purpose, the design of two different integrated circuits realized in a 0.11- $\mu\text{m}$  technology is presented, each one addressing a different aspect. With respect to efficiency enhancement, the design of a dynamic supply RF power amplifier is treated, making up the material of Chaps. 2 to 4.

This first book on load-pull systems is intended for readers with a broad knowledge of high frequency transistor device characterization, nonlinear and linear microwave measurements, RF power amplifiers and transmitters. Load-Pull Techniques with Applications to Power Amplifier Design fulfills the demands of users, designers, and researchers both from industry and academia who have felt the need of a book on this topic. It presents a comprehensive reference spanning different load-pull measurement systems, waveform measurement and engineering systems, and associated calibration procedures for accurate large signal characterization. Besides, this book also provides in-depth practical considerations required in the realization and usage of load-pull and waveform engineering systems. In addition, it also provides procedure to design application specific load-pull setup and includes several case studies where the user can customize architecture of load-pull setups to meet any specific measurement requirements. Furthermore, the materials covered in this book can be part of a full semester graduate course on microwave device characterization and power amplifier design.

This book tackles both high efficiency and high linearity power amplifier (PA) design in low-voltage CMOS. With its emphasis on theory, design and implementation, the book offers a guide for those actively involved in the design of fully integrated CMOS wireless transceivers. Offering mathematical background, as well as intuitive insight, the book is essential reading for RF design engineers and researchers and is also suitable as a text book.

A majority of people now have a digital mobile device whether it be a cell phone, laptop, or blackberry. Now that we have the mobility we want it to be more versatile and dependable; RF power amplifiers accomplish just that. These amplifiers take a small input and make it stronger and larger creating a wider area of use with a more robust signal. Switching mode RF amplifiers have been theoretically possible for decades, but were largely impractical because they distort analog signals until they are unrecognizable. However, distortion is not an issue with digital signals—like those used by WLANs and digital cell phones—and switching mode RF amplifiers have become a hot area of RF/wireless design. This book explores both the theory behind switching mode RF amplifiers and design techniques for them. \*Provides essential design and implementation techniques for use in CDMA2000, WiMAX, and other digital mobile standards \*Both authors have written several articles on the topic and are well known in the industry \*Includes specific design equations to greatly simplify the design of switchmode amplifiers

Design techniques for nonlinear microwave circuits are much less developed than for linear microwave circuits. Until now there has been no up-to-date text available in this area. Current titles in this field are considered outdated and tend to focus on analysis, failing to adequately address design and measurement aspects. Giannini and Leuzzi provide the theoretical background to non-linear microwave circuits before going on to discuss the practical design and measurement of non-linear circuits and components. Non-linear Microwave Circuit Design reviews all of the established analysis and characterisation techniques available and provides detailed coverage of key modelling methods. Practical examples are used throughout the text to emphasise the design and application focus of the book. Provides a unique, design-focused, coverage of non-linear microwave circuits Covers the fundamental properties of nonlinear circuits and methods for device modelling Outlines non-linear measurement techniques and characterisation of active devices Reviews available design methodologies for non-linear power amplifiers and details advanced software modelling tools Provides the first detailed

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treatment of non-linear frequency multipliers, mixers and oscillators Focuses on the application potential of non-linear components Practicing engineers and circuit designers working in microwave and communications engineering and designing new applications, as well as senior undergraduates, graduate students and researchers in microwave and communications engineering and their libraries will find this a highly rewarding read.

Reconfigurable RF Power Amplifiers on Silicon for Wireless Handsets is intended to designers and researchers who have to tackle the efficiency/linearity trade-off in modern RF transmitters so as to extend their battery lifetime. High data rate 3G/4G standards feature broad channel bandwidths, high dynamic range and critical envelope variations which generally forces the power amplifier (PA) to operate in a low efficiency "backed-off" regime. Classic efficiency enhancement techniques such as Envelope Elimination and Restoration reveal to be little compliant with handset-dedicated PA implementation due to their channel-bandwidth-limited behavior and their increased die area consumption and/or bill-of-material. The architectural advances that are proposed in this book circumvent these issues since they put the stress on low die-area /low power-consumption control circuitry. The advantages of silicon over III/V technologies are highlighted by several analogue signal processing techniques that can be implemented on-chip with a power amplifier. System-level and transistor-level simulations are combined to illustrate the principles of the proposed power adaptive solutions. Measurement on BICMOS demonstrators allows validating the functionality of dynamic linearity/efficiency management. In Reconfigurable RF Power Amplifiers on Silicon for Wireless Handsets, PA designers will find a review of technologies, architectures and theoretical formalisms (Volterra series...) that are traditionally related to PA design. Specific issues that one encounters in power amplifiers (such as thermal / memory effects, stability, VSWR sensitivity...) and the way of overcoming them are also extensively considered throughout this book.

This book provides a detailed review of millimeter-wave power amplifiers, discussing design issues and performance limitations commonly encountered in light of the latest research. Power amplifiers, which are able to provide high levels of output power and linearity while being easily integrated with surrounding circuitry, are a crucial component in wireless microwave systems. The book is divided into three parts, the first of which introduces readers to mm-wave wireless systems and power amplifiers. In turn, the second focuses on design principles and EDA concepts, while the third discusses future trends in power amplifier research. The book provides essential information on mm-wave power amplifier theory, as well as the implementation options and technologies involved in their effective design, equipping researchers, circuit designers and practicing engineers to design, model, analyze, test and implement high-performance, spectrally clean and energy-efficient mm-wave systems.

This cutting-edge resource presents a complete and systematic overview of the practical design considerations of radio frequency (RF) high efficiency load modulation power amplifiers (PA) for modern wireless communications for 4G and beyond. It provides comprehensive insight into all aspects of load modulation PA design and optimization not only covering design approaches specifically for passive and active load modulation operation but also hybrid with dynamic supply modulation and digital signal processing algorithms required for performance enhancement. Passive load impedance tuner design, dynamic load modulation PA, active load modulation PA and Doherty PA design for efficiently enhancement are explained. Readers find practical guidance into load modulation PA design for bandwidth extension, including video bandwidth enhancement techniques, broadband dynamic load amplifiers, topology selection, design procedures, and network output. This book presents the evolution and integration of classical load modulation PA topologies in order to meet new challenges in the field.

This extensively revised edition offers a comprehensive, practical, up-to-date understanding of how to tackle a power amplifier design with confidence and quickly determine the cause of malfunctioning hardware.

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A Comprehensive and Up-to-Date Treatment of RF and Microwave Transistor Amplifiers This book provides state-of-the-art coverage of RF and microwave transistor amplifiers, including low-noise, narrowband, broadband, linear, high-power, high-efficiency, and high-voltage. Topics covered include modeling, analysis, design, packaging, and thermal and fabrication considerations. Through a unique integration of theory and practice, readers will learn to solve amplifier-related design problems ranging from matching networks to biasing and stability. More than 240 problems are included to help readers test their basic amplifier and circuit design skills-and more than half of the problems feature fully worked-out solutions. With an emphasis on theory, design, and everyday applications, this book is geared toward students, teachers, scientists, and practicing engineers who are interested in broadening their knowledge of RF and microwave transistor amplifier circuit design.

This book is essential for audio power amplifier designers and engineers for one simple reason...it enables you as a professional to develop reliable, high-performance circuits. The Author Douglas Self covers the major issues of distortion and linearity, power supplies, overload, DC-protection and reactive loading. He also tackles unusual forms of compensation and distortion produced by capacitors and fuses. This completely updated fifth edition includes four NEW chapters including one on The XD Principle, invented by the author, and used by Cambridge Audio. Crosstalk, power amplifier input systems, and microcontrollers in amplifiers are also now discussed in this fifth edition, making this book a must-have for audio power amplifier professionals and audiophiles.

This second edition of the highly acclaimed RF Power Amplifiers has been thoroughly revised and expanded to reflect the latest challenges associated with power transmitters used in communications systems. With more rigorous treatment of many concepts, the new edition includes a unique combination of class-tested analysis and industry-proven design techniques. Radio frequency (RF) power amplifiers are the fundamental building blocks used in a vast variety of wireless communication circuits, radio and TV broadcasting transmitters, radars, wireless energy transfer, and industrial processes. Through a combination of theory and practice, RF Power Amplifiers, Second Edition provides a solid understanding of the key concepts, the principle of operation, synthesis, analysis, and design of RF power amplifiers. This extensive update boasts: up to date end of chapter summaries; review questions and problems; an expansion on key concepts; new examples related to real-world applications illustrating key concepts and brand new chapters covering 'hot topics' such as RF LC oscillators and dynamic power supplies. Carefully edited for superior readability, this work remains an essential reference for research & development staff and design engineers. Senior level undergraduate and graduate electrical engineering students will also find it an invaluable resource with its practical examples & summaries, review questions and end of chapter problems. Key features: • A fully revised solutions manual is now hosted on a companion website alongside new simulations. • Extended treatment of a broad range of topologies of RF power amplifiers. • In-depth treatment of state-of-the art of modern transmitters and a new chapter on oscillators. • Includes problem-solving methodology, step-by-step derivations and closed-form design equations with illustrations.

This peer-reviewed book explores the methodologies that are used for effective research, design and innovation in the vast field of millimeter-wave circuits, and describes how these have to be modified to fit the uniqueness of high-frequency nanoelectronics design. Each chapter focuses on a specific research challenge related to either small form factors or higher operating frequencies. The book first examines nanodevice scaling and the emerging electronic design automation tools that can be used in millimeter-wave research, as well as the singular challenges of combining deep-submicron and millimeter-wave design. It also demonstrates the importance of

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considering, in the millimeter-wave context, system-level design leading to differing packaging options. Further, it presents integrated circuit design methodologies for all major transceiver blocks typically employed at millimeter-wave frequencies, as these methodologies are normally fundamentally different from the traditional design methodologies used in analogue and lower-frequency electronics. Lastly, the book discusses the methodologies of millimeter-wave research and design for extreme or harsh environments, rebooting electronics, the additional opportunities for terahertz research, and the main differences between the approaches taken in millimeter-wave research and terahertz research.

This comprehensive book on audio power amplifier design will appeal to members of the professional audio engineering community as well as the student and enthusiast. Designing Audio Power Amplifiers begins with power amplifier design basics that a novice can understand and moves all the way through to in-depth design techniques for very sophisticated audiophiles and professional audio power amplifiers. This book is the single best source of knowledge for anyone who wishes to design audio power amplifiers. It also provides a detailed introduction to nearly all aspects of analog circuit design, making it an effective educational text. Develop and hone your audio amplifier design skills with in-depth coverage of these and other topics: Basic and advanced audio power amplifier design Low-noise amplifier design Static and dynamic crossover distortion demystified Understanding negative feedback and the controversy surrounding it Advanced NFB compensation techniques, including TPC and TMC Sophisticated DC servo design MOSFET power amplifiers and error correction Audio measurements and instrumentation Overlooked sources of distortion SPICE simulation for audio amplifiers, including a tutorial on LTspice SPICE transistor modeling, including the VDMOS model for power MOSFETs Thermal design and the use of ThermalTrak(tm) transistors Four chapters on class D amplifiers, including measurement techniques Professional power amplifiers Switch-mode power supplies (SMPS). design Static and dynamic crossover distortion demystified Understanding negative feedback and the controversy surrounding it Advanced NFB compensation techniques, including TPC and TMC Sophisticated DC servo design MOSFET power amplifiers and error correction Audio measurements and instrumentation Overlooked sources of distortion SPICE simulation for audio amplifiers, including a tutorial on LTspice SPICE transistor modeling, including the VDMOS model for power MOSFETs Thermal design and the use of ThermalTrak(tm) transistors Four chapters on class D amplifiers, including measurement techniques Professional power amplifiers Switch-mode power supplies (SMPS). the use of ThermalTrak(tm) transistors Four chapters on class D amplifiers, including measurement techniques Professional power amplifiers Switch-mode power supplies (SMPS).

Build high-performance, spectrally clean, energy-efficient mm-wave power amplifiers and transmitters with this cutting-edge guide to designing, modeling, analysing, implementing and testing new mm-wave systems. Suitable for students, researchers and practicing engineers, this self-contained guide provides in-depth coverage of state-of-the-art semiconductor devices and technologies, linear and nonlinear power amplifier technologies, efficient power combining systems, circuit concepts, system architectures and system-on-a-chip realizations. The world's foremost experts from industry and academia cover all aspects of the design process, from device technologies to system

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architectures. Accompanied by numerous case studies highlighting practical design techniques, tradeoffs and pitfalls, this is a superb resource for those working with high-frequency systems.

Wireless voice and data communications have made great improvements, with connectivity now virtually ubiquitous. Users are demanding essentially perfect transmission and reception of voice and data. The infrastructure that supports this wide connectivity and nearly error-free delivery of information is complex, costly, and continually being improved. This resource describes the mathematical methods and practical implementations of linearization techniques for RF power amplifiers for mobile communications. This includes a review of RF power amplifier design for high efficiency operation. Readers are also provided with mathematical approaches to modeling nonlinear dynamical systems, which can be applied in the context of modeling the PA for identification in a pre-distortion system. This book also describes typical approaches to linearization and digital pre-distortion that are used in practice.

The work establishes the design flow for the optimization of linear CMOS power amplifiers from the first steps of the design to the final IC implementation and tests. The authors also focuses on design guidelines of the inductor's geometrical characteristics for power applications and covers their measurement and characterization. Additionally, a model is proposed which would facilitate designs in terms of transistor sizing, required inductor quality factors or minimum supply voltage. The model considers limitations that CMOS processes can impose on implementation. The book also provides different techniques and architectures that allow for optimization.

This book provides a detailed review of power amplifiers, including classes and topologies rarely covered in books, and supplies sufficient information to allow the reader to design an entire amplifier system, and not just the power amplification stage. A central aim is to furnish readers with ideas on how to simplify the design process for a preferred power amplifier stage by introducing software-based routines in a programming language of their choice. The book is in two parts, the first focusing on power amplifier theory and the second on EDA concepts. Readers will gain enough knowledge of RF and microwave transmission theory, principles of active and passive device design and manufacturing, and power amplifier design concepts to allow them to quickly create their own programs, which will help to accelerate the transceiver design process. All circuit designers facing the challenge of designing an RF or microwave power amplifier for frequencies from 2 to 18 GHz will find this book to be a valuable asset.

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