

# Fundamentals Of Logic Design Problem Solutions

For sophomore courses on digital design in an Electrical Engineering, Computer Engineering, or Computer Science department. & Digital Design, fourth edition is a modern update of the classic authoritative text on digital design.& This book teaches the basic concepts of digital design in a clear, accessible manner. The book presents the basic tools for the design of digital circuits and provides procedures suitable for a variety of digital applications.

Updated with modern coverage, a streamlined presentation, and an excellent CD-ROM, this fifth edition achieves a balance between theory and application. Author Charles H. Roth, Jr. carefully presents the theory that is necessary for understanding the fundamental concepts of logic design while not overwhelming students with the mathematics of switching theory.

Divided into 20 easy-to-grasp study units, the book covers such fundamental concepts as Boolean algebra, logic gates design, flip-flops, and state machines. By combining flip-flops with networks of logic gates, students will learn to design counters, adders, sequence detectors, and simple digital systems. After covering the basics, this text presents modern design techniques using programmable logic devices and the VHDL hardware description language.

Updated with modern coverage, a streamlined presentation, and an excellent companion CD, this sixth edition achieves yet again an unmatched balance between theory and application. Authors Charles H. Roth, Jr. and Larry L. Kinney carefully present the theory that is necessary for understanding the fundamental concepts of logic design while not overwhelming students with the mathematics of switching theory. Divided into 20 easy-to-grasp study units, the book covers such fundamental concepts as Boolean algebra, logic gates design, flip-flops, and state

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machines. By combining flip-flops with networks of logic gates, students will learn to design counters, adders, sequence detectors, and simple digital systems. After covering the basics, this text presents modern design techniques using programmable logic devices and the VHDL hardware description language.

New, updated and expanded topics in the fourth edition include: EBCDIC, Grey code, practical applications of flip-flops, linear and shaft encoders, memory elements and FPGAs. The section on fault-finding has been expanded. A new chapter is dedicated to the interface between digital components and analog voltages. \*A highly accessible, comprehensive and fully up to date digital systems text \*A well known and respected text now revamped for current courses \*Part of the Newnes suite of texts for HND/1st year modules

Fundamentals of Digital Logic With VHDL Design teaches the basic design techniques for logic circuits. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples, which are easy to understand. Then, a modular approach is used to show how larger circuits are designed.

VHDL is used to demonstrate how the basic building blocks and larger systems are defined in a hardware description language, producing designs that can be implemented with modern CAD tools. The book emphasizes the concepts that should be covered in an introductory course on logic design, focusing on: Logic functions, gates, and rules of Boolean algebra  
Circuit synthesis and optimization techniques  
Number representation and arithmetic circuits  
Combinational-circuit building blocks, such as multiplexers, decoders, encoders, and code converters  
Sequential-circuit building blocks, such as flip-flops, registers, and counters  
Design of synchronous sequential circuits  
Use of the basic building blocks in designing larger systems

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It also includes chapters that deal with important, but more advanced topics: Design of asynchronous sequential circuits Testing of logic circuits For students who have had no exposure to basic electronics, but are interested in learning a few key concepts, there is a chapter that presents the most basic aspects of electronic implementation of digital circuits. Major changes in the second edition of the book include new examples to clarify the presentation of fundamental concepts over 50 new examples of solved problems provided at the end of chapters NAND and NOR gates now introduced in Chapter 2 more complete discussion of techniques for minimization of logic functions in Chapter 4 (including the tabular method) a new chapter explaining the CAD flow for synthesis of logic circuits Altera's Quartus II CAD software provided on a CD-ROM three appendices that give tutorials on the use of Quartus II software

Written for advanced study in digital systems design, Roth/John's DIGITAL SYSTEMS DESIGN USING VHDL, 3E integrates the use of the industry-standard hardware description language, VHDL, into the digital design process. The book begins with a valuable review of basic logic design concepts before introducing the fundamentals of VHDL. The book concludes with detailed coverage of advanced VHDL topics. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version. This book provides broad and comprehensive coverage of the entire EDA flow. EDA/VLSI practitioners and researchers in need of fluency in an "adjacent" field will find this an invaluable reference to the basic EDA concepts, principles, data structures, algorithms, and architectures for the design, verification, and test of VLSI circuits. Anyone who needs to learn the concepts, principles, data structures, algorithms, and architectures of the EDA flow will benefit from this

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book. Covers complete spectrum of the EDA flow, from ESL design modeling to logic/test synthesis, verification, physical design, and test - helps EDA newcomers to get "up-and-running" quickly Includes comprehensive coverage of EDA concepts, principles, data structures, algorithms, and architectures - helps all readers improve their VLSI design competence Contains latest advancements not yet available in other books, including Test compression, ESL design modeling, large-scale floorplanning, placement, routing, synthesis of clock and power/ground networks - helps readers to design/develop testable chips or products Includes industry best-practices wherever appropriate in most chapters - helps readers avoid costly mistakes

Digital Logic Design MCQs: Multiple Choice Questions and Answers (Quiz & Practice Tests with Answer Key) PDF, Digital Logic Design Worksheets & Quick Study Guide covers exam review worksheets to solve problems with 700 solved MCQs. "Digital Logic Design MCQ" PDF with answers covers concepts, theory and analytical assessment tests. "Digital Logic Design Quiz" PDF book helps to practice test questions from exam prep notes. Computer science study guide provides 700 verbal, quantitative, and analytical reasoning solved past question papers MCQs. Digital Logic Design Multiple Choice Questions and Answers PDF download, a book covers solved quiz questions and answers on chapters: Algorithmic state machine, asynchronous sequential logic, binary systems, Boolean algebra and logic gates, combinational logics, digital integrated circuits,

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DLD experiments, MSI and PLD components, registers counters and memory units, simplification of Boolean functions, standard graphic symbols, synchronous sequential logics worksheets for college and university revision guide. "Digital Logic Design Quiz Questions and Answers" PDF download with free sample test covers beginner's questions and mock tests with exam workbook answer key. Digital logic design MCQs book, a quick study guide from textbooks and lecture notes provides exam practice tests. "Digital Logic Design Worksheets" PDF book with answers covers problem solving in self-assessment workbook from computer science textbooks with past papers worksheets as: Worksheet 1: Algorithmic State Machine MCQs Worksheet 2: Asynchronous Sequential Logic MCQs Worksheet 3: Binary Systems MCQs Worksheet 4: Boolean Algebra and Logic Gates MCQs Worksheet 5: Combinational Logics MCQs Worksheet 6: Digital Integrated Circuits MCQs Worksheet 7: DLD Experiments MCQs Worksheet 8: MSI and PLD Components MCQs Worksheet 9: Registers Counters and Memory Units MCQs Worksheet 10: Simplification of Boolean Functions MCQs Worksheet 11: Standard Graphic Symbols MCQs Worksheet 12: Synchronous Sequential Logics MCQs Practice Algorithmic State Machine MCQ PDF with answers to solve MCQ test questions: Introduction to algorithmic state machine, algorithmic state machine chart, ASM chart, control

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implementation in ASM, design with multiplexers, state machine diagrams, and timing in state machines. Practice Asynchronous Sequential Logic MCQ PDF with answers to solve MCQ test questions: Introduction to asynchronous sequential logic, analysis of asynchronous sequential logic, circuits with latches, design procedure of asynchronous sequential logic, and transition table. Practice Binary Systems MCQ PDF with answers to solve MCQ test questions: Binary systems problems, complements in binary systems, character alphanumeric codes, arithmetic addition, binary codes, binary numbers, binary storage and registers, code, decimal codes, definition of binary logic, digital computer and digital system, error detection code, gray code, logic gates, number base conversion, octal and hexadecimal numbers, radix complement, register transfer, signed binary number, subtraction with complement, switching circuits, and binary signals. Practice Boolean Algebra and Logic Gates MCQ PDF with answers to solve MCQ test questions: Basic definition of Boolean algebra, digital logic gates, axiomatic definition of Boolean algebra, basic algebraic manipulation, theorems and properties of Boolean algebra, Boolean functions, complement of a function, canonical and standard forms, conversion between canonical forms, standard forms, integrated circuits, logical operations, operator precedence, product of maxterms, sum of minterms, and Venn diagrams. Practice

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required in the design of digital systems. Features updates and revision to more than half of the material from the previous edition Offers an all-encompassing focus on the areas of computer design, digital logic, and digital systems, unlike other texts in the marketplace Written with clear and concise explanations of fundamental topics such as number system and Boolean algebra, and simplified examples and tutorials utilizing the PIC18F4321 microcontroller Covers an enhanced version of both combinational and sequential logic design, basics of computer organization, and microcontrollers

Comprehensive and self contained, this tutorial covers the design of a plethora of combinational and sequential logic circuits using conventional logic design and Verilog HDL. Number systems and number representations are presented along with various binary codes. Several advanced topics are covered, including functional decomposition and iterative networks. A variety of examples are provided for combinational and sequential logic, computer arithmetic, and advanced topics such as Hamming code error correction. Constructs supported by Verilog are described in detail. All designs are continued to completion. Each chapter includes numerous design issues of varying complexity to be resolved by the reader.

This is an up-to-date treatment of the analysis and design of CMOS integrated

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digital logic circuits. The self-contained book covers all of the important digital circuit design styles found in modern CMOS chips, emphasizing solving design problems using the various logic styles available in CMOS.

Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples, which are easy to understand. Then, a modular approach is used to show how larger circuits are designed. The book emphasizes CAD through the use of Altera's Quartus II CAD software, a state-of-the-art digital circuit design package. This software produces automatic mapping of designs written in VHDL into Field Programmable Gate Arrays).

Updated with modern coverage, a streamlined presentation, and excellent companion software, this seventh edition of FUNDAMENTALS OF LOGIC DESIGN achieves yet again an unmatched balance between theory and application. Authors Charles H. Roth, Jr. and Larry L. Kinney carefully present the theory that is necessary for understanding the fundamental concepts of logic design while not overwhelming students with the mathematics of switching theory. Divided into 20 easy-to-grasp study units, the book covers such fundamental concepts as Boolean algebra, logic gates design, flip-flops, and

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state machines. By combining flip-flops with networks of logic gates, students will learn to design counters, adders, sequence detectors, and simple digital systems. After covering the basics, this text presents modern design techniques using programmable logic devices and the VHDL hardware description language. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

This practical introduction explains exactly how digital circuits are designed, from the basic circuit to the advanced system. It covers combinational logic circuits, which collect logic signals, to sequential logic circuits, which embody time and memory to progress through sequences of states. The primer also highlights digital arithmetic and the integrated circuits that implement the logic functions. Based on the author's extensive experience in teaching digital electronics to undergraduates, the book translates theory directly into practice and presents the essential information in a compact, digestible style. Worked problems and examples are accompanied by abbreviated solutions, with demonstrations to ensure that the design material and the circuits' operation are fully understood. This is essential reading for any electronic or electrical engineering student new to digital electronics and requiring a succinct yet comprehensive introduction.

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Find exactly what you need to introduce your students to the fundamentals of programming logic with Farrell's direct, efficient JUST ENOUGH PROGRAMMING LOGIC AND DESIGN, 2E. This unique, language-independent approach to logic provides seven chapters focused on key programming and logic content in a concise format that helps readers progress through the subject matter quickly. Students study introductory concepts, structure, decision-making, looping, array manipulation, and calling methods as well as an introduction to object-oriented programming. Everyday examples and clear explanations in this edition's streamlined presentation make this a perfect choice for students with no prior programming experience. Twenty-five brief new videos from the author expand upon and clarify topics, while new Debugging Exercises and a wealth of review and programming exercises in each chapter help students hone their coding and programming skills. Use this concise approach alone or as a companion text in any programming language course. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

Digital Design and Computer Architecture: ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor. By the end of

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this book, readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing an ARM processor.

SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Features side-by-side examples of the two most prominent Hardware Description Languages (HDLs)—SystemVerilog and VHDL—which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. The Companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. The Companion website also includes appendices covering practical digital design issues

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and C programming as well as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises.

This book, *Electronic Devices and Circuit Application*, is the first of four books of a larger work, *Fundamentals of Electronics*. It is comprised of four chapters describing the basic operation of each of the four fundamental building blocks of modern electronics: operational amplifiers, semiconductor diodes, bipolar junction transistors, and field effect transistors. Attention is focused on the reader obtaining a clear understanding of each of the devices when it is operated in equilibrium. Ideas fundamental to the study of electronic circuits are also developed in the book at a basic level to lessen the possibility of misunderstandings at a higher level. The difference between linear and non-linear operation is explored through the use of a variety of circuit examples including amplifiers constructed with operational amplifiers as the fundamental component and elementary digital logic gates constructed with various transistor types. *Fundamentals of Electronics* has been designed primarily for use in an upper division course in electronics for electrical engineering students. Typically such a course spans a full academic year consisting of two semesters or three quarters. As such, *Electronic Devices and Circuit Applications*, and the following two books, *Amplifiers: Analysis and Design* and *Active Filters and Amplifier Frequency Response*, form an appropriate body of material for such a course. Secondary applications include the use in a one-semester electronics course for engineers or as a reference for

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practicing engineers.

With an abundance of insightful examples, problems, and computer experiments, Introduction to Logic Design provides a balanced, easy-to-read treatment of the fundamental theory of logic functions and applications to the design of digital devices and systems. Requiring no prior knowledge of electrical circuits or electronics, it supplies the

Fundamentals of Digital Logic and Microcomputer Design, has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers. In this Fifth Edition, the author focuses on computer design at three levels: the device level, the logic level, and the system level. Basic topics are covered, such as number systems and Boolean algebra, combinational and sequential logic design, as well as more advanced subjects such as assembly language programming and microprocessor-based system design. Numerous examples are provided throughout the text. Coverage includes: Digital circuits at the gate and flip-flop levels Analysis and design of combinational and sequential circuits Microcomputer organization, architecture, and programming concepts Design of computer instruction sets, CPU, memory, and I/O System design features associated with popular microprocessors from Intel and Motorola Future plans in microprocessor development An instructor's manual, available upon request Additionally, the accompanying CD-ROM, contains step-by-step procedures for installing and using

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Altera Quartus II software, MASM 6.11 (8086), and 68asmsim (68000), provides valuable simulation results via screen shots. Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems.

Unlike books currently on the market, this book attempts to satisfy two goals: combine circuits and electronics into a single, unified treatment, and establish a strong connection with the contemporary world of digital systems. It will introduce a new way of looking not only at the treatment of circuits, but also at the treatment of introductory coursework in engineering in general. Using the concept of "abstraction," the book attempts to form a bridge between the world of physics and the world of large computer systems. In particular, it attempts to unify electrical engineering and computer science as the art of creating and exploiting successive abstractions to manage the complexity of building useful electrical systems. Computer systems are simply one type of electrical systems. +Balances circuits theory with practical digital electronics applications. +Illustrates concepts with real devices. +Supports the popular circuits and electronics course on the MIT OpenCourse Ware from which professionals worldwide study this new approach. +Written by two educators well known for their innovative teaching and research and their collaboration with industry. +Focuses on contemporary MOS technology.

The Fourth edition of this well-received text continues to provide coherent and

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comprehensive coverage of digital circuits. It is designed for the undergraduate students pursuing courses in areas of engineering disciplines such as Electrical and Electronics, Electronics and Communication, Electronics and Instrumentation, Telecommunications, Medical Electronics, Computer Science and Engineering, Electronics, and Computers and Information Technology. It is also useful as a text for MCA, M.Sc. (Electronics) and M.Sc. (Computer Science) students. Appropriate for self study, the book is useful even for AMIE and grad IETE students. Written in a student-friendly style, the book provides an excellent introduction to digital concepts and basic design techniques of digital circuits. It discusses Boolean algebra concepts and their application to digital circuitry, and elaborates on both combinational and sequential circuits. It provides numerous fully worked-out, laboratory tested examples to give students a solid grounding in the related design concepts. It includes a number of short questions with answers, review questions, fill in the blanks with answers, multiple choice questions with answers and exercise problems at the end of each chapter. For courses on digital design in an Electrical Engineering, Computer Engineering, or Computer Science department. Digital Design, fifth edition is a modern update of the classic authoritative text on digital design. This book teaches the basic concepts of digital design in a clear, accessible manner. The book presents the basic tools for the design of digital circuits and provides procedures suitable for a variety of digital applications. This book focuses on the basic principles of digital electronics and logic design. It is designed as a textbook for undergraduate students of electronics, electrical engineering, computer

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science, physics, and information technology. The text covers the syllabi of several Indian and foreign universities. It depicts the comprehensive resources on the recent ideas in the area of digital electronics explored by leading experts from both industry and academia. A good number of diagrams are provided to illustrate the concepts related to digital electronics so that students can easily comprehend the subject. Solved examples within the text explain the concepts discussed and exercises are provided at the end of each chapter.

This textbook, based on the author's fifteen years of teaching, is a complete teaching tool for turning students into logic designers in one semester. Each chapter describes new concepts, giving extensive applications and examples. Assuming no prior knowledge of discrete mathematics, the authors introduce all background in propositional logic, asymptotics, graphs, hardware and electronics. Important features of the presentation are:

- All material is presented in full detail. Every designed circuit is formally specified and implemented, the correctness of the implementation is proved, and the cost and delay are analyzed
- Algorithmic solutions are offered for logical simulation, computation of propagation delay and minimum clock period
- Connections are drawn from the physical analog world to the digital abstraction
- The language of graphs is used to describe formulas and circuits
- Hundreds of figures, examples and exercises enhance understanding.

The extensive website (<http://www.eng.tau.ac.il/~guy/Even-Medina/>) includes teaching slides, links to Logisim and a DLX assembly simulator.

Fundamentals of Digital Logic With Verilog Design teaches the basic design techniques for logic circuits. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples. Use of CAD

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software is well integrated into the book. A CD-ROM that contains Altera's Quartus CAD software comes free with every copy of the text. The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays (FPGAs) and Complex Programmable Logic Devices (CPLDs). Students will be able to try, firsthand, the book's Verilog examples (over 140) and homework problems. Engineers use Quartus CAD for designing, simulating, testing and implementing logic circuits. The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language. Students will be able to: enter a design into the CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices (using the school's laboratory facilities) Verilog is a complex language, so it is introduced gradually in the book. Each Verilog feature is presented as it becomes pertinent for the circuits being discussed. To teach the student to use the Quartus CAD, the book includes three tutorials.

This complete introduction to computer engineering includes the use of the microprocessor as a building block for digital logic design. The authors offer a top-down approach to designing digital systems, with consideration of both hardware and software. They emphasize structured design throughout, and the design methods, techniques, and notations are consistent with this theme. The first part of the book lays the foundation for structured design techniques; the second part provides the fundamentals of microprocessor and up-based design. Topics covered include mixed logic notation, the algorithm state machine, and structured programming techniques with well-documented programs. Contains an abundance of examples and end-of-chapter problems.

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Master the principles of logic design with the exceptional balance of theory and application found in Roth/Kinney/John's FUNDAMENTALS OF LOGIC DESIGN, ENHANCED, 7th Edition. This edition introduces you to today's latest advances. The authors have carefully developed a clear presentation that introduces the fundamental concepts of logic design without overwhelming you with the mathematics of switching theory. Twenty engaging, easy-to-follow study units present basic concepts, such as Boolean algebra, logic gate design, flip-flops and state machines. You learn to design counters, adders, sequence detectors and simple digital systems. After mastering the basics, you progress to modern design techniques using programmable logic devices as well as VHDL hardware description language. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

A COMPREHENSIVE GUIDE TO THE DESIGN & ORGANIZATION OF MODERN COMPUTING SYSTEMS Digital Logic Design and Computer Organization with Computer Architecture for Security provides practicing engineers and students with a clear understanding of computer hardware technologies. The fundamentals of digital logic design as well as the use of the Verilog hardware description language are discussed. The book covers computer organization and architecture, modern design concepts, and computer security through hardware. Techniques for designing both small and large combinational and sequential circuits are thoroughly explained. This detailed reference addresses memory technologies, CPU design and techniques to increase performance, microcomputer architecture, including "plug and play" device interface, and memory hierarchy. A chapter on security engineering methodology as it applies to computer architecture concludes the book. Sample problems,

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design examples, and detailed diagrams are provided throughout this practical resource. **COVERAGE INCLUDES:** Combinational circuits: small designs Combinational circuits: large designs Sequential circuits: core modules Sequential circuits: small designs Sequential circuits: large designs Memory Instruction set architecture Computer architecture: interconnection Memory system Computer architecture: security

FPGAs have almost entirely replaced the traditional Application Specific Standard Parts (ASSP) such as the 74xx logic chip families because of their superior size, versatility, and speed. For example, FPGAs provide over a million fold increase in gates compared to ASSP parts. The traditional approach for hands-on exercises has relied on ASSP parts, primarily because of their simplicity and ease of use for the novice. Not only is this approach technically outdated, but it also severely limits the complexity of the designs that can be implemented. By introducing the readers to FPGAs, they are being familiarized with current digital technology and the skills to implement complex, sophisticated designs. However, working with FPGAs comes at a cost of increased complexity, notably the mastering of an HDL language, such as Verilog. Therefore, this book accomplishes the following: first, it teaches basic digital design concepts and then applies them through exercises; second, it implements these digital designs by teaching the user the syntax of the Verilog language while implementing the exercises. Finally, it employs contemporary digital hardware, such as the FPGA, to build a simple calculator, a basic music player, a frequency and period counter and it ends with a microprocessor being embedded in the fabric of the FGPA to communicate with the PC. In the process, readers learn about digital mathematics and digital-to-analog converter concepts through pulse width modulation.

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Fundamentals of Logic Design Thomson Learning

Fundamentals of Switching Theory and Logic Design discusses the basics of switching theory and logic design from a slightly alternative point of view and also presents links between switching theory and related areas of signal processing and system theory. Switching theory is a branch of applied mathematic providing mathematical foundations for logic design, which can be considered as a part of digital system design concerning realizations of systems whose inputs and outputs are described by logic functions.

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