

Emc Control With Pcb Design For Working Engineers

A practical introduction to techniques for the design of electronic products from the Electromagnetic compatibility (EMC) perspective Introduces techniques for the design of electronic products from the EMC aspects Covers normalized EMC requirements and design principles to assure product compatibility Describes the main topics for the control of electromagnetic interferences and recommends design improvements to meet international standards requirements (FCC, EU EMC directive, Radio acts, etc.) Well organized in a logical sequence which starts from basic knowledge and continues through the various aspects required for compliance with EMC requirements Includes practical examples and case studies to illustrate design features and troubleshooting Author is the founder of the EMC design risk evaluation approach and this book presents many years' experience in teaching and researching the topic

The Keep It Simple (KISS) philosophy is the primary focus of this book. It is written in very simple language with minimal math, as a compilation of helpful EMI troubleshooting hints. Its light-hearted tone is at odds with the extreme seriousness of most engineering reference works that become boring after a few pages. This text tells engineers what to do and how to do it. Only a basic knowledge of math, electronics, and a basic understanding of EMI/EMC are necessary to understand the concepts and circuits described. Once EMC troubleshooting is demystified, readers learn there are quick and simple techniques to solve complicated problems a key aspect of this book. Simple and inexpensive methods to resolve EMI issues are discussed to help generate unique ideas and methods for developing additional diagnostic tools and measurement procedures. An appendix on how to build probes is included. It can be a fun activity, even humorous at times with bizarre techniques (i.e., the sticky finger probe).

PCB Design for Real-World EMI Control Springer Science & Business Media
Tim Williams has worked for a variety of companies as an electronic design engineer over the last 20 years. He has monitored the progress of the EMC Directive and its associated standards since it was first made public. He is a member of the Institution of Electrical Engineers and now runs his own consultancy, specialising in EMC design and training. *Save money on consultancy bills with this book *Practical guide to implementing EMC within the product design process *The leading professional guide to the EMC Directive -100% up-to-date and reliable

With growing consumer demand for portability and miniaturization in electronics, design engineers must concentrate on many additional aspects in their core design. The plethora of components that must be considered requires that engineers have a concise understanding of each aspect of the design process in

order to prevent bug-laden prototypes. Electronic Circuit Design allows engineers to understand the total design process and develop prototypes which require little to no debugging before release. It provides step-by-step instruction featuring modern components, such as analog and mixed signal blocks, in each chapter. The book details every aspect of the design process from conceptualization and specification to final implementation and release. The text also demonstrates how to utilize device data sheet information and associated application notes to design an electronic system. The hybrid nature of electronic system design poses a great challenge to engineers. This book equips electronics designers with the practical knowledge and tools needed to develop problem free prototypes that are ready for release.

This accessible, new reference work shows how and why RF energy is created within a printed circuit board and the manner in which propagation occurs. With lucid explanations, this book enables engineers to grasp both the fundamentals of EMC theory and signal integrity and the mitigation process needed to prevent an EMC event. Author Montrose also shows the relationship between time and frequency domains to help you meet mandatory compliance requirements placed on printed circuit boards. Using real-world examples the book features:

- Clear discussions, without complex mathematical analysis, of flux minimization concepts
- Extensive analysis of capacitor usage for various applications
- Detailed examination of components characteristics with various grounding methodologies, including implementation techniques
- An in-depth study of transmission line theory
- A careful look at signal integrity, crosstalk, and termination

The Circuit Designer's Companion covers the theoretical aspects and practices in analogue and digital circuit design. Electronic circuit design involves designing a circuit that will fulfill its specified function and designing the same circuit so that every production model of it will fulfill its specified function, and no other undesired and unspecified function. This book is composed of nine chapters and starts with a review of the concept of grounding, wiring, and printed circuits. The subsequent chapters deal with the passive and active components of circuitry design. These topics are followed by discussions of the principles of other design components, including linear integrated circuits, digital circuits, and power supplies. The remaining chapters consider the vital role of electromagnetic compatibility in circuit design. These chapters also look into safety, design of production, testability, reliability, and thermal management of the designed circuit. This book is of great value to electrical and design engineers.

In all possible industrial, military and household/personal applications, the number of digital devices operating with data rates of hundreds of Megabits, using processor chips with Gigahertz clocks, has increased astronomically. At the same time, a myriad of popular RF receivers like portable telephones, laptop PCs with integrated wireless modems, wireless Internet, and other electronic devices, are becoming ubiquitous, such that the number of sensitive, licit receivers operating within a square kilometer of an urban area can be counted in tens of thousands. In the crowded space that they share,

the conjunction of both events is increasing the number of potential interference situations, especially in the upper VHF and UHF regions where spurious radiations are most difficult to contain. There is, in addition, a growing, although controversial, concern about the possible health hazard caused by long exposure to near fields of low power radio transmitters. All these aspects result in a continuous effort for lowering RF radiations. This new edition of *Controlling Radiated Emissions by Design* retains the step-by-step approach for incorporating EMC into every new design, from the ground up. Quite different from other classical EMC books, it approaches the problem from a development engineer's viewpoint, starting with the selection of quieter IC technologies, their implementation into a noise-free printed circuit layout, and the gathering of all these into a low radiation packaging, including I/O filtering, connectors and cables considerations. Equally far from a cookbook of recipes, all guidelines are supported by thorough, but relatively easy and comprehensive calculated examples, allowing a quantitative design, instead of purely qualitative. New to this edition is material on surface mount techniques, IC's ground-bounce, random-versus-periodic frequency spectra and recent progress in low cost ferrite and filter components. Also included is detailed information on radiation from high-speed chips (e.g. Pentium >200 MHz) and the efforts by some manufacturers to reduce it. The book has numerous tables, all of which have been updated to reflect the latest changes in the field, including a brief overview of the U.S. and worldwide emission tests. *Controlling Radiated Emissions by Design* is an invaluable tool for helping design engineers, EMC specialists and technicians develop more efficient and economical control of emissions.

This updated and expanded version of the very successful first edition offers new chapters on controlling the emission from electronic systems, especially digital systems, and on low-cost techniques for providing electromagnetic compatibility (EMC) for consumer products sold in a competitive market. There is also a new chapter on the susceptibility of electronic systems to electrostatic discharge. There is more material on FCC regulations, digital circuit noise and layout, and digital circuit radiation. Virtually all the material in the first edition has been retained. Contains a new appendix on FCC EMC test procedures.

Focused on the field of knowledge lying between digital and analog circuit theory, this new text will help engineers working with digital systems shorten their product development cycles and help fix their latest design problems. The scope of the material covered includes signal reflection, crosstalk, and noise problems which occur in high speed digital machines (above 10 megahertz). This volume will be of practical use to digital logic designers, staff and senior communications scientists, and all those interested in digital design.

The 3rd edition of *Controlling Radiated Emissions by Design* has been updated to reflect the latest changes in the field. New to this edition is material on aspects of technical advance, specifically long term energy efficiency, energy saving, RF pollution control, etc. This book retains the step-by-step approach for incorporating EMC into every new design, from the ground up. It describes the selection of quieter IC technologies, their implementation into a noise-free printed circuit layout, and the gathering of all these into low radiation packaging, including I/O filtering, connectors and cables considerations. All guidelines are supported by thorough and comprehensive calculated examples. Design engineers, EMC specialists and

technicians will benefit from learning about the development of more efficient and economical control of emissions.

Proper design of printed circuit boards can make the difference between a product passing emissions requirements during the first cycle or not. Traditional EMC design practices have been simply rule-based, that is, a list of rules-of-thumb are presented to the board designers to implement. When a particular rule-of-thumb is difficult to implement, it is often ignored. After the product is built, it will often fail emission requirements and various time consuming and costly add-ons are then required. Proper EMC design does not require advanced degrees from universities, nor does it require strenuous mathematics. It does require a basic understanding of the underlying principles of the potential causes of EMC emissions. With this basic understanding, circuit board designers can make trade-off decisions during the design phase to ensure optimum EMC design. Consideration of these potential sources will allow the design to pass the emissions requirements the first time in the test laboratory. A number of other books have been published on EMC. Most are general books on EMC and do not focus on printed circuit board design. This book is intended to help EMC engineers and design design. This book engineers understand the potential sources of emissions and how to reduce, control, or eliminate these sources. This book is intended to be a 'hands-on' book, that is, designers should be able to apply the concepts in this book directly to their designs in the real-world.

This book emphasizes understanding basic concepts of controlling the currents on printed circuit boards (pcb's), and provides a wealth of insightful information detailing the possible sources of emissions. Numerous design strategies are presented to help readers understand how to produce, control and eliminate emission sources. Additional highlights include the following: -Information explaining how to design pcb's to pass EMC Requirements the first time! -Controlling intentional and unintentional currents at their source; -Decoupling strategies explained and myths exposed; -Proper I/O filter design and connection strategy explained; -Not simply a list of do's and don't's ... but an explanation of "why" things work as they do; -"Ground is a place where potatoes and carrots grow!"--Basic shielding design considerations for PCBs included, and more. This 'hands-on' book will help designers understand "why" or "why not" to implement a specific design practice.

This book provides the knowledge and good design practice for the design or test engineer to take the necessary measures to improve EMC performance and therefore the chance of achieving compliance, early on in the design process. There are many advantages for both the component supplier and consumer, of looking at EMC at component and PCB level. For the suppliers, not only will their products have the competitive edge because they have known EMC performance, but they will be prepared should EMC compliance become mandatory in the future. For consumers it is a distinct advantage to know how a component will behave within a system with regard to EMC. Shows how to achieve EMC compliance early on in the design process Provides the knowledge to trace system EMC performance problems Follows best design practices

Praise for Noise Reduction Techniques IN electronic systems "Henry Ott has literally 'written the book' on the subject of EMC. . . . He not only knows the subject, but has the rare ability to communicate that knowledge to others." —EE Times Electromagnetic Compatibility Engineering

is a completely revised, expanded, and updated version of Henry Ott's popular book *Noise Reduction Techniques in Electronic Systems*. It reflects the most recent developments in the field of electromagnetic compatibility (EMC) and noise reduction, and their practical applications to the design of analog and digital circuits in computer, home entertainment, medical, telecom, industrial process control, and automotive equipment, as well as military and aerospace systems. While maintaining and updating the core information—such as cabling, grounding, filtering, shielding, digital circuit grounding and layout, and ESD—that made the previous book such a wide success, this new book includes additional coverage of:

Equipment/systems grounding
Switching power supplies and variable-speed motor drives
Digital circuit power distribution and decoupling
PCB layout and stack-up
Mixed-signal PCB layout
RF and transient immunity
Power line disturbances
Precompliance EMC measurements
New appendices on dipole antennae, the theory of partial inductance, and the ten most common EMC problems
The concepts presented are applicable to analog and digital circuits operating from below audio frequencies to those in the GHz range. Throughout the book, an emphasis is placed on cost-effective EMC designs, with the amount and complexity of mathematics kept to the strictest minimum. Complemented with over 250 problems with answers, *Electromagnetic Compatibility Engineering* equips readers with the knowledge needed to design electronic equipment that is compatible with the electromagnetic environment and compliant with national and international EMC regulations. It is an essential resource for practicing engineers who face EMC and regulatory compliance issues and an ideal textbook for EE courses at the advanced undergraduate and graduate levels.

The application of computational electromagnetics to practical EMI/EMC engineering is an emerging technology. Because of the increased complexity in EMI/EMC issues resulting from advancements in electronics and telecommunications, it is no longer possible to rely exclusively on traditional techniques and tools to solve the growing list of electronic engineering design problems. *EMI/EMC Computational Modeling Handbook* introduces modeling and simulation of electromagnetics to real-world EMI/EMC engineering. It combines the essentials of electromagnetics, computational techniques, and actual EMI/EMC applications. Included are such popular full-wave computational modeling techniques as the Method of Moments, Finite-Difference Time Domain Technique, Finite Element Method, and several others. The authors have included a myriad of applications for computers, telecommunications, consumer electronics, medical electronics, and military uses. *EMI/EMC Computational Modeling Handbook* is an invaluable reference work for practicing EMI/EMC engineers, electronic design engineers, and any engineer involved in computational electromagnetics.

The book reviews developments in the following fields: electromagnetic compatibility; EMC standards; EMC testing; radiated emission testing; antennas; radiated susceptibility testing; measurement equipment; electromagnetic transient testing; and uncertainty analysis

"Electromagnetic compatibility (EMC) is an engineering discipline often identified as "black magic." This belief exists because the fundamental mechanisms on how radio frequency (RF) energy is developed within a printed circuit board (PCB) is not well understood by practicing engineers. Rigorous mathematical analysis is not required to design a PCB. Using basic EMC theory and converting complex concepts into simple analogies helps engineers understand the mitigation process that deters EMC events from occurring. This user-friendly reference covers a broad spectrum of information never before published, and is as fluid and comprehensive as the first edition. The simplified approach to PCB design and layout is based on real-life experience, training, and knowledge. *Printed Circuit Board Techniques for EMC Compliance, Second Edition* will help prevent the emission or reception of unwanted RF energy generated by components and interconnects, thus achieving acceptable levels of EMC for electrical equipment. It prepares one for complying with stringent domestic and international regulatory

requirements. Also, it teaches how to solve complex problems with a minimal amount of theory and math. Essential topics discussed include: * Introduction to EMC * Interconnects and I/O * PCB basics * Electrostatic discharge protection * Bypassing and decoupling * Backplanes-Ribbon Cables-Daughter Cards * Clock Circuits-Trace Routing-Terminations * Miscellaneous design techniques This rules-driven book-formatted for quick access and cross-reference-is ideal for electrical and EMC engineers, consultants, technicians, and PCB designers regardless of experience or educational background." Sponsored by: IEEE Electromagnetic Compatibility Society

The packaging of electronic devices and systems represents a significant challenge for product designers and managers. Performance, efficiency, cost considerations, dealing with the newer IC packaging technologies, and EMI/RFI issues all come into play. Thermal considerations at both the device and the systems level are also necessary. The Electronic Packaging Handbook, a new volume in the Electrical Engineering Handbook Series, provides essential factual information on the design, manufacturing, and testing of electronic devices and systems. Co-published with the IEEE, this is an ideal resource for engineers and technicians involved in any aspect of design, production, testing or packaging of electronic products, regardless of whether they are commercial or industrial in nature. Topics addressed include design automation, new IC packaging technologies, materials, testing, and safety. Electronics packaging continues to include expanding and evolving topics and technologies, as the demand for smaller, faster, and lighter products continues without signs of abatement. These demands mean that individuals in each of the specialty areas involved in electronics packaging—such as electronic, mechanical, and thermal designers, and manufacturing and test engineers—are all interdependent on each others knowledge. The Electronic Packaging Handbook elucidates these specialty areas and helps individuals broaden their knowledge base in this ever-growing field.

Electromagnetic Compatibility of Integrated Circuits: Techniques for Low Emission and Susceptibility focuses on the electromagnetic compatibility of integrated circuits. The basic concepts, theory, and an extensive historical review of integrated circuit emission and susceptibility are provided. Standardized measurement methods are detailed through various case studies. EMC models for the core, I/Os, supply network, and packaging are described with applications to conducted switching noise, signal integrity, near-field and radiated noise. Case studies from different companies and research laboratories are presented with in-depth descriptions of the ICs, test set-ups, and comparisons between measurements and simulations. Specific guidelines for achieving low emission and susceptibility derived from the experience of EMC experts are presented.

Presents simple techniques for designing and laying out circuits that meet the most stringent domestic and international regulations on electromagnetic compatibility for high technology products. Includes sample designs in every stage of the product development cycle, information on the latest suppression techniques, and a checklist of layout techniques.

Annotation copyrighted by Book News, Inc., Portland, OR

Offering simple methods of measuring AC and DC power lines, this highly popular, revised and expanded reference describes the selection of cores, capacitors, mechanical shapes, and styles for the timeliest design, construction, and testing of filters. It presents analyses of matrices of various filter types based on close approximations, observation, and trial and error. Supplying simple parameters and techniques for creating manufacturable, repeatable products, the second edition provides insights into the cause and elimination of common mode noise in lines and equipment, explores new data on spike, pulse, trapezoid, and

quasisquare waves, and reviews the latest high-current filters.

EMC for Product Designers: Meeting the European EMC Directives is a six-chapter text that considers the by-product of the co-existence of all kinds of radio services, called electromagnetic compatibility (EMC). This book discusses the solution to the damaging frequency interference of EMC and the problem of EMC to electronic equipment. The opening chapter considers the effect of adapting the EMC Directives to decrease the economic damage being caused by electromagnetic interference, as well as the analysis, definition, and compliance of EMC and EMC Directives. The next chapters deal with the measurement of EMC; RF emission testing; features of circuits, layout, and grounding; digital and analogue circuit design; and description of interfaces, filtering, and shielding. These topics are followed by discussion of the equipment for mains harmonic emission, the facilities and equipment for measuring RF susceptibility, and the transient susceptibility to ESD. The concluding chapters examine the use of performance criteria in measuring EMC. These chapters describe the features and application of the Fourier spectrum. The book can provide useful information to economists, engineers, radio technicians, students, and researchers.

This hands-on trouble-shooting style book offers step-by-step 'recipes' to assist those who are trying to solve EMI problems, by detailing exactly what to do and how to do it.

Co-published with the IEEE Press, this book is a practical, hands-on guide to EMC issues for medical device designers and installers. It addresses electromagnetic interference and covers the basics of EMC design, physics, and installation, minimizing theory and concentrating upon the correct way to ground and shield. Covering EMC from the inside out, the book provides the basics of electronics, discusses and evaluates problems and common causes, and explores effective remedial techniques at three levels: circuit, box, and interconnect. It contains appendices that provide important reference material such as constants and conversion factors.

FCCS2012 is an integrated conference concentrating its focus on Future Computer and Control Systems. "Advances in Future Computer and Control Systems" presents the proceedings of the 2012 International Conference on Future Computer and Control Systems(FCCS2012) held April 21-22,2012, in Changsha, China including recent research results on Future Computer and Control Systems of researchers from all around the world.

Proceedings of the FISITA 2012 World Automotive Congress are selected from nearly 2,000 papers submitted to the 34th FISITA World Automotive Congress, which is held by Society of Automotive Engineers of China (SAE-China) and the International Federation of Automotive Engineering Societies (FISITA). This proceedings focus on solutions for sustainable mobility in all areas of passenger car, truck and bus transportation. Volume 6: Vehicle Electronics focuses on:

- Engine/Chassis/Body Electronic Control
- Electrical and Electronic System
- Software and Hardware Development
- Electromagnetic Compatibility (EMC)

•Vehicle Sensor and Actuator •In-Vehicle Network •Multi-Media/Infotainment System Above all researchers, professional engineers and graduates in fields of automotive engineering, mechanical engineering and electronic engineering will benefit from this book. SAE-China is a national academic organization composed of enterprises and professionals who focus on research, design and education in the fields of automotive and related industries. FISITA is the umbrella organization for the national automotive societies in 37 countries around the world. It was founded in Paris in 1948 with the purpose of bringing engineers from around the world together in a spirit of cooperation to share ideas and advance the technological development of the automobile.

When designing an electronic circuit it is necessary to take a number of precautions to ensure that its EMC performance requirements can be met. Trying to fix the EMC performance once the circuit has been designed and built will be far more difficult and costly. There are a number of areas that can be addressed during the circuit design and PCB layout stage to ensure that the EMC performance is optimized: -PCB Circuit design -PCB Circuit partitioning-PCB Grounding-PCB Routing-EMC Filters-I/O Filtering and ShieldingBy adopting these precautions, the EMC performance of PCB layout can be greatly enhanced With electromagnetic compliance (EMC) now a major factor in the design of all electronic products, it is crucial to understand how electromagnetic interference (EMI) shielding products are used in various industries. Focusing on the practicalities of this area, *Advanced Materials and Design for Electromagnetic Interference Shielding* comprehensively introduces the design guidelines, materials selection, characterization methodology, manufacturing technology, and future potential of EMI shielding. After an overview of EMI shielding theory and product design guidelines, the book extensively reviews the characterization methodology of EMI materials. Subsequent chapters focus on particular EMI shielding materials and component designs, including enclosures, metal-formed gaskets, conductive elastomer and flexible graphite components, conductive foam and ventilation structures, board-level shielding materials, composite materials and hybrid structures, absorber materials, grounding and cable-level shielding materials, and aerospace and nuclear shielding materials. The last chapter presents a perspective on future trends in EMI shielding materials and design. Offering detailed coverage on many important topics, this indispensable book illustrates the efficiency and reliability of a range of materials and design solutions for EMI shielding.

"Mark I. Montrose, the best-selling author of *PRINTED CIRCUIT BOARD DESIGN TECHNIQUES FOR EMC COMPLIANCE*, now brings you his newest book, *EMC AND THE PRINTED CIRCUIT BOARD*. This accessible, new reference work shows how and why RF energy is created within a printed circuit board and the manner in which propagation occurs. With lucid explanations, this book enables engineers to grasp both the fundamentals of EMC theory and signal integrity and the mitigation process needed to prevent an EMC event.

Author Montrose also shows the relationship between time and frequency domains to help you meet mandatory compliance requirements placed on printed circuit boards. Using real-world examples the book features: * Clear discussions, without complex mathematical analysis, of flux minimization concepts * Extensive analysis of capacitor usage for various applications * Detailed examination of components characteristics with various grounding methodologies, including implementation techniques * An in-depth study of transmission line theory * A careful look at signal integrity, crosstalk, and termination" Sponsored by: IEEE Electromagnetic Compatibility Society.

Modeling and Design of Electromagnetic Compatibility for High-Speed Printed Circuit Boards and Packaging presents the electromagnetic modelling and design of three major electromagnetic compatibility (EMC) issues related to the high-speed printed circuit board (PCB) and electronic packages: signal integrity (SI), power integrity (PI), and electromagnetic interference (EMI). The emphasis is put on two essential passive components of PCBs and packages: the power distribution network and the signal distribution network. This book includes two parts. Part one talks about the field-circuit hybrid methods used for the EMC modeling, including the modal method, the integral equation method, the cylindrical wave expansion method and the de-embedding method. Part two illustrates EMC design methods and explores the applications of novel metamaterials and two-dimensional materials on traditional EMC problems. This book is designed to enhance worthwhile electromagnetic theory and mathematical methods for practical engineers and to train students with advanced EMC applications.

Grounding design and installation is critical for the safety and performance of any electrical or electronic system. Blending theory and practice, this is the first book to provide a thorough approach to grounding from circuit to system. It covers: grounding for safety aspects in facilities, lightning, and NEMP; grounding in printed circuit board, cable shields, and enclosure grounding; and applications in fixed and mobile facilities on land, at sea, and in air. It's an indispensable resource for electrical and electronic engineers concerned with the design of electronic circuits and systems.

Before putting digital systems for information technology or telecommunication applications on the market, an essential requirement is to perform tests in order to comply with the limits of radiated emission imposed by the standards. This book provides an investigation into signal integrity (SI) and electromagnetic interference (EMI) problems. Topics such as reflections, crosstalk, switching noise and radiated emission (RE) in high-speed digital systems are covered, which are essential for IT and telecoms applications. The highly important topic of modelling is covered which can reduce costs by enabling simulation data to demonstrate that a product meets design specifications and regulatory limits. According to the new European EMC directive, this can help to avoid the expensive use of large semi-anechoic chambers or open area test sites for

radiated emission assessments. Following a short introduction to signalling and radiated interference in digital systems, the book provides a detailed characterization of logic families in terms of static and dynamic characteristic useful for modelling techniques. Crosstalk in multi-coupled line structures are investigated by analytical, graphical and circuit-based methods, and techniques to mitigate these phenomena are provided. Grounding, filtering and shielding with multilayer PCBs are also examined and design rules given. Written by authors with extensive experience in industry and academia. Explains basic conceptual problems from a theoretical and practical point of view by using numerous measurements and simulations. Presents models for mathematical and SPICE-like circuit simulators. Provides examples of using full-wave codes for SI and RE investigations. Companion website containing lists of codes and sample material. Signal Integrity and Radiated Emission of High-Speed Digital Systems is a valuable resource to industrial designers of information technology, telecommunication equipment and automation equipment as well as to development engineers. It will also be of interest to managers and designers of consumer electronics, and researchers in electronics.

The 2016 International Conference on Energy Science and Applied Technology (ESAT 2016) held on June 25-26 in Wuhan, China aimed to provide a platform for researchers, engineers, and academicians, as well as industrial professionals, to present their research results and development activities in energy science and engineering and its applied technology. The themes presented in Energy Science and Applied Technology ESAT 2016 are: Technologies in Geology, Mining, Oil and Gas; Renewable Energy, Bio-Energy and Cell Technologies; Energy Transfer and Conversion, Materials and Chemical Technologies; Environmental Engineering and Sustainable Development; Electrical and Electronic Technology, Power System Engineering; Mechanical, Manufacturing, Process Engineering; Control and Automation; Communications and Applied Information Technologies; Applied and Computational Mathematics; Methods and Algorithms Optimization; Network Technology and Application; System Test, Diagnosis, Detection and Monitoring; Recognition, Video and Image Processing.

[Copyright: 978a904c7524eeb9719b17f761f7bcac](http://www.978a904c7524eeb9719b17f761f7bcac.com)