

Digital Circuit Design For Computer Science Students An Introductory Textbook 1st Edition

Offers a complete hands-on approach to the use of computer-aided software and laboratory-based hardware tools for the design and fabrication of electronic printed circuit boards in an EDA environment. Beginning with basic electronic concepts and ending with fully-developed projects, it features extensive examples and complete solutions to computer-aided electronic circuit board design and fabrication using the most affordable and widely used EDA software tools from OrCAD, Inc. Appropriate for self-paced study in computer-aided tools for electronic design.

Digital Circuit Design for Computer Science Students An Introductory Textbook Springer Science & Business Media

This practical introduction explains exactly how digital circuits are designed, from the basic circuit to the advanced system. It covers combinational logic circuits, which collect logic signals, to sequential logic circuits, which embody time and memory to progress through sequences of states. The primer also highlights digital arithmetic and the integrated circuits that implement the logic functions. Based on the author's extensive experience in teaching digital electronics to undergraduates, the book translates theory directly into practice and presents the essential information in a compact, digestible style. Worked problems and examples are accompanied by abbreviated solutions, with demonstrations to ensure that the design material and the circuits' operation are fully understood. This is essential reading for any electronic or electrical engineering student new to digital electronics and requiring a succinct yet comprehensive introduction.

A COMPREHENSIVE GUIDE TO THE DESIGN & ORGANIZATION OF MODERN COMPUTING SYSTEMS Digital Logic Design and Computer Organization with Computer Architecture for Security provides practicing engineers and students with a clear understanding of computer hardware technologies. The fundamentals of digital logic design as well as the use of the Verilog hardware description language are discussed. The book covers computer organization and architecture, modern design concepts, and computer security through hardware. Techniques for designing both small and large combinational and sequential circuits are thoroughly explained. This detailed reference addresses memory technologies, CPU design and techniques to increase performance, microcomputer architecture, including "plug and play" device interface, and memory hierarchy. A chapter on security engineering methodology as it applies to computer architecture concludes the book. Sample problems, design examples, and detailed diagrams are provided throughout this practical resource. COVERAGE INCLUDES: Combinational circuits: small designs Combinational circuits: large designs Sequential circuits: core modules Sequential circuits: small designs Sequential circuits: large designs Memory Instruction set architecture Computer architecture: interconnection Memory system Computer architecture: security

An Introduction to Logic Circuit Testing provides a detailed coverage of techniques for test generation and testable design of digital electronic circuits/systems. The material covered in the book should be sufficient for a course, or part of a course, in digital circuit testing for senior-level undergraduate and first-year graduate students in Electrical Engineering and Computer Science. The book will also be a valuable resource for engineers working in the industry. This book has four chapters. Chapter 1 deals with various types of faults that may occur in very large scale integration (VLSI)-based digital circuits. Chapter 2 introduces the major concepts of all test generation techniques such as redundancy, fault coverage, sensitization, and backtracking. Chapter 3 introduces the key concepts of testability, followed by some ad hoc design-for-

testability rules that can be used to enhance testability of combinational circuits. Chapter 4 deals with test generation and response evaluation techniques used in BIST (built-in self-test) schemes for VLSI chips. Table of Contents: Introduction / Fault Detection in Logic Circuits / Design for Testability / Built-in Self-Test / References

Digital Design and Computer Architecture: ARM Edition takes a unique and modern approach to digital design. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, Harris and Harris use these fundamental building blocks as the basis for what follows: the design of an actual ARM processor. With over 75% of the world's population using products with ARM processors, the design of the ARM processor offers an exciting and timely application of digital design while also teaching the fundamentals of computer architecture. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. By the end of this book, readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works. Harris and Harris have combined an engaging and humorous writing style with an updated and hands-on approach to digital design. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Features side-by-side examples of the two most prominent Hardware Description Languages (HDLs)-SystemVerilog and VHDL-which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. The Companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises.

This digital electronics text focuses on "how to" design, build, operate and adapt data acquisition systems. The material begins with basic logic gates and ends with a 40 KHz voltage measurer. The approach aims to cover a minimal number of topics in detail. The data acquisition circuits described communicate with a host computer through parallel I/O ports. The fundamental idea of the book is that parallel I/O ports (available for all popular computers) offer a superior balance of simplicity, low cost, speed, flexibility and adaptability. All circuits and software are thoroughly tested. Construction details and troubleshooting guidelines are included. This book is intended to serve people who teach or study one of the following: digital electronics, circuit design, software that interacts outside hardware, the process of computer based acquisition, and the design, adaptation, construction and testing of measurement systems.

New, updated and expanded topics in the fourth edition include: EBCDIC, Grey code, practical applications of flip-flops, linear and shaft encoders, memory elements and FPGAs. The section on fault-finding has been expanded. A new chapter is dedicated to the interface between digital components and analog voltages. *A highly accessible, comprehensive and fully up to date digital systems text *A well known and respected text now revamped for current courses *Part of the Newnes suite of texts for HND/1st year modules

In today's digital design environment, engineers must achieve quick turn-around time with ready accesses to circuit synthesis and simulation applications. This type of productivity relies on the principles and practices of computer aided design (CAD). Digital Design: Basic Concepts and Principles addresses the many challenging issues critical to today's digital design practices such as hazards and logic minimization, finite-state-machine synthesis, cycles and races, and testability theories while providing hands-on

experience using one of the industry's most popular design application, Xilinx Web PACKTM. The authors begin by discussing conventional and unconventional number systems, binary coding theories, and arithmetic as well as logic functions and Boolean algebra. Building upon classic theories of digital systems, the book illustrates the importance of logic minimization using the Karnaugh map technique. It continues by discussing implementation options and examining the pros and cons of each method in addition to an assessment of tradeoffs that often accompany design practices. The book also covers testability, emphasizing that a good digital design must be easy to verify and test with the lowest cost possible. Throughout the text, the authors analyze combinational and sequential logic elements and illustrate the designs of these components in structural, hierarchical, and behavior VHDL descriptions. Covering fundamentals and best practices, Digital Design: Basic Concepts and Principles provides you with critical knowledge of how each digital component ties together to form a system and develops the skills you need to design and simulate these digital components using modern CAD software.

Digital Computer Design: Logic, Circuitry, and Synthesis focuses on the logical structure, electronic realization, and application of digital information processors. The manuscript first offers information on numerical symbols, fundamentals of computing aids, quantization, representation of numbers in an electronic digital computer, and computer applications. The text then ponders on the nature of automatic computation and Boolean algebra. Discussions focus on the advantages of a Boolean algebraic description of a digital computer; clock pulse generators and timing circuits; sequential switching networks; elements of information processing systems and types of digital computers; and automatic sequencing methods. The book elaborates on circuit descriptions of switching and storage elements and large capacity storage systems. Topics include static magnetic storage, dynamic delay line storage, cathode-ray storage, vacuum tube systems of circuit logic, and magnetic core systems of circuit logic. The publication also examines the system design of GP computers, digital differential analyzer, and the detection and correction of errors. The text is a valuable source of data for mathematicians and engineers interested in digital computer design.

The impact of digital integrated circuits on our modern society has been pervasive. They are the enabling technology of the current computer and information-technology revolution. This is largely true because of the immense amount of signal and computer processing that can be realized in a single integrated circuit; modern IC's may contain millions of logic gates. This text book is intended to take a reader having only a minimal background and knowledge in electronics to the point where they can design state-of-the-art digital integrated circuits. Designing high-performance digital integrated circuits requires expertise in many different areas. These include semiconductor physics, integrated circuit processing, transistor-level design, logic-level design, system-level design, testing, etc. Aspects of these topics are covered throughout this text, although the emphasis is on transistor-level design of digital integrated circuits and systems. This is in contrast to the perspective in many other texts, which takes a system-level or VLSI approach where transistor-level details are minimized. It is the author's belief that before system-level considerations can be properly evaluated, an in-depth transistor-level understanding must first be obtained. Important system-level considerations such as timing, pipe-lining, clock distribution, and system building blocks are covered in detail, but the emphasis on transistors first.

Throughout the book, physical and intuitive explanations are given, and although mathematical quantitative analysis of many circuits have necessarily been presented, Martin has attempted not to "miss seeing the forest because of the trees". This book presents the critical underlying concepts without becoming entangled in tedious and over-complicated circuit analyses. It is intended for senior/graduate level students in electrical and computer engineering. This course assumes the Sedra/Smith Microelectronic Circuits course as a prerequisite.

This textbook is intended to introduce the student of electronics to the fundamentals of digital circuits, both combinational and sequential, in a reasonable and systematic manner. It proceeds from basic logic concepts to circuits and designs.

Since the mid 1960s, the digital computer has been used as a design tool by electronic circuit designers. Computer software programs called ECAP' and 2 SCEPTRE were among the earliest circuit analysis codes to gain general acceptance by the design community. These programs permitted circuit performance to be simulated for small-signal frequency responses, dc operation points, and transient responses to varying input stimuli. Unfortunately, accessibility to programs such as these by the design community of that era was quite limited since they could be used solely on large, expensive mainframe computers. Only a fraction of the circuit designers at that time were employed by companies large enough to afford the acquisition and maintenance costs of these large computers. The availability of personal computers (PCs) at moderate prices has dramatically changed this picture. The sophistication of the PCs as well as the software that can be run on them has potentially put circuit performance simulation at every designer's desk. Since the early days of ECAP and SCEPTRE, the amount of software for circuit design and analysis has grown enormously. At the same time, the sophistication of the analyses provided by this software has correspondingly increased. In addition, the accuracy of simulation software has improved to where laboratory measurements have become a verification of the analyses, rather than vice versa.

This textbook for a one-semester course in Digital Systems Design describes the basic methods used to develop "traditional" Digital Systems, based on the use of logic gates and flip flops, as well as more advanced techniques that enable the design of very large circuits, based on Hardware Description Languages and Synthesis tools. It was originally designed to accompany a MOOC (Massive Open Online Course) created at the Autonomous University of Barcelona (UAB), currently available on the Coursera platform. Readers will learn what a digital system is and how it can be developed, preparing them for steps toward other technical disciplines, such as Computer Architecture, Robotics, Bionics, Avionics and others. In particular, students will learn to design digital systems of medium complexity, describe digital systems using high level hardware description languages, and understand the operation of computers at their most basic level. All concepts introduced are reinforced by plentiful illustrations, examples, exercises, and applications. For example, as an applied example of the design techniques presented, the authors demonstrate the synthesis of a simple processor, leaving the student in a position to enter the world of Computer Architecture and Embedded Systems.

The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books, this RISC-V Edition covers the

fundamentals of digital logic design and reinforces logic concepts through the design of a RISC-V microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of a processor. By the end of this book, readers will be able to build their own RISC-V microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing a RISC-V processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use SparkFun's RED-V RedBoard to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC-V microprocessor Gives students a full understanding of the RISC-V instruction set architecture, enabling them to build a RISC-V processor and program the RISC-V processor in hardware simulation, software simulation, and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single-cycle, multicycle, and pipelined versions of the RISC-V architecture Features a companion website with a bonus chapter on I/O systems with practical examples that show how to use SparkFun's RED-V RedBoard to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems

This volume offers a new, hands-on approach to teaching Discrete Mathematics. A simple functional language is used to allow students to experiment with mathematical notations which are traditionally difficult to pick up. This practical approach provides students with instant feedback and also allows lecturers to monitor progress easily. All the material needed to use the book will be available via ftp (the software is freely available and runs on Mac, PC and Unix platforms), including a special module which implements the concepts to be learned. No prior knowledge of Functional Programming is required: apart from List Comprehension (which is comprehensively covered in the text) everything the students need is either provided for them or can be picked up easily as they go along. An Instructors Guide will also be available on the WWW to help lecturers adapt existing courses.

The modern world is overrun with electronic equipment, handling huge quantities of data. At the heart of this scenario lies the digital circuitry, which provides the powerful intelligence needed. Thus, there is an increasing need for design engineers in this expanding area. This text starts from basic ideas of logical gates, and progresses through to advanced concepts of digital systems. Each chapter comes with a wealth of illustrative examples and assignment questions for lecture-room use. Contents List of Digital Circuit Design Chapter 1 Introduction to Digital Systems and Logic Gates 1.1 The transition from analogue to digital signals 1.2 Digital logic levels 1.3 The concept of gates 1.4 The AND gate 1.5 The OR gate 1.6 The XOR gate (Exclusive-OR) 1.7 The NOT gate 1.8 Bubbled gates 1.9 The NOR gate 1.10 The NAND gate 1.11 The XNOR gate Chapter 2 Boolean Algebra 2.1 Introducing Boolean algebra 2.2 The AND operation in Boolean algebra 2.3 The OR operation in Boolean algebra 2.4 The XOR operation in Boolean algebra 2.5 The NOT function in Boolean algebra 2.6 Examples of Boolean calculations 2.7 Theorems of Boolean algebra Chapter 3 Combinational Logic 3.1 Illustrations of combinational logic 3.2 Developing Boolean expressions for combinational circuits 3.3 The importance of minimisation 3.4 Karnaugh maps (K-maps) 3.5 Summary of K-map looping rules

3.6 "Can't Happen" states 3.7 Static hazards Chapter 4 Number Systems 4.1 Types of numerical system 4.2 The Decimal number system 4.3 The Binary system 4.4 Binary-to-Decimal conversion 4.5 Decimal-to-binary conversion 4.6 Binary operations 4.7 The Hexadecimal number system Chapter 5 Adders, Subtractors and Multipliers 5.1 Arithmetic in digital circuits 5.2 The half adder 5.3 The full adder 5.4 The parallel binary adder (Ripple carry parallel adder) 5.5 The half subtractor 5.6 The full subtractor 5.7 Multipliers Chapter 6 Multiplexers and Decoders 6.1 Comparators 6.2 Multiplexers 6.3 Demultiplexers 6.4 Encoders 6.5 Decoders Chapter 7 Latches and Flip-Flops 7.1 Introducing time into logic circuits 7.2 The bistable multivibrator (Flip-flop) 7.3 The SR latch 7.4 The SR flip-flop 7.5 The T-type flip-flop 7.6 The D-type flip-flop (Data latch) 7.7 The JK flip-flop 7.8 The Master-Slave JK flip-flop 7.9 Preset and Clear inputs 7.10 Integrated circuit flip-flops Chapter 8 Shift Registers 8.1 Basic shift register functions 8.2 Serial-in serial-out shift registers 8.3 Serial-in parallel-out shift registers 8.4 Parallel-in serial-out shift registers 8.5 Parallel-in parallel-out shift registers 8.6 Bidirectional shift registers 8.7 Shift register counters Chapter 9 Multivibrators and Timers 9.1 What are multivibrators? 9.2 Astable multivibrators 9.3 The monostable multivibrator 9.4 The 555 timer 9.5 Applications of the 555 timer Chapter 10 Counters 10.1 Introducing counters 10.2 Asynchronous counter operation 10.3 Synchronous counter operation 10.4 Up/down synchronous counters 10.5 Cascaded counters 10.6 Counter decoding 10.7 Counter applications conversion Chapter 11 Memories and Data Storage 11.1 Memory types 11.2 Classification by fabrication technology 11.3 Memory terminology 11.4 ROM (Read-Only Memory) 11.5 RAM (Random-Access Memory) Chapter 12 Design of Digital Integrated Circuits (ICs) 12.1 Logic families 12.2 Electrical characteristics of digital ICs margin 12.3 RTL and DTL families 12.4 The TTL logic family 12.5 The ECL logic family 12.6 The I²L logic family 12.7 The MOSFET logic family 12.8 CMOS circuits gates

As the costs of power and timing become increasingly difficult to manage in traditional synchronous systems, designers are being forced to look at asynchronous alternatives. Based on reworked and expanded papers from the VII Banff Higher Order Workshop, this volume examines asynchronous methods which have been used in large circuit design, ranging from initial formal specification to more standard finite state machine based control models. Written by leading practitioners in the area, the papers cover many aspects of current practice including practical design, silicon compilation, and applications of formal specification. It also includes a state-of-the-art survey of asynchronous hardware design. The resulting volume will be invaluable to anyone interested in designing correct asynchronous circuits which exhibit high performance or low power operation.

Digital Design and Computer Architecture: ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor. By the end of this book, readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing an ARM processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Features side-by-side examples of the two most prominent Hardware Description

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Languages (HDLs)—SystemVerilog and VHDL—which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader’s understanding and retention of key concepts and techniques. The Companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises.

This is an up-to-date treatment of the analysis and design of CMOS integrated digital logic circuits. The self-contained book covers all of the important digital circuit design styles found in modern CMOS chips, emphasizing solving design problems using the various logic styles available in CMOS.

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A completely updated and expanded comprehensive treatment of VHDL and its applications to the design and simulation of real, industry-standard circuits. This comprehensive treatment of VHDL and its applications to the design and simulation of real, industry-standard circuits has been completely updated and expanded for the third edition. New features include all VHDL-2008 constructs, an extensive review of digital circuits, RTL analysis, and an unequalled collection of VHDL examples and exercises. The book focuses on the use of VHDL rather than solely on the language, with an emphasis on design examples and laboratory exercises. The third edition begins with a detailed review of digital circuits (combinatorial, sequential, state machines, and FPGAs), thus providing a self-contained single reference for the teaching of digital circuit design with VHDL. In its coverage of VHDL-2008, it makes a clear distinction between VHDL for synthesis and VHDL for simulation. The text offers complete VHDL codes in examples as well as simulation results and comments. The significantly expanded examples and exercises include many not previously published, with multiple physical demonstrations meant to inspire and motivate students. The book is suitable for undergraduate and graduate students in VHDL and digital circuit design, and can be used as a professional reference for VHDL practitioners. It can also serve as a text for digital VLSI in-house or academic courses.

This book explains the application of recent advances in computational intelligence – algorithms, design methodologies, and synthesis techniques – to the design of integrated circuits and systems. It highlights new biasing and sizing approaches and optimization techniques and their application to the design of high-performance digital, VLSI, radio-frequency, and mixed-signal circuits and systems. This second of two related volumes addresses digital and network designs and applications, with 12 chapters grouped into parts on digital circuit design, network optimization, and applications. It will be of interest to practitioners and researchers in computer science and electronics engineering engaged with the design of electronic circuits.

A unique, practical approach to the design of high-speed digital circuit boards The demand for ever-faster digital circuit designs is beginning to render the circuit theory used by engineers ineffective. Digital Circuit Boards presents an alternative to the circuit theory approach, emphasizing energy flow rather than just signal interconnection to explain logic circuit behavior. The book shows

how treating design in terms of transmission lines will ensure that the logic will function, addressing both storage and movement of electrical energy on these lines. It covers transmission lines in all forms to illustrate how trace geometry defines where the signals can travel, then goes on to examine transmission lines as energy sources, the true nature of decoupling, types of resonances, ground bounce, cross talk, and more. Providing designers with the tools they need to lay out digital circuit boards for fast logic and to get designs working the first time around, Digital Circuit Boards: Reviews in simple terms the basic physics necessary to understand fast logic design Debunks the idea that electrical conductors carry power and signals, showing that signal travels in the spaces, not the traces, of circuit boards Explains logic circuit behavior through real-time analysis involving the fields and waves that carry signal and energy Provides new information on how ground/power planes work Outlines a software program for solving energy flow in complex networks

This book presents the basic concepts used in the design and analysis of digital systems and introduces the principles of digital computer organization and design.

This textbook for core courses in Electronic Circuit Design teaches students the design and application of a broad range of analog electronic circuits in a comprehensive and clear manner. Readers will be enabled to design complete, functional circuits or systems. The authors first provide a foundation in the theory and operation of basic electronic devices, including the diode, bipolar junction transistor, field effect transistor, operational amplifier and current feedback amplifier. They then present comprehensive instruction on the design of working, realistic electronic circuits of varying levels of complexity, including power amplifiers, regulated power supplies, filters, oscillators and waveform generators. Many examples help the reader quickly become familiar with key design parameters and design methodology for each class of circuits. Each chapter starts from fundamental circuits and develops them step-by-step into a broad range of applications of real circuits and systems. Written to be accessible to students of varying backgrounds, this textbook presents the design of realistic, working analog electronic circuits for key systems; Includes worked examples of functioning circuits, throughout every chapter, with an emphasis on real applications; Includes numerous exercises at the end of each chapter; Uses simulations to demonstrate the functionality of the designed circuits; Enables readers to design important electronic circuits including amplifiers, power supplies and oscillators.

This volume concentrates on three topics: mixed analog--digital circuit design, sensor interface circuits and communication circuits. The book comprises six papers on each topic of a tutorial nature aimed at improving the design of analog circuits. The book is divided into three parts. Part I: Mixed Analog--Digital Circuit Design considers the largest growth area in microelectronics. Both standard designs and ASICs have begun integrating analog cells and digital sections on the same chip. The papers cover topics such as groundbounce and supply-line spikes, design methodologies for high-level design and actual mixed analog--digital designs. Part II: Sensor Interface Circuits describes various types of signal conditioning circuits and interfaces for sensors. These include interface solutions for capacitive sensors, sigma--delta modulation used to combine a microprocessor compatible interface with on chip CMOS sensors, injectable sensors and responders, signal conditioning circuits and sensors combined with indirect

converters. Part III: Communication Circuits concentrates on systems and implemented circuits for use in personal communication systems. These have applications in cordless telephones and mobile telephone systems for use in cellular networks. A major requirement for these systems is low power consumption, especially when operating in standby mode, so as to maximise the time between battery recharges.

With growing consumer demand for portability and miniaturization in electronics, design engineers must concentrate on many additional aspects in their core design. The plethora of components that must be considered requires that engineers have a concise understanding of each aspect of the design process in order to prevent bug-laden prototypes. Electronic Circuit Design allows engineers to understand the total design process and develop prototypes which require little to no debugging before release. It provides step-by-step instruction featuring modern components, such as analog and mixed signal blocks, in each chapter. The book details every aspect of the design process from conceptualization and specification to final implementation and release. The text also demonstrates how to utilize device data sheet information and associated application notes to design an electronic system. The hybrid nature of electronic system design poses a great challenge to engineers. This book equips electronics designers with the practical knowledge and tools needed to develop problem free prototypes that are ready for release. The author is the leading programming language designer of our time and in this book, based on a course for 2nd-year students at, he closes the gap between hardware and software design. He encourages students to put the theory to work in exercises that include lab work culminating in the design of a simple yet complete computer. In short, a modern introduction to designing circuits using state-of-the-art technology and a concise, easy to master hardware description language (Lola).

For courses on digital design in an Electrical Engineering, Computer Engineering, or Computer Science department. Digital Design, fifth edition is a modern update of the classic authoritative text on digital design. This book teaches the basic concepts of digital design in a clear, accessible manner. The book presents the basic tools for the design of digital circuits and provides procedures suitable for a variety of digital applications.

For those with a basic understanding of digital design, this book teaches the essential skills to design digital integrated circuits using Verilog and the relevant extensions of SystemVerilog. In addition to covering the syntax of Verilog and SystemVerilog, the author provides an appreciation of design challenges and solutions for producing working circuits. The book covers not only the syntax and limitations of HDL coding, but deals extensively with design problems such as partitioning and synchronization, helping you to produce designs that are not only logically correct, but will actually work when turned into physical circuits. Throughout the book, many small examples are used to validate concepts and demonstrate how to apply design skills. This book takes readers who have already learned the fundamentals of digital design to the point where they can produce working circuits using modern design methodologies. It clearly explains what is useful for circuit design and what parts of the languages are only software, providing a non-theoretical, practical guide to robust, reliable and optimized hardware design and development. Produce working hardware: Covers not only syntax, but also provides design know-how, addressing problems such as synchronization and

partitioning to produce working solutions Usable examples: Numerous small examples throughout the book demonstrate concepts in an easy-to-grasp manner Essential knowledge: Covers the vital design topics of synchronization, essential for producing working silicon; asynchronous interfacing techniques; and design techniques for circuit optimization, including partitioning This book takes an authoritative introduction to basic principles of digital design and practical requirements in both board-level and VLSI systems. Digital Design covers the most widespread logic design practices while building a solid foundation of theoretical and engineering principles. This easy-to-follow book uses a practical writing style. Includes low voltage and LVCMOS/LVTTL. Coverage of Complex Programmable Logic Devices (CPLDs) and Field-Programmable Gate Arrays (FPGAs). Introduction of HDL-based digital design Covers VHDL as well as ABEL. Including simulation and synthesis.

Unlike books currently on the market, this book attempts to satisfy two goals: combine circuits and electronics into a single, unified treatment, and establish a strong connection with the contemporary world of digital systems. It will introduce a new way of looking not only at the treatment of circuits, but also at the treatment of introductory coursework in engineering in general. Using the concept of "abstraction," the book attempts to form a bridge between the world of physics and the world of large computer systems. In particular, it attempts to unify electrical engineering and computer science as the art of creating and exploiting successive abstractions to manage the complexity of building useful electrical systems. Computer systems are simply one type of electrical systems. +Balances circuits theory with practical digital electronics applications. +Illustrates concepts with real devices. +Supports the popular circuits and electronics course on the MIT OpenCourse Ware from which professionals worldwide study this new approach. +Written by two educators well known for their innovative teaching and research and their collaboration with industry. +Focuses on contemporary MOS technology.

This book is an undergraduate textbook for students of electrical and electronic engineering. It is written with second year students particularly in mind, and discusses analogue circuits used in various fields.

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