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This book provides readers with a valuable reference on cyber weapons and, in particular, viruses, software and hardware Trojans. The authors discuss in detail the most dangerous computer viruses, software Trojans and spyware, models of computer Trojans affecting computers, methods of implementation and mechanisms of their interaction with an attacker — a hacker, an intruder or an intelligence agent. Coverage includes Trojans in electronic equipment such as telecommunication systems, computers, mobile communication systems, cars and even consumer electronics. The evolutionary path of development of hardware Trojans from "cabinets", "crates" and "boxes" to the microcircuits (IC) is also discussed. Readers will benefit from the detailed review of the major known types of hardware Trojans in chips, principles of their design, mechanisms of their functioning, methods of their introduction, means of camouflaging and detecting, as well as methods of protection and counteraction.

"This book highlights invaluable research covering the design, development, and evaluation of online learning environments, examining the role of technology enhanced learning in this emerging area"--Provided by publisher.--

Simulation modelling involves the development of

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models that imitate real-world operations, and statistical analysis of their performance with a view to improving efficiency and effectiveness. This non-technical textbook is focused towards the needs of business, engineering and computer science students, and concentrates on discrete event simulations as it is used in operations management. Stewart Robinson of Warwick Business School offers guidance through the key stages in a simulation project in terms of both the technical requirements and the project management issues surrounding it. Readers will emerge able to develop appropriate valid conceptual models, perform simulation experiments, analyse the results and draw insightful conclusions.

Circuit Simulation with SPICE OPUS Theory and Practice Springer Science & Business Media

A presentation of circuit synthesis and circuit simulation using VHDL (including VHDL 2008), with an emphasis on design examples and laboratory exercises. This text offers a comprehensive treatment of VHDL and its applications to the design and simulation of real, industry-standard circuits. It focuses on the use of VHDL rather than solely on the language, showing why and how certain types of circuits are inferred from the language constructs and how any of the four simulation categories can be implemented. It makes a rigorous distinction between VHDL for synthesis and VHDL for simulation. The VHDL codes in all design examples are complete, and circuit diagrams, physical synthesis in FPGAs, simulation results, and explanatory comments are included with the designs. The text reviews fundamental

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concepts of digital electronics and design and includes a series of appendixes that offer tutorials on important design tools including ISE, Quartus II, and ModelSim, as well as descriptions of programmable logic devices in which the designs are implemented, the DE2 development board, standard VHDL packages, and other features. All four VHDL editions (1987, 1993, 2002, and 2008) are covered. This expanded second edition is the first textbook on VHDL to include a detailed analysis of circuit simulation with VHDL testbenches in all four categories (nonautomated, fully automated, functional, and timing simulations), accompanied by complete practical examples. Chapters 1–9 have been updated, with new design examples and new details on such topics as data types and code statements. Chapter 10 is entirely new and deals exclusively with simulation. Chapters 11–17 are also entirely new, presenting extended and advanced designs with theoretical and practical coverage of serial data communications circuits, video circuits, and other topics. There are many more illustrations, and the exercises have been updated and their number more than doubled.

"The English version of Dissemination [is] an able translation by Barbara Johnson Derrida's central contention is that language is haunted by dispersal, absence, loss, the risk of unmeaning, a risk which is starkly embodied in all writing. The distinction between philosophy and literature therefore becomes of secondary importance. Philosophy vainly attempts to control the irrecoverable dissemination of its own meaning, it strives—against the grain of language—to offer

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a sober revelation of truth. Literature—on the other hand—flaunts its own meretriciousness, abandons itself to the Dionysiac play of language. In

Dissemination—more than any previous work—Derrida joins in the revelry, weaving a complex pattern of puns, verbal echoes and allusions, intended to 'deconstruct' both the pretension of criticism to tell the truth about literature, and the pretension of philosophy to the literature of truth."—Peter Dews, *New Statesman*

This book is the second of two volumes addressing the design challenges associated with new generations of semiconductor technology. The various chapters are compiled from tutorials presented at workshops in recent years by prominent authors from all over the world. Technology, productivity and quality are the main aspects under consideration to establish the major requirements for the design and test of upcoming systems on a chip.

This book introduces the basic mathematical tools used to describe noise and its propagation through linear systems and provides a basic description of the improvement of signal-to-noise ratio by signal averaging and linear filtering. The text also demonstrates how op amps are the keystone of modern analog signal conditioning systems design, and il

â€œToward a Ludic Architectureâ€ is a pioneering publication, architecturally framing play and games as human practices in and of space. Filling the gap in literature, Steffen P. Walz considers game design theory and practice alongside architectural theory and practice, asking: how are play and games architected? What kind of architecture do they produce and in what way does architecture program play and games? What kind of architecture could be produced by

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playing and gameplaying?

In recent decades, the importance of sound for remembering the past and for creating a sense of belonging has been increasingly acknowledged. We keep "sound souvenirs" such as cassette tapes and long play albums in our attics because we want to be able to recreate the music and everyday sounds we once cherished. Artists and ordinary listeners deploy the newest digital audio technologies to recycle past sounds into present tunes. Sound and memory are inextricably intertwined, not just through the commercially exploited nostalgia on oldies radio stations, but through the exchange of valued songs by means of pristine recordings and cultural practices such as collecting, archiving and listing. This book explores several types of cultural practices involving the remembrance and restoration of past sounds. At the same time, it theorizes the cultural meaning of collecting, recycling, reciting, and remembering sound and music. An original, endlessly thought-provoking, and controversial look at the nature of consciousness and identity argues that the key to understanding selves and consciousness is the "strange loop," a special kind of abstract feedback loop inhabiting our brains.

This book is all about Spice Circuit Simulations Using LTspice. LTspice is available free from Linear Technology. LTspice is perhaps one of the most widely used free simulators. It is a powerful simulator with a simple interface to handle. The book covers the requirements of a laboratory course in SPICE simulations at an introductory level. It can be used as an aid to practical understanding in any undergraduate engineering course of Analog electronics. The book can also be used as an aid to any standard text on Analog Electronics. Salient Features: * Step by step simulation procedure is presented * Experiments are clearly illustrated. * Brief theory on each topic for understanding is presented.

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The European Computing Conference offers a unique forum for establishing new collaborations within present or upcoming research projects, exchanging useful ideas, presenting recent research results, participating in discussions and establishing new academic collaborations, linking university with the industry. Engineers and Scientists working on various areas of Systems Theory, Applied Mathematics, Simulation, Numerical and Computational Methods and Parallel Computing present the latest findings, advances, and current trends on a wide range of topics. This proceedings volume will be of interest to students, researchers, and practicing engineers.

First Published in 2018. Routledge is an imprint of Taylor & Francis, an Informa company.

This market-leading textbook continues its standard of excellence and innovation built on the solid pedagogical foundation of previous editions. This new edition has been thoroughly updated to reflect changes in technology, and includes new BJT/MOSFET coverage that combines and emphasizes the unity of the basic principles while allowing for separate treatment of the two device types where needed. Amply illustrated by a wealth of examples and complemented by an expanded number of well-designed end-of-chapter problems and practice exercises, Microelectronic Circuits is the most current resource available for teaching tomorrow's engineers how to analyze and design electronic circuits.

This text focuses on techniques for minimizing power dissipation during test application at logic and

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register-transfer levels of abstraction of the VLSI design flow. It surveys existing techniques and presents several test automation techniques for reducing power in scan-based sequential circuits and BIST data paths.

This book presents the art of advanced MOSFET modeling for integrated circuit simulation and design. It provides the essential mathematical and physical analyses of all the electrical, mechanical and thermal effects in MOS transistors relevant to the operation of integrated circuits. Particular emphasis is placed on how the BSIM model evolved into the first ever industry standard SPICE MOSFET model for circuit simulation and CMOS technology development. The discussion covers the theory and methodology of how a MOSFET model, or semiconductor device models in general, can be implemented to be robust and efficient, turning device physics theory into a production-worthy SPICE simulation model. Special attention is paid to MOSFET characterization and model parameter extraction methodologies, making the book particularly useful for those interested or already engaged in work in the areas of semiconductor devices, compact modeling for SPICE simulation, and integrated circuit design. This book is intended for the reader who wishes to gain a solid understanding of Phase Locked Loop architectures and their applications. It provides a unique balance between both theoretical

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perspectives and practical design trade-offs.

Engineers faced with real world design problems will find this book to be a valuable reference providing example implementations, the underlying equations that describe synthesizer behavior, and measured results that will improve confidence that the equations are a reliable predictor of system behavior. New material in the Fourth Edition includes partially integrated loop filter implementations, voltage controlled oscillators, and modulation using the PLL.

THE LATEST SPICE SIMULATION AND DESIGN TOOLS FOR CREATING STATE-OF-THE-ART SWITCHMODE POWER SUPPLIES

Fully updated to incorporate new SPICE features and capabilities, this practical guide explains, step by step, how to simulate, test, and improve switch-mode power supply designs. Detailed formulas with founding equations are included. Based on the author's continued research and in-depth, hands-on work in the field, this revised resource offers a collection of the latest SPICE solutions to the most difficult problem facing power supply designers: creating smaller, more heat-efficient power supplies in shorter design cycles. NEW to this edition: Complete analysis of rms currents for the three basic cells in CCM and DCM PWM switch at work in the small-signal analysis of the DCM boost and the QR flyback OTA-based compensators Complete transistor-level

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TL431 model Small-signal analysis of the borderline-operated boost PFC circuit operated in voltage or current mode All-over power phenomena in QR or fixed-frequency discontinuous/continuous flyback converters Small-signal model of a QR flyback converter Small-signal model of the active clamp forward converter operated in voltage mode control Electronic content—design templates and examples available online Switch-Mode Power Supplies: SPICE Simulations and Practical Designs, Second Edition, covers: Small-signal modeling * Feedback and control loops * Basic blocks and generic switched models * Nonisolated converters * Off-line converters * Flyback converters * Forward converters * Power factor correction

Vacuum technology has enormous impact on human life in many aspects and fields, such as metallurgy, material development and production, food and electronic industry, microelectronics, device fabrication, physics, materials science, space science, engineering, chemistry, technology of low temperature, pharmaceutical industry, and biology. All decorative coatings used in jewelries and various daily products—including shiny decorative papers, the surface finish of watches, and light fixtures—are made using vacuum technological processes. Vacuum analytical techniques and vacuum technologies are pillars of the technological processes, material synthesis, deposition, and

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material analyses—all of which are used in the development of novel materials, increasing the value of industrial products, controlling the technological processes, and ensuring the high product quality. Based on physical models and calculated examples, the book provides a deeper look inside the vacuum physics and technology.

This book constitutes the post-proceedings of the Third International Computer Music Modeling and Retrieval Symposium, CMMR 2005. The 24 revised full papers address a broad variety of topics, organized in topical sections on sound synthesis; music perception and cognition; interactive music: interface, interaction, gestures and sensors, music composition; music retrieval; music performance, music analysis, music representation; as well as interdisciplinarity and computer music.

For correctness of observation and readiness of wit Varthema stands in the foremost rank of the old Oriental travellers. In Arabia and in the Indian archipelago east of Java he is (for Europe and Christendom) a real discoverer. Even where passing over ground traversed by earlier European explorers, his keen intelligence frequently adds valuable original notes on peoples, manners, customs, laws, religions, products, trade, methods of war. --Richard Francis Burton.

This volume provides a discussion of the challenges and perspectives of electromagnetics and network

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theory and their microwave applications in all aspects. It collects the most interesting contribution of the symposium dedicated to Professor Peter Russer held in October 2009 in Munich.

In this book key contributions on developments and challenges in research and education on microelectronics, microsystems and related areas are published. Topics of interest include, but are not limited to: emerging fields in design and technology, new concepts in teaching, multimedia in microelectronics, industrial roadmaps and microelectronic education, curricula, nanoelectronics teaching, long distance education. The book is intended for academic education level and targets professors, researchers and PhDs involved in microelectronics and/or more generally, in electrical engineering, microsystems and material sciences. The 2004 edition of European Workshop on Microelectronics Education (EWME) is particularly focused on the interface between microelectronics and bio-medical sciences.

Before putting digital systems for information technology or telecommunication applications on the market, an essential requirement is to perform tests in order to comply with the limits of radiated emission imposed by the standards. This book provides an investigation into signal integrity (SI) and electromagnetic interference (EMI) problems. Topics such as reflections, crosstalk, switching noise and

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radiated emission (RE) in high-speed digital systems are covered, which are essential for IT and telecoms applications. The highly important topic of modelling is covered which can reduce costs by enabling simulation data to demonstrate that a product meets design specifications and regulatory limits. According to the new European EMC directive, this can help to avoid the expensive use of large semi-anechoic chambers or open area test sites for radiated emission assessments. Following a short introduction to signalling and radiated interference in digital systems, the book provides a detailed characterization of logic families in terms of static and dynamic characteristic useful for modelling techniques. Crosstalk in multi-coupled line structures are investigated by analytical, graphical and circuit-based methods, and techniques to mitigate these phenomena are provided. Grounding, filtering and shielding with multilayer PCBs are also examined and design rules given. Written by authors with extensive experience in industry and academia. Explains basic conceptual problems from a theoretical and practical point of view by using numerous measurements and simulations. Presents models for mathematical and SPICE-like circuit simulators. Provides examples of using full-wave codes for SI and RE investigations. Companion website containing lists of codes and sample material. Signal Integrity and Radiated Emission of

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High-Speed Digital Systems is a valuable resource to industrial designers of information technology, telecommunication equipment and automation equipment as well as to development engineers. It will also be of interest to managers and designers of consumer electronics, and researchers in electronics.

This new book, written by Andre Vladimirescu, who was instrumental in the development of SPICE at the University of California Berkeley, introduces computer simulation of electrical and electronics circuits based on the SPICE standard. Relying on the functionality first supported in SPICE2 that is now supported in all SPICE programs, this text is addressed to all users of electrical simulation. The approach to learning circuit simulation is to interpret simulation results in relation to electrical engineering fundamentals; the book asks the student to solve most circuit examples by hand before verifying the results with SPICE. Addressed to both the SPICE novice and the experienced user, the first six chapters provide the relevant information on SPICE functionality for the analysis of linear as well as nonlinear circuits. Each of these chapters starts out with a linear example accessible to any new user of SPICE and proceeds with nonlinear transistor circuits. The latter part of the book goes into more detail on such issues as functional and hierarchical models, distortion analysis, basic algorithms in

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SPICE and related options-parameters, and, how to direct SPICE to find a solution when it does not converge to a solution. The approach emphasizes that SPICE is not a substitute for knowledge of circuit operation but a complement. The SPICE Book is different from previously published books in the approach of solving circuit problems with a computer. The solution to most circuit examples is sketched out by hand first and followed by a SPICE verification. For more complex circuits it is not feasible to find the solution by hand but the approach stresses the need for the SPICE user to understand the results. Readers gain a better comprehension of SPICE thanks to the importance placed on the relation between EE fundamentals and computer simulation. The tutorial approach advances from the hand solution of a circuit to SPICE verification and simulation results interpretation. This book teaches the approach to electrical circuit simulation rather than a specific simulation program. Examples are simulated alternatively with SPICE2, SPICE3 or PSPICE. Accurate descriptions, simulation rationale and cogent explanations make this an invaluable reference.

Essential reading for experts in the field of RF circuit design and engineers needing a good reference. This book provides complete design procedures for multiple-pole Butterworth, Chebyshev, and Bessel filters. It also covers capacitors, inductors, and other

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components with their behavior at RF frequencies discussed in detail. Provides complete design procedures for multiple-pole Butterworth, Chebyshev, and Bessel filters Covers capacitors, inductors, and other components with their behavior at RF frequencies discussed in detail

Engineering productivity in integrated circuit product design and development today is limited largely by the effectiveness of the CAD tools used. For those domains of product design that are highly dependent on transistor-level circuit design and optimization, such as high-speed logic and memory, mixed-signal analog-digital interfaces, RF functions, power integrated circuits, and so forth, circuit simulation is perhaps the single most important tool. As the complexity and performance of integrated electronic systems has increased with scaling of technology feature size, the capabilities and sophistication of the underlying circuit simulation tools have correspondingly increased. The absolute size of circuits requiring transistor-level simulation has increased dramatically, creating not only problems of computing power resources but also problems of task organization, complexity management, output representation, initial condition setup, and so forth. Also, as circuits of more complexity and mixed types of functionality are attacked with simulation, the spread between time constants or event time scales within the circuit has tended to become wider,

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requiring new strategies in simulators to deal with large time constant spreads.

Embedded systems encompass a variety of hardware and software components which perform specific functions in host systems, for example, satellites, washing machines, hand-held telephones and automobiles. Embedded systems have become increasingly digital with a non-digital periphery (analog power) and therefore, both hardware and software codesign are relevant. The vast majority of computers manufactured are used in such systems. They are called 'embedded' to distinguish them from standard mainframes, workstations, and PCs.

Although the design of embedded systems has been used in industrial practice for decades, the systematic design of such systems has only recently gained increased attention. Advances in microelectronics have made possible applications that would have been impossible without an embedded system design. Embedded System Applications describes the latest techniques for embedded system design in a variety of applications. This also includes some of the latest software tools for embedded system design. Applications of embedded system design in avionics, satellites, radio astronomy, space and control systems are illustrated in separate chapters. Finally, the book contains chapters related to industrial best-practice in embedded system design. Embedded System

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Applications will be of interest to researchers and designers working in the design of embedded systems for industrial applications.

This comprehensive book is written to inform and improve outcomes of patients in need of blood management during surgical procedures. Information is presented in an accessible format, allowing for immediate use in clinical practice. Beginning with an overview of the history of blood transfusions, early chapters present the foundational information needed to comprehend information in later chapters. Nuanced procedures, drugs, and techniques are covered, including new biologicals to assist clotting and blood substitutes. Further discussions focus on potential complications seen in blood transfusions, such as diseases of the coagulation system, pathogen transmissions, and acute lung injuries. Chapters also examine the complexities of treating specific demographics, of which include the geriatric patient and patients suffering from substance abuse. Essentials of Blood Product Management in Anesthesia Practice is an invaluable guide for anesthesiologists, surgeons, trauma physicians, and solid organ transplant providers.

Multisim is now the de facto standard for circuit simulation. It is a SPICE-based circuit simulator which combines analog, discrete-time, and mixed-mode circuits. In addition, it is the only simulator which incorporates microcontroller simulation in the

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same environment. It also includes a tool for printed circuit board design. Advanced Circuit Simulation Using Multisim Workbench is a companion book to Circuit Analysis Using Multisim, published by Morgan & Claypool in 2011. This new book covers advanced analyses and the creation of models and subcircuits. It also includes coverage of transmission lines, the special elements which are used to connect components in PCBs and integrated circuits. Finally, it includes a description of Ultiboard, the tool for PCB creation from a circuit description in Multisim. Both books completely cover most of the important features available for a successful circuit simulation with Multisim. Table of Contents: Models and Subcircuits / Transmission Lines / Other Types of Analyses / Simulating Microcontrollers / PCB Design With Ultiboard

This book is a unique combination of a basic guide to general analog circuit simulation and a SPICE OPUS software manual, which may be used as a textbook or self-study reference. The book is divided into three parts: mathematical theory of circuit analysis, a crash course on SPICE OPUS, and a complete SPICE OPUS reference guide. All simulations as well as the free simulator software may be directly downloaded from the SPICE OPUS homepage: www.spiceopus.si. Circuit Simulation with SPICE OPUS is intended for a wide audience of undergraduate and graduate students, researchers,

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and practitioners in electrical and systems engineering, circuit design, and simulation development.

This Dictionary covers information and communication technology (ICT), including hardware and software; information networks, including the Internet and the World Wide Web; automatic control; and ICT-related computer-aided fields. The Dictionary also lists abbreviated names of relevant organizations, conferences, symposia and workshops. This reference is important for all practitioners and users in the areas mentioned above, and those who consult or write technical material. This Second Edition contains 10,000 new entries, for a total of 33,000.

Publisher's Note: Products purchased from Third Party sellers are not guaranteed by the publisher for quality, authenticity, or access to any online entitlements included with the product. Learn the principles and practices of simulation-based analog IC design This comprehensive textbook and on-the-job reference offers clear instruction on analog integrated circuit design using the latest simulation techniques. Ideal for graduate students and professionals alike, the book shows, step by step, how to develop and deploy integrated circuits for cutting-edge Internet of Things (IoT) and other applications. Analog Integrated Circuit Design by Simulation: Techniques, Tools, and Methods lays out

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practical, ready-to-apply engineering strategies.

Application layer, device layer, and circuit layer IC design are covered in complete detail. You will learn how to tackle real-world design problems and avoid long cycles of trial and error. Coverage includes:

- First-order DC response
- Unified closed-loop model
- Accurate modeling of DC response
- Frequency and step response
- Multi-pole dynamic response and stability
- Effect of external network on differential gain
- Continuous-time and discrete-time amplifiers
- MOSFET, NMOS, and PMOS characteristics
- Small-signal modeling and circuit analysis
- Resistor and capacitor design
- Current sources, sinks, and mirrors
- Basic, symmetrical, folded-cascode, and Miller OTAs
- Opamps with source-follower and common-source output stages
- Fully differential OTAs and opamps

This book constitutes the refereed proceedings of the 14th International Workshop on Power and Timing Optimization and Simulation, PATMOS 2004, held in Santorini, Greece in September 2004. The 85 revised papers presented together with abstracts of 6 invited presentations were carefully reviewed and selected from 152 papers submitted. The papers are organized in topical sections on buses and communication, circuits and devices, low power issues, architectures, asynchronous circuits, systems design, interconnect and physical design,

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security and safety, low-power processing, digital
design, and modeling and simulation.

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