

## Behzad Razavi Cmos Solution

Designers of high-speed integrated circuits face a bewildering array of choices and too often spend frustrating days tweaking gates to meet speed targets. Logical Effort: Designing Fast CMOS Circuits makes high speed design easier and more methodical, providing a simple and broadly applicable method for estimating the delay resulting from factors such as topology, capacitance, and gate sizes. The brainchild of circuit and computer graphics pioneers Ivan Sutherland and Bob Sproull, "logical effort" will change the way you approach design challenges. This book begins by equipping you with a sound understanding of the method's essential procedures and concepts-so you can start using it immediately. Later chapters explore the theory and finer points of the method and detail its specialized applications. Features Explains the method and how to apply it in two practically focused chapters. Improves circuit design intuition by teaching simple ways to discern the consequences of topology and gate size decisions. Offers easy ways to choose the fastest circuit from among an array of potential circuit designs. Reduces the time spent on tweaking and simulations-so you can rapidly settle on a good design. Offers in-depth coverage of specialized areas of application for logical effort: skewed or unbalanced gates, other circuit families (including pseudo-NMOS and domino), wide structures such as decoders, and irregularly forking circuits. Presents a complete derivation of the method-so you see how and why it works.

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This book provides the most comprehensive and in-depth coverage of the latest circuit design developments in RF CMOS technology. It is a practical and cutting-edge guide, packed with proven circuit techniques and innovative design methodologies for solving challenging problems associated with RF integrated circuits and systems. This invaluable resource features a collection of the finest design practices that may soon drive the system-on-chip revolution. Using this book's state-of-the-art design techniques, one can apply existing technologies in novel ways and to create new circuit designs for the future.

With the exponential growth of the number of Internet nodes, the volume of the data transported on the backbone has increased with the same trend. The load of the global Internet backbone will soon increase to tens of terabits per second. This indicates that the backbone bandwidth requirements will increase by a factor of 50 to 100 every seven years. Transportation of such high volumes of data requires suitable media with low loss and high bandwidth. Among the available transmission media, optical fibers achieve the best performance in terms of loss and bandwidth. High-speed data can be transported over hundreds of kilometers of single-mode fiber without significant loss in signal integrity. These fibers progressively benefit from reduction of cost and improvement of performance. Meanwhile, the electronic interfaces used in an optical network are not capable of exploiting the ultimate bandwidth of the fiber, limiting the throughput of the network. Different solutions at both the system and the circuit levels

have been proposed to increase the data rate of the backbone. System-level solutions are based on the utilization of wave-division multiplexing (WDM), using different colors of light to transmit several sequences simultaneously. In parallel with that, a great deal of effort has been put into increasing the operating rate of the electronic transceivers using highly-developed fabrication processes and novel circuit techniques.

This book describes intuitive analog design approaches using digital inverters, providing filter architectures and circuit techniques enabling high performance analog circuit design. The authors provide process, supply voltage and temperature (PVT) variation-tolerant design techniques for inverter based circuits. They also discuss various analog design techniques for lower technology nodes and lower power supply, which can be used for designing high performance systems-on-chip.

This book discusses design techniques, layout details and measurements of several key analog building blocks that currently limit the performance of 5G and E-Band transceivers implemented in deep-scaled CMOS. The authors present recent developments in low-noise quadrature VCOs and tunable inductor-less frequency dividers. Moreover, the design of low-loss broadband transformer-based filters that realize inter-stage matching, power division/combining and impedance transformation is discussed in great detail. The design and measurements of a low-noise amplifier, a downconverter and a highly-linear power amplifier that leverage the proposed techniques are shown. All the prototypes were realized in advanced nanometer scaled

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CMOS technologies without RF thick to metal option.

Learn the basic properties and designs of modern VLSI devices, as well as the factors affecting performance, with this thoroughly updated second edition. The first edition has been widely adopted as a standard textbook in microelectronics in many major US universities and worldwide. The internationally renowned authors highlight the intricate interdependencies and subtle trade-offs between various practically important device parameters, and provide an in-depth discussion of device scaling and scaling limits of CMOS and bipolar devices. Equations and parameters provided are checked continuously against the reality of silicon data, making the book equally useful in practical transistor design and in the classroom. Every chapter has been updated to include the latest developments, such as MOSFET scale length theory, high-field transport model and SiGe-base bipolar devices.

Market\_Desc: Engineers Special Features: " Updates the coverage of bipolar technologies" Enhances the discussion of biCMOS" Provides a more unified treatment of digital and analog circuit design while strengthening the coverage of CMOS" Removes the chapter on non-linear analog circuits" Adds a new operational amplifier example to chapter 11 About The Book: This is the only comprehensive book in the market for engineers that covers CMOS, bipolar technologies, and biCMOS integrated circuits. The fifth edition retains its completeness, updates the coverage of bipolar technologies, and enhances the discussion of biCMOS. It provides a more unified

treatment of digital and analog circuit design while strengthening the coverage of CMOS. The chapter on non-linear analog circuits has been removed and chapter 11 has been updated to include an operational amplifier example. With its streamlined and up-to-date coverage, more engineers can turn to this resource to explore key concepts in the field.

For Electrical Engineering courses in analog layout or professional layout designers. This text covers the issues involved in successfully laying out analog integrated circuits. Hastings provides clear guidance and does not stress theoretical physics or mathematical analysis of layouts. He emphasizes cross-sections of devices and carrier-based models of device operation as compared to the more common geometric and schematic representation of devices.

Discover a fresh approach to efficient and insight-driven analog integrated circuit design in nanoscale-CMOS with this hands-on guide. Expert authors present a sizing methodology that employs SPICE-generated lookup tables, enabling close agreement between hand analysis and simulation. This enables the exploration of analog circuit tradeoffs using the gm/ID ratio as a central variable in script-based design flows, and eliminates time-consuming iterations in a circuit simulator. Supported by downloadable MATLAB code, and including over forty detailed worked examples, this book will provide professional analog circuit designers, researchers, and graduate students with the theoretical know-how and practical tools needed to acquire a systematic and re-use oriented design style for analog integrated circuits in modern CMOS. The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-

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established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability. This book begins with the premise that energy demands are directing scientists towards ever-greener methods of power management, so highly integrated power control ICs (integrated chip/circuit) are increasingly in demand for further reducing power consumption. A timely and comprehensive reference guide for IC designers dealing with the increasingly widespread demand for integrated low power management Includes new topics such as LED lighting, fast transient response, DVS-tracking and design with advanced technology nodes Leading author (Chen) is an active and renowned contributor to the power management IC design field, and has extensive industry experience Accompanying website includes presentation files with book illustrations, lecture notes, simulation circuits, solution manuals, instructors' manuals, and program downloads Equips students with essential industry-relevant knowledge through in-depth explanations, practical applications, examples, and exercises.

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This book is the first graduate-level textbook presenting a comprehensive treatment of Data Converters. The advancement of digital electronics urged the availability of a still missing support for teaching and self-learning analog-digital interfaces at many levels: the specification, the conversion methods and architectures, the circuit design and the testing. This book, after the necessary study of the background theoretical elements, covers aspects and provide elements for a deep and comprehensive knowledge. The breath and the level of details of topics is enhanced by introductory material in each chapter and the use of many examples, most of them in the form of computer behavioral simulations. The examples and the end-of-chapter problems help in understanding and favor self-practice using tools that are effective for training and for design activity. Data Converters is a textbook that is also essential for engineering professionals as it was written for responding to a shortage of organically organized material on the topic. The book assumes a solid background in analog and digital circuits as well as a working knowledge of simulation tools for circuit and behavioral analysis. A background on statistical analysis is also helpful, though not strictly necessary. Coverage of all the basic elements essential for a clear understanding of sampling, quantization, noise in sampled-data systems and mathematical tools for sampled-data linear systems Comprehensive definition of the parameters used to specify data converters and necessary for understanding product data sheets Coverage of all the architectures used in Nyquist-rate data converters and detailed study of features, limits and design techniques Detailed study of oversampled and Sigma-Delta converters with simulation examples and use of spectra and histograms for a clear understanding of features and limit if the noise shaping Coverage of digital correction and calibration techniques for enhancing performances Use of theory and

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intuitive views to explain circuits and systems operation and limits Coverage of testing methods and description of the data processing used for testing and characterization Extensive use of Simulink and Matlab in examples and problem sets to assist reader comprehension and favor deeper study

By helping students develop an intuitive understanding of the subject, Microelectronics teaches them to think like engineers. The second edition of Razavi's Microelectronics retains its hallmark emphasis on analysis by inspection and building students' design intuition, and it incorporates a host of new pedagogical features that make it easier to teach and learn from, including: application sidebars, self-check problems with answers, simulation problems with SPICE and MULTISIM, and an expanded problem set that is organized by degree of difficulty and more clearly associated with specific chapter sections.

Design of Analog CMOS Integrated CircuitsTata McGraw-Hill EducationDesign of Analog CMOS Integrated CircuitsMcGraw-Hill Higher EducationDesign of CMOS Phase-Locked LoopsFrom Circuit Level to Architecture LevelCambridge University Press

A large part of the research currently being conducted in the fields of materials science and engineering mechanics is devoted to carbon nanotubes and their applications. In this process, modeling is a very attractive investigation tool due to the difficulties in manufacturing and testing of nanomaterials. Continuum modeling offers significant advantages over atomistic modeling. Furthermore, the lack of accuracy in continuum methods can be overtaken by incorporating input data either from experiments or atomistic methods. This book reviews the recent progress in continuum modeling of carbon nanotubes and their composites. The advantages and disadvantages of continuum methods over atomistic methods are

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comprehensively discussed. Numerical models, mainly based on the finite element method, as well as analytical models are presented in a comparative way starting from the simulation of isolated pristine and defected nanotubes and proceeding to nanotube-based composites. The ability of continuum methods to bridge different scales is emphasized. Recommendations for future research are given by focusing on what still continuum methods have to learn from the nano-scale. The scope of the book is to provide current knowledge aiming to support researchers entering the scientific area of carbon nanotubes to choose the appropriate modeling tool for accomplishing their study and place their efforts to further improve continuum methods.

An expert guide to the new and emerging field of broadband circuits for optical fiber communication. This exciting publication makes it easy for readers to enter into and deepen their knowledge of the new and emerging field of broadband circuits for optical fiber communication. The author's selection and organization of material have been developed, tested, and refined from his many industry courses and seminars. Five types of broadband circuits are discussed in detail: \* Transimpedance amplifiers \* Limiting amplifiers \* Automatic gain control (AGC) amplifiers \* Lasers drivers \* Modulator drivers. Essential background on optical fiber, photodetectors, lasers, modulators, and receiver theory is presented to help readers understand the system environment in which these broadband circuits operate. For each circuit type, the main specifications and their impact on system performance are explained and illustrated with numerical values. Next, the circuit concepts are discussed and illustrated

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with practical implementations. A broad range of circuits in MESFET, HFET, BJT, HBT, BiCMOS, and CMOS technologies is covered. Emphasis is on circuits for digital, continuous-mode transmission in the 2.5 to 40 Gb/s range, typically used in SONET, SDH, and Gigabit Ethernet applications. Burst-mode circuits for passive optical networks (PON) and analog circuits for hybrid fiber-coax (HFC) cable-TV applications also are discussed. Learning aids are provided throughout the text to help readers grasp and apply difficult concepts and techniques, including:

- \* Chapter summaries that highlight the key points
- \* Problem-and-answer sections to help readers apply their new knowledge
- \* Research directions that point to exciting new technological breakthroughs on the horizon
- \* Product examples that show the performance of actual broadband circuits
- \* Appendices that cover eye diagrams, differential circuits, S-parameters, transistors, and technologies
- \* A bibliography that leads readers to more complete and in-depth treatment of specialized topics

This is a superior learning tool for upper-level undergraduates and graduate-level students in circuit design and optical fiber communication. Unlike other texts that concentrate on analog circuits in general or mostly on optics, this text provides balanced coverage of electronic, optic, and system issues. Professionals in the fiber optic industry will find it an excellent reference, incorporating the latest technology and discoveries in the industry.

The Acclaimed RF Microelectronics Best-Seller, Expanded and Updated for the Newest

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Architectures, Circuits, and Devices Wireless communication has become almost as ubiquitous as electricity, but RF design continues to challenge engineers and researchers. In the 15 years since the first edition of this classic text, the demand for higher performance has led to an explosive growth of RF design techniques. In *RF Microelectronics, Second Edition*, Behzad Razavi systematically teaches the fundamentals as well as the state-of-the-art developments in the analysis and design of RF circuits and transceivers. Razavi has written the second edition to reflect today's RF microelectronics, covering key topics in far greater detail. At nearly three times the length of the first edition, the second edition is an indispensable tome for both students and practicing engineers. With his lucid prose, Razavi now Offers a stronger tutorial focus along with hundreds of examples and problems Teaches design as well as analysis with the aid of step-by-step design procedures and a chapter dedicated to the design of a dual-band WiFi transceiver Describes new design paradigms and analysis techniques for circuits such as low-noise amplifiers, mixers, oscillators, and frequency dividers This edition's extensive coverage includes brand new chapters on mixers, passive devices, integer-N synthesizers, and fractional-N synthesizers. Razavi's teachings culminate in a new chapter that begins with WiFi's radio specifications and, step by step, designs the transceiver at the transistor level. Coverage includes Core RF principles, including noise and nonlinearity, with ties to analog design, microwave theory, and communication systems An intuitive treatment of modulation theory and

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wireless standards from the standpoint of the RF IC designer Transceiver architectures such as heterodyne, sliding-IF, directconversion, image-reject, and low-IF topologies. Low-noise amplifiers, including cascode common-gate and commonsource topologies, noise-cancelling schemes, and reactance-cancelling configurations Passive and active mixers, including their gain and noise analysis and new mixer topologies Voltage-controlled oscillators, phase noise mechanisms, and various VCO topologies dealing with noisepower-tuning trade-offs All-new coverage of passive devices, such as integrated inductors, MOS varactors, and transformers A chapter on the analysis and design of phase-locked loops with emphasis on low phase noise and low spur levels Two chapters on integer-N and fractional-N synthesizers, including the design of frequency dividers Power amplifier principles and circuit topologies along with transmitter architectures, such as polar modulation and outphasing This modern, pedagogic textbook from leading author Behzad Razavi provides a comprehensive and rigorous introduction to CMOS PLL design, featuring intuitive presentation of theoretical concepts, extensive circuit simulations, over 200 worked examples, and 250 end-of-chapter problems. The perfect text for senior undergraduate and graduate students.

Beginning with discussions on the operation of electronic devices and analysis of the nucleus of digital design, the text addresses: the impact of interconnect, design for low power, issues in timing and clocking, design methodologies, and the effect of design

automation on the digital design perspective.

- Applicable for bookstore catalogue

Featuring an extensive 40 page tutorial introduction, this carefully compiled anthology of 65 of the most important papers on phase-locked loops and clock recovery circuits brings you comprehensive coverage of the field—all in one self-contained volume. You'll gain an understanding of the analysis, design, simulation, and implementation of phase-locked loops and clock recovery circuits in CMOS and bipolar technologies along with valuable insights into the issues and trade-offs associated with phase locked systems for high speed, low power, and low noise.

Special Features: · Written by the author of the best-seller, CMOS: Circuit Design, Layout, and Simulation· Fills a hole in the technical literature for an advanced-tutorial book on mixed-signal circuit design from a circuit designer's point of view· Presents more advance topics, and will be an excellent companion to the first volume About The Book: This book will fill a hole in the technical literature for an advanced-tutorial book on mixed-signal circuit design. There are no competitors in this area. Mixed-signal design is performed in industry by a select few gurus . The techniques can be found in hard-to-digest technical papers.

Praise for CMOS: Circuit Design, Layout, and Simulation Revised Second Edition from the Technical Reviewers "A refreshing industrial flavor. Design concepts are presented as they are needed for 'just-in-time' learning. Simulating and designing circuits using

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SPICE is emphasized with literally hundreds of examples. Very few textbooks contain as much detail as this one. Highly recommended!" --Paul M. Furth, New Mexico State University "This book builds a solid knowledge of CMOS circuit design from the ground up. With coverage of process integration, layout, analog and digital models, noise mechanisms, memory circuits, references, amplifiers, PLLs/DLLs, dynamic circuits, and data converters, the text is an excellent reference for both experienced and novice designers alike." --Tyler J. Gomm, Design Engineer, Micron Technology, Inc. "The Second Edition builds upon the success of the first with new chapters that cover additional material such as oversampled converters and non-volatile memories. This is becoming the de facto standard textbook to have on every analog and mixed-signal designer's bookshelf." --Joe Walsh, Design Engineer, AMI Semiconductor CMOS circuits from design to implementation CMOS: Circuit Design, Layout, and Simulation, Revised Second Edition covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. Features include: Updated materials to reflect CMOS technology's movement into nanometer sizes Discussions on phase- and delay-locked loops, mixed-signal

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circuits, data converters, and circuit noise More than 1,000 figures, 200 examples, and over 500 end-of-chapter problems In-depth coverage of both analog and digital circuit-level design techniques Real-world process parameters and design rules The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid learning

Fundamentals of Microelectronics, 3rd Edition, is a comprehensive introduction to the design and analysis of electrical circuits, enabling students to develop the practical skills and engineering intuition necessary to succeed in their future careers. Through an innovative “analysis by inspection” framework, students learn to deconstruct complex problems into familiar components and reach solutions using basic principles. A step-by-step synthesis approach to microelectronics demonstrates the role of each device in a circuit while helping students build “design-oriented” mindsets. The revised third edition covers basic semiconductor physics, diode models and circuits, bipolar transistors and amplifiers, oscillators, frequency response, and more. In-depth chapters feature illustrative examples and numerous problems of varying levels of difficulty, including design problems that challenge students to select the bias and component values to satisfy particular requirements. The text contains a wealth of pedagogical tools, such as application sidebars, chapter summaries, self-tests with answers, and

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Multisim and SPICE software simulation problems. Now available in enhanced ePub format, Fundamentals of Microelectronics is ideal for single- and two-semester courses in the subject.

This is the only comprehensive book in the market for engineers that covers the design of CMOS and bipolar analog integrated circuits. The fifth edition retains its completeness and updates the coverage of bipolar and CMOS circuits. A thorough analysis of a new low-voltage bipolar operational amplifier has been added to Chapters 6, 7, 9, and 11. Chapter 12 has been updated to include a fully differential folded cascode operational amplifier example. With its streamlined and up-to-date coverage, more engineers will turn to this resource to explore key concepts in the field.

Top-down approach to practical, tool-independent, digital circuit design, reflecting how circuits are designed.

Fundamentals of Microelectronics, 2nd Edition is designed to build a strong foundation in both design and analysis of electronic circuits this text offers conceptual understanding and mastery of the material by using modern examples to motivate and prepare readers for advanced courses and their careers. The books unique problem-solving framework enables readers to deconstruct complex problems into components that they are familiar with which builds the confidence and intuitive skills needed for success.

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### Analog Circuit Design

The 2nd Edition of Analog Integrated Circuit Design focuses on more coverage about several types of circuits that have increased in importance in the past decade.

Furthermore, the text is enhanced with material on CMOS IC device modeling, updated processing layout and expanded coverage to reflect technical innovations. CMOS devices and circuits have more influence in this edition as well as a reduced amount of text on BiCMOS and bipolar information. New chapters include topics on frequency response of analog ICs and basic theory of feedback amplifiers.

Low-Power CMOS Design for Wireless Transceivers provides a comprehensive treatment of the challenges in low-power RF CMOS design. The author addresses trade-offs and techniques that improve the performance from the component level to the architectural level. Low-Power CMOS Design for Wireless Transceivers deals with the design and implementation of low- power wireless transceivers in a standard digital CMOS process. This includes architecture, circuits and monolithic passive components. The book is written for engineers and graduate students interested in learning about wireless networks, transceiver architectures, stacked inductors, design of RF front ends, and the design of a 2.4-GHz transceiver.

"The increasing demand for high-speed transport of data has revitalized optical communications, leading to extensive work on high-speed device and circuit design. This book deals with the design of high-speed integrated circuits for optical

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communication transceivers. Building upon a detailed understanding of optical devices, the book describes the analysis and design of critical building blocks, such as transimpedance and limiting amplifiers, laser drivers, phase-locked loops, oscillators, clock and data recovery circuits, and multiplexers. This second edition of this best selling textbook has been updated to provide information on the latest developments in the field"--

This advanced text and reference covers the design and implementation of integrated circuits for analog-to-digital and digital-to-analog conversion. It begins with basic concepts and systematically leads the reader to advanced topics, describing design issues and techniques at both circuit and system level. Gain a system-level perspective of data conversion units and their trade-offs with this state-of-the-art book. Topics covered include: sampling circuits and architectures, D/A and A/D architectures; comparator and op amp design; calibration techniques; testing and characterization; and more!

Based on the work of MIT graduate students Alice Wang and Benton Calhoun, this book surveys the field of sub-threshold and low-voltage design and explores such aspects of sub-threshold circuit design as modeling, logic and memory circuit design. One important chapter of the book is dedicated to optimizing energy dissipation - a key metric for energy constrained designs. This book also includes invited chapters on the subject of analog sub-threshold circuits.

Analog Integrated Circuits for Communication: Principles, Simulation and Design, Second Edition covers the analysis and design of nonlinear analog integrated circuits that form the basis of present-day communication systems. Both bipolar and MOS transistor circuits are analyzed and several numerical examples are used to illustrate the analysis and design

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techniques developed in this book. Especially unique to this work is the tight coupling between the first-order circuit analysis and circuit simulation results. Extensive use has been made of the public domain circuit simulator Spice, to verify the results of first-order analyses, and for detailed simulations with complex device models. Highlights of the new edition include: A new introductory chapter that provides a brief review of communication systems, transistor models, and distortion generation and simulation. Addition of new material on MOSFET mixers, compression and intercept points, matching networks. Revisions of text and explanations where necessary to reflect the new organization of the book Spice input files for all the circuit examples that are available to the reader from a website. Problem sets at the end of each chapter to reinforce and apply the subject matter. An instructors solutions manual is available on the book's webpage at [springer.com](http://springer.com). Analog Integrated Circuits for Communication: Principles, Simulation and Design, Second Edition is for readers who have completed an introductory course in analog circuits and are familiar with basic analysis techniques as well as with the operating principles of semiconductor devices. This book also serves as a useful reference for practicing engineers.

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