

Aurix 32 Bit Microcontrollers As The Basis For Adas

Building Secure Cars Assuring the Automotive Software Development Lifecycle
John Wiley & Sons

This is a "How-To" book which explains, with hands-on examples, how to design and implement a SuperSpeed USB peripheral that can interface to your hardware using a 32-bit 100MHz bus with standard or custom protocols. The book is based on the Cypress FX3 SuperSpeed Device and the firmware examples are written around a low-cost SuperSpeed Explorer board and a companion CPLD board which are available from www.cypress.com/fx3book. The software examples are written for the Windows operating system and the CPLD examples are written in Verilog. The source code for all of the examples is downloadable from the book web site. If you currently think that SuperSpeed USB design is only for the elite then look inside this book and discover that SuperSpeed technology has now been made accessible to the rest of us!

Without correct timing, there is no safe and reliable embedded software. This book shows how to consider timing early in the development process for embedded systems, how to solve acute timing problems, how to perform timing optimization, and how to address the aspect of timing verification. The book is organized in twelve chapters. The first three cover various basics of microprocessor technologies and the operating systems used therein. The next four chapters cover timing problems both in theory and

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practice, covering also various timing analysis techniques as well as special issues like multi- and many-core timing. Chapter 8 deals with aspects of timing optimization, followed by chapter 9 that highlights various methodological issues of the actual development process. Chapter 10 presents timing analysis in AUTOSAR in detail, while chapter 11 focuses on safety aspects and timing verification. Finally, chapter 12 provides an outlook on upcoming and future developments in software timing. The number of embedded systems that we encounter in everyday life is growing steadily. At the same time, the complexity of the software is constantly increasing. This book is mainly written for software developers and project leaders in industry. It is enriched by many practical examples mostly from the automotive domain, yet the vast majority of the book is relevant for any embedded software project. This way it is also well-suited as a textbook for academic courses with a strong practical emphasis, e.g. at applied sciences universities.

Features and Benefits

- * Shows how to consider timing in the development process for embedded systems, how to solve timing problems, and how to address timing verification
- * Enriched by many practical examples mostly from the automotive domain
- * Mainly written for software developers and project leaders in industry

This book offers the first comprehensive view on integrated circuit and system design for the Internet of Things (IoT), and in particular for the tiny nodes at its edge. The authors provide a fresh perspective on how the IoT will evolve based on recent and

foreseeable trends in the semiconductor industry, highlighting the key challenges, as well as the opportunities for circuit and system innovation to address them. This book describes what the IoT really means from the design point of view, and how the constraints imposed by applications translate into integrated circuit requirements and design guidelines. Chapter contributions equally come from industry and academia. After providing a system perspective on IoT nodes, this book focuses on state-of-the-art design techniques for IoT applications, encompassing the fundamental sub-systems encountered in Systems on Chip for IoT: ultra-low power digital architectures and circuits low- and zero-leakage memories (including emerging technologies) circuits for hardware security and authentication System on Chip design methodologies on-chip power management and energy harvesting ultra-low power analog interfaces and analog-digital conversion short-range radios miniaturized battery technologies packaging and assembly of IoT integrated systems (on silicon and non-silicon substrates). As a common thread, all chapters conclude with a prospective view on the foreseeable evolution of the related technologies for IoT. The concepts developed throughout the book are exemplified by two IoT node system demonstrations from industry. The unique balance between breadth and depth of this book: enables expert readers quickly to develop an understanding of the specific challenges and state-of-the-art solutions for IoT, as well as their evolution in the foreseeable future provides non-experts with a comprehensive introduction to integrated circuit design for IoT, and

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serves as an excellent starting point for further learning, thanks to the broad coverage of topics and selected references makes it very well suited for practicing engineers and scientists working in the hardware and chip design for IoT, and as textbook for senior undergraduate, graduate and postgraduate students (familiar with analog and digital circuits).

Looks at the combustion basics of fuel injection engines and offers information on such topics as VE equation, airflow estimation, setups and calibration, creating timing maps, and auxiliary output controls.

Deep learning networks are getting smaller. Much smaller. The Google Assistant team can detect words with a model just 14 kilobytes in size—small enough to run on a microcontroller. With this practical book you'll enter the field of TinyML, where deep learning and embedded systems combine to make astounding things possible with tiny devices. Pete Warden and Daniel Situnayake explain how you can train models small enough to fit into any environment. Ideal for software and hardware developers who want to build embedded systems using machine learning, this guide walks you through creating a series of TinyML projects, step-by-step. No machine learning or microcontroller experience is necessary. Build a speech recognizer, a camera that detects people, and a magic wand that responds to gestures Work with Arduino and ultra-low-power microcontrollers Learn the essentials of ML and how to train your own models Train models to understand audio, image, and accelerometer data Explore

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TensorFlow Lite for Microcontrollers, Google's toolkit for TinyML Debug applications and provide safeguards for privacy and security Optimize latency, energy usage, and model and binary size

This textbook is written for junior/senior undergraduate and first-year graduate students in the electrical and computer engineering departments. Using PSoC mixed-signal array design, the authors define the characteristics of embedd design, embedded mixed-signal architectures, and top-down design. Optimized implementations of these designs are included to illustrate the theory. Exercises are provided at the end of each chapter for practice. Topics covered include the hardware and software used to implement analog and digital interfaces, various filter structures, amplifiers and other signal-conditioning circuits, pulse-width modulators, timers, and data structures for handling multiple similar peripheral devices. The practical exercises contained in the companion laboratory manual, which was co-authored by Cypress Staff Applications Engineer Dave Van Ess, are also based on PSoC. PSoC's integrated microcontroller, highly configurable analog/digital peripherals, and a full set of development tools make it an ideal learning tool for developing mixed-signal embedded design skills.

The Only Official Guide to the Parallax Multicore Propeller Microcontroller Written by a team of Propeller experts, this authoritative guide shows you how to realize your design concepts by taking full advantage of the multicore Propeller microcontroller's unique architecture. The book begins with a review of the Propeller hardware, software, and Spin language so you can get

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started right away. Programming and Customizing the Multicore Propeller Microcontroller: The Official Guide is filled with a wide variety of step-by-step, hands-on projects. Put your ideas into production when you learn how to: Debug code for multiple cores Understand how the Propeller interacts with different sensors Wirelessly network Propeller chips Build a balancing robot and control it with computer vision Develop networking applications using an off-the-shelf Ethernet chip Create a portable multivariable GPS tracking and data logging device Use the Propeller as a remote virtual peripheral for media applications Create a Propeller-powered HVAC green house model Synthesize speech with the Propeller Experience more of the process at mhprofessional.com/propeller

This extensive and increasing use of embedded systems and their integration in everyday products mark a significant evolution in information science and technology. Nowadays embedded systems design is subject to seamless integration with the physical and electronic environment while meeting requirements like reliability, availability, robustness, power consumption, cost, and deadlines. Thus, embedded systems design raises challenging problems for research, such as security, reliable and mobile services, large-scale heterogeneous distributed systems, adaptation, component-based development, and validation and tool-based certification. This book results from the ARTIST FP5 project funded by the European Commission. By integration 28 leading European research institutions with many top researchers in the area, this book assesses and strategically advances the state of the art in embedded systems. The coherently written monograph-like book is a valuable source of reference for researchers active in the field and serves well as an introduction to scientists and professionals interested in learning about embedded systems design.

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Takes engine-tuning techniques to the next level. It is a must-have for tuners and calibrators and a valuable resource for anyone who wants to make horsepower with a fuel-injected, electronically controlled engine.

This book constitutes the proceedings of the 28th International Conference on Architecture of Computing Systems, ARCS 2015, held in Porto, Portugal, in March 2015. The 19 papers presented together with three invited papers were carefully reviewed and selected from 45 submissions. The papers are organized in six sessions covering the topics: hardware, design, applications, trust and privacy, real-time issues and a best papers session.

This book constitutes the proceedings of the Workshops held in conjunction with SAFECOMP 2019, 38th International Conference on Computer Safety, Reliability and Security, in September 2019 in Turku, Finland. The 32 regular papers included in this volume were carefully reviewed and selected from 43 submissions; the book also contains two invited papers. The workshops included in this volume are: ASSURE 2019: 7th International Workshop on Assurance Cases for Software-Intensive Systems DECSoS 2019: 14th ERCIM/EWICS/ARTEMIS Workshop on Dependable Smart Embedded and Cyber-Physical Systems and Systems-of-Systems SASSUR 2019: 8th International Workshop on Next Generation of System Assurance Approaches for Safety-Critical Systems STRIVE 2019: Second International Workshop on Safety, securITy, and pRivacy In automotiVe systEMs WAISE 2019: Second International Workshop on Artificial Intelligence Safety Engineering Arthur Kay's exciting new publication is a must have for practicing, professional electrical engineers. This comprehensive guide shows engineers how to design amplifiers and associated electronics to minimize noise, providing tricks, rules-of-thumb, and analysis to

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create successful low noise circuits. Forget the classical textbook traps of equations, virtual grounds, and a lot of double-speak, the novel but educational presentation used here uses definition-by-example and straight-forward analysis. This is the ultimate reference book for engineers who don't have the time to read, since the concepts are presented in detailed pictures and then repeated in the text for those who like both. Operational amplifiers play a vital role in modern electronics design. Today, op amps serve as the interfaces between the digital world of microprocessors, microcontrollers, and other digital circuits and the analog "real world". If an analog signal must be amplified, conditioned, filtered, or converted to be used by a digital system, an op amp is almost always involved. Noise is an unwanted signal that will corrupt or distort the desired signal, and veteran engineers as well as new college graduates are often faced with a lack of experience in noise analysis for operational amplifiers. The author has created a publication that is packed with essential information, while still being accessible to all readers. Clear, definition-by-example presentation allows for immediate use of techniques introduced Tricks and rules-of-thumb, derived from author's decades of experience Extreme use of figures for rapid absorption of concepts Concise text explains the key points in all figures Accessible to all types of readers Analysis and design of low-noise circuits using op amps, including design tradeoffs for low-noise Desktop reference for designing low-noise op amp circuits for novice to experienced engineers Accurate measurement and prediction of intrinsic noise levels, using analysis by hand and SPICE simulation

Digital Design and Computer Architecture: ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to

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digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor. By the end of this book, readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing an ARM processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Features side-by-side examples of the two most prominent Hardware Description Languages (HDLs)—SystemVerilog and VHDL—which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. The Companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises.

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Embedded software is in almost every electronic device in use today. There is software hidden away inside our watches, DVD players, mobile phones, antilock brakes, and even a few toasters. The military uses embedded software to guide missiles, detect enemy aircraft, and pilot UAVs. Communication satellites, deep-space probes, and many medical instruments would've been nearly impossible to create without it.

Someone has to write all that software, and there are tens of thousands of electrical engineers, computer scientists, and other professionals who actually do.

This book constitutes the refereed proceedings of five workshops co-located with SAFECOMP 2018, the 37th International Conference on Computer Safety, Reliability, and Security, held in Västerås, Sweden, in September 2018. The 28 revised full papers and 21 short papers presented together with 5 introductory papers to each workshop were carefully reviewed and selected from 73 submissions. This year's workshops are: ASSURE 2018 – Assurance Cases for Software-Intensive Systems; DECSoS 2018 – ERCIM/EWICS/ARTEMIS Dependable Smart Embedded and Cyber-Physical Systems and Systems-of-Systems; SASSUR 2018 – Next Generation of System Assurance Approaches for Safety-Critical Systems; STRIVE 2018 – Safety, securiT_y, and pRivacy In automotiVe systEmS; and WAISE 2018 – Artificial Intelligence Safety Engineering. The chapter "'Boxing Clever": Practical Techniques for Gaining Insights into Training Data and Monitoring Distribution Shift' is available open access under an Open Government License via link.springer.com.

BUILDING SECURE CARS Explores how the automotive industry can address the increased risks of cyberattacks and incorporate security into the software development lifecycle. While increased connectivity and advanced software-based automotive systems provide tremendous benefits and improved user experiences, they also make the modern vehicle highly susceptible to cybersecurity attacks. In response, the automotive industry is investing heavily in establishing cybersecurity engineering processes. Written by a seasoned automotive security expert with abundant international industry expertise, *Building Secure Cars: Assuring the Automotive Software Development Lifecycle* introduces readers to various types of cybersecurity activities, measures, and solutions that can be applied at each stage in the typical automotive development process. This book aims to assist auto industry insiders build more secure cars by incorporating key security measures into their software development lifecycle. Readers will learn to better understand common problems and pitfalls in the development process that lead to security vulnerabilities. To overcome such challenges, this book details how to apply and optimize various automated solutions, which allow software development and test teams to identify and fix vulnerabilities in their products quickly and efficiently. This book balances technical solutions with automotive technologies, making implementation practical. *Building Secure Cars* is: One of the first books to explain how the automotive industry can address the increased risks of cyberattacks, and how to incorporate security into the

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software development lifecycle An optimal resource to help improve software security with relevant organizational workflows and technical solutions A complete guide that covers introductory information to more advanced and practical topics Written by an established professional working at the heart of the automotive industry Fully illustrated with tables and visuals, plus real-life problems and suggested solutions to enhance the learning experience This book is written for software development process owners, security policy owners, software developers and engineers, and cybersecurity teams in the automotive industry. All readers will be empowered to improve their organizations' security postures by understanding and applying the practical technologies and solutions inside.

This book constitutes the proceedings of the 25th International Workshop on Formal Methods for Industrial Critical Systems, FMICS 2020, which was held during September 2-3, 2020. The conference was planned to take place in Vienna, Austria. Due to the COVID-19 pandemic it changed to a virtual event. The 11 full papers presented in this volume were carefully reviewed and selected from 26 submissions. The papers are organized in topical sections as follows: Quantitative Analysis and Cyber-Physical Systems, Formal Verification of Industrial Systems, Temporal Logic and Model Checking. The book also contains a lengthy report on a Formal Methods Survey conducted on occasion of the 25th edition of the conference.

This book is a compilation of the recent technologies and innovations in the field of

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automotive embedded systems with a special mention to the role of Internet of Things in automotive systems. The book provides easy interpretable explanations for the key technologies involved in automotive embedded systems. The authors illustrate various diagnostics over internet protocol and over-the-air update process, present advanced driver assistance systems, discuss various cyber security issues involved in connected cars, and provide necessary information about Autosar and Misra coding standards. The book is relevant to academics, professionals, and researchers.

This book presents deep analysis of machine control for different applications, focusing on its implementation in embedded systems. Necessary peripherals for various microcontroller families are analysed for machine control and software architecture patterns for high-quality software development processes in motor control units are described. Abundant figures help the reader to understand the theoretical, simulation and practical implementation stages of machine control. Model-based design, used as a mathematical and visual approach to construction of complex control algorithms, code generation that eliminates hand-coding errors, and co-simulation tools such as Simulink, PSIM and finite element analysis are discussed. The simulation and verification tools refine, and retest the models without having to resort to prototype construction. The book shows how a voltage source inverter can be designed with tricks, protection elements, and space vector modulation. Practical Control of Electric Machines: Model-Based Design and Simulation is based on the author's experience of

a wide variety of systems in domestic, automotive and industrial environments, and most examples have implemented and verified controls. The text is ideal for readers looking for an insight into how electric machines play an important role in most real-life applications of control. Practitioners and students preparing for a career in control design applied in electric machines will benefit from the book's easily understood theoretical approach to complex machine control. The book contains mathematics appropriate to various levels of experience, from the student to the academic and the experienced professional. Advances in Industrial Control reports and encourages the transfer of technology in control engineering. The rapid development of control technology has an impact on all areas of the control discipline. The series offers an opportunity for researchers to present an extended exposition of new work in all aspects of industrial control.

The book discusses the emerging topic of comprehensive energy management in electric vehicles from the viewpoint of academia and from the industrial perspective. It provides a seamless coverage of all relevant systems and control algorithms for comprehensive energy management, their integration on a multi-core system and their reliability assurance (validation and test). Relevant European projects contributing to the evolvement of comprehensive energy management in fully electric vehicles are also included. This volume includes contributions on model based functional safety and fault-tolerant E/E architectures, advanced control making use of external information (from a

cloud) as well and thermal management as a central part for energy optimization and finally some aspects on fuel cells. The second volume (ISBN) includes chapters on ECO driving and ECO routing covering different approaches for optimal speed profiles for a given route (mostly interconnecting with cloud data).

Nowadays, embedded systems - computer systems that are embedded in various kinds of devices and play an important role of specific control functions, have permeated various scenes of industry. Therefore, we can hardly discuss our life or society from now onwards without referring to embedded systems. For wide-ranging embedded systems to continue their growth, a number of high-quality fundamental and applied researches are indispensable. This book contains 13 excellent chapters and addresses a wide spectrum of research topics of embedded systems, including parallel computing, communication architecture, application-specific systems, and embedded systems projects. Embedded systems can be made only after fusing miscellaneous technologies together. Various technologies condensed in this book as well as in the complementary book "Embedded Systems - Theory and Design Methodology", will be helpful to researchers and engineers around the world.

This book constitutes the thoroughly refereed proceedings of the 11th International Conference on Security for Information Technology and

Communications, SecITC 2018, held in Bucharest, Romania, in November 2018. The 35 revised full papers presented together with 3 invited talks were carefully reviewed and selected from 70 submissions. The papers present advances in the theory, design, implementation, analysis, verification, or evaluation of secure systems and algorithms.

The history of flight started with the pioneer era. The introduction of mechanical controls (including hydraulics) then led to the second era. Later, with the utilization of computers and automation in aircraft, we reached the third era. Now, we are moving towards the fourth era of flight, namely Flight 4.0, which is characterized by “smart” and “connected” aircraft that extensively exploit emerging information and communication technologies. Aeronautical informatics is advancing rapidly through the synergy between information and communication technologies and aeronautics. Multi-core avionic platforms, wireless avionics networking, service-oriented architectures and IoT, data sciences and semantic infrastructures are shaping systems to come. Increasing autonomy requirements are challenging the community to investigate new ways to assure safety. Modern software engineering methodologies and real-time software techniques are altering the established development practice. Universities are starting to align their aerospace engineering and computer

science curriculums in order to address this synergy. This book is a unique compilation of advancements in aeronautical informatics, introducing the changing technology landscape of flight with respect to a new push in information and communication technology.

Designus Maximus Unleashed! is more than a collection of article reprints; in this book, the original (unedited) text is revisited, along with new insights and previously unpublished material, all presented in the author's distinctive personal style. The accompanying CD-ROM includes a fully-functioning virtual computer, as well as BOOL Logic Synthesis, MMLogic Multimedia Logic Design System, and Analog Magic. Clive Maxfield, a popular columnist, has collected his articles in a new order, grouped by topic, and expanded from the limits of magazine space. These articles have been published in magazines such as EDN, Electronic Design, and Electronic Design & Technology. In addition, he includes new material such as the history of computing, logic design tools, and the virtual computer. Two chapters of personal perspective begin and end the text. Clive 'Max' Maxfield received his B.S.C. in Control Engineering from Sheffield Polytechnic (now Sheffield Hallam University), England, and began his career as a mainframe CPU designer. He is currently a Member of the Technical Staff at Intergraph Computer Systems, Huntsville AL. In his spare time, Max is a

contributing editor to EDN magazine and a member of the advisory board to the Computer History Association of California. In addition to numerous technical articles and papers, Max is also the author of *Bebop to the Boolean Boogie* and the co-author of *Bebop BYTES Back (An Unconventional Guide to Computers)*. Based primarily on Designus Maximus series of articles from EDN magazine with new chapters and expanded text Includes a CD-ROM including the *Beboputer: Virtual Computer* Written by a popular columnist

It is a pleasure to present you the proceedings of the 12th International Symposium on Automotive Lighting, which takes place in Darmstadt on September 25-27, 2017. This conference is the document of a series of successful conferences since the first PAL-conference in 1995 and shows the latest innovative potentials of the automotive industry in the application of lighting technologies.

This book constitutes the proceedings of the 9th International Workshop on Model-Based Design of Cyber Physical Systems, CyPhy 2019 and 15th International Workshop on Embedded and Cyber-Physical Systems Education, WESE 2019, held in conjunction with ESWeek 2019, in New York City, NY, USA, in October 2019. The 13 full papers presented together in this volume were carefully reviewed and selected from 24 submissions. The conference presents a

wide range of domains including models and design; simulation and tools; formal methods; embedded and cyber-physical systems education.

This book constitutes the proceedings of the 32nd International Conference on Architecture of Computing Systems, ARCS 2019, held in Copenhagen, Denmark, in May 2019. The 24 full papers presented in this volume were carefully reviewed and selected from 40 submissions. ARCS has always been a conference attracting leading-edge research outcomes in Computer Architecture and Operating Systems, including a wide spectrum of topics ranging from embedded and real-time systems all the way to large-scale and parallel systems. The selected papers are organized in the following topical sections: Dependable systems; real-time systems; special applications; architecture; memory hierarchy; FPGA; energy awareness; NoC/SoC. The chapter 'MEMPower: Data-Aware GPU Memory Power Model' is open access under a CC BY 4.0 license at link.springer.com.

Drawing on a wealth of knowledge and experience and a background of more than 1,000 magazine articles on the subject, engine control expert Jeff Hartman explains everything from the basics of engine management to the building of complicated project cars. Hartman has substantially updated the material from his 1993 MBI book Fuel Injection (0-879387-43-2) to address the incredible developments in automotive fuel

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injection technology from the past decade, including the multitude of import cars that are the subject of so much hot rodding today. Hartman's text is extremely detailed and logically arranged to help readers better understand this complex topic.

This book gathers papers from the 23rd International Forum on Advanced Microsystems for Automotive Applications (AMAA 2020) held online from Berlin, Germany, on May 26-27, 2020. Focusing on intelligent system solutions for auto mobility and beyond, it discusses in detail innovations and technologies enabling electrification, automation and diversification, as well as strategies for a better integration of vehicles into the networks of traffic, data and power. Further, the book addresses other relevant topics, including the role of human factors and safety issues in automated driving, solutions for shared mobility, as well as automated bus transport in rural areas. Implications of current circumstances, such as those generated by climate change, on the future development of auto mobility, are also analysed, providing researchers, practitioners and policy makers with an authoritative snapshot of the state-of-the-art, and a source of inspiration for future developments and collaborations.

This book constitutes the refereed proceedings of four workshops co-located with SAFECOMP 2016, the 35th International Conference on Computer Safety, Reliability, and Security, held in Trondheim, Norway, in September 2016. The 30 revised full papers presented together with 4 short and 5 invited papers were carefully reviewed and selected from numerous submissions. This year's workshop are: ASSURE 2016 -

Assurance Cases for Software-intensive Systems; DECSoS 2016 - EWICS/ERCIM/ARTEMIS Dependable Cyber-physical Systems and Systems-of-Systems Workshop; SASSUR 2016 - Next Generation of System Assurance Approaches for Safety-Critical Systems; and TIPS 2016 – Timing Performance in Safety Engineering.

Nowadays, the prevalence of computing systems in our lives is so ubiquitous that we live in a cyber-physical world dominated by computer systems, from pacemakers to cars and airplanes. These systems demand for more computational performance to process large amounts of data from multiple data sources with guaranteed processing times. Actuating outside of the required timing bounds may cause the failure of the system, being vital for systems like planes, cars, business monitoring, e-trading, etc. High-Performance and Time-Predictable Embedded Computing presents recent advances in software architecture and tools to support such complex systems, enabling the design of embedded computing devices which are able to deliver high-performance whilst guaranteeing the application required timing bounds. Technical topics discussed in the book include: Parallel embedded platforms Programming models Mapping and scheduling of parallel computations Timing and schedulability analysis Runtimes and operating systems The work reflected in this book was done in the scope of the European project P?SOCRATES, funded under the FP7 framework program of the European Commission. High-performance and time-predictable embedded computing

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is ideal for personnel in computer/communication/embedded industries as well as academic staff and master/research students in computer science, embedded systems, cyber-physical systems and internet-of-things.

This volume contains the lectures given in honor to Georg Färber as tribute to his contributions in the area of real-time and embedded systems. The chapters of many leading scientists cover a wide range of aspects, like robot or automotive vision systems or medical aspects.

In this new edition the latest ARM processors and other hardware developments are fully covered along with new sections on Embedded Linux and the new freeware operating system eCOS. The hot topic of embedded systems and the internet is also introduced. In addition a fascinating new case study explores how embedded systems can be developed and experimented with using nothing more than a standard PC. * A practical introduction to the hottest topic in modern electronics design * Covers hardware, interfacing and programming in one book * New material on Embedded Linux for embedded internet systems

This book constitutes the proceedings of the 14th International Workshop on Open MP, IWOMP 2018, held in Barcelona, Spain, in September 2018. The 16 full papers presented in this volume were carefully reviewed and selected for inclusion in this book. The papers are organized in topical sections named: best paper; loops and OpenMP; OpenMP in heterogeneous systems; OpenMP improvements and innovations; OpenMP

