

An Asic Low Power Primer Analysis Techniques And Specification

Top-down approach to practical, tool-independent, digital circuit design, reflecting how circuits are designed.

This book describes RTL design using Verilog, synthesis and timing closure for System On Chip (SOC) design blocks. It covers the complex RTL design scenarios and challenges for SOC designs and provides practical information on performance improvements in SOC, as well as Application Specific Integrated Circuit (ASIC) designs. Prototyping using modern high density Field Programmable Gate Arrays (FPGAs) is discussed in this book with the practical examples and case studies. The book discusses SOC design, performance improvement techniques, testing and system level verification, while also describing the modern Intel FPGA/XILINX FPGA architectures and their use in SOC prototyping. Further, the book covers the Synopsys Design Compiler (DC) and Prime Time (PT) commands, and how they can be used to optimize complex ASIC/SOC designs. The contents of this book will be useful to students and professionals alike.

This book details molecular methodologies used in identifying a disease gene, from the initial stage of study design to the next stage of preliminary locus identification, and ending with stages involved in target characterization and validation.

Microwave dielectric materials play a key role in our global society with a wide range of applications, from terrestrial and satellite communication including software radio, GPS, and DBS TV to environmental monitoring via satellite. A small ceramic component made from a dielectric material is fundamental to the operation of filters and oscillators in several microwave systems. In microwave communications, dielectric resonator filters are used to discriminate between wanted and unwanted signal frequencies in the transmitted and received signal. When the wanted frequency is extracted and detected, it is necessary to maintain a strong signal. For clarity it is also critical that the wanted signal frequencies are not affected by seasonal temperature changes. In order to meet the specifications of current and future systems, improved or new microwave components based on dedicated dielectric materials and new designs are required. The recent progress in microwave telecommunication, satellite broadcasting and intelligent transport systems (ITS) has resulted in an increased demand for Dielectric Resonators (DRs). With the recent revolution in mobile phone and satellite communication systems using microwaves as the propagation media, the research and development in the field of device miniaturization has been a major challenge in contemporary Materials Science. In a mobile phone communication, the message is sent from a phone to the nearest base station, and then on via a series of base stations to the other phone. At the heart of each base station is the combiner/filter unit which has the job of receiving

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the messages, keeping them separate, amplifying the signals and sending them onto the next base station. For such a microwave circuit to work, part of it needs to resonate at the specific working frequency. The frequency determining component (resonator) used in such a high frequency device must satisfy certain criteria. The three important characteristics required for a dielectric resonator are (a) a high dielectric constant which facilitates miniaturization (b) a high quality factor (Q_{xf}) which improves the signal-to-noise ratio, (c) a low temperature coefficient of the resonant frequency which determines the stability of the transmitted frequency. During the past 25 years scientists the world over have developed a large number of new materials (about 3000) or improved the properties of known materials. About 5000 papers have been published and more than 1000 patents filed in the area of dielectric resonators and related technologies. This book brings the data and science of these several useful materials together, which will be of immense benefit to researchers and engineers the world over. The topics covered in the book includes factors affecting the dielectric properties, measurement of dielectric properties, important low loss dielectric material systems such as perovskites, tungsten bronze type materials, materials in BaO-TiO₂ system, (Zr,Sn)TiO₄, alumina, rutile, AnBn-1O3n type materials, LTCC, ceramic-polymer composites etc. The book also has a data table listing all reported low loss dielectric materials with properties and references arranged in the order of increasing dielectric constant. Collects together in one source data on all new materials used in wireless communication Includes tabulated properties of all reported low loss dielectric materials In-depth treatment of dielectric resonator materials

The implementation of Enterprise Networks or e-Networking is of paramount importance for organisations. Enterprise-wide networking would warrant that the components of information architecture are organised to harness more out of the organisation's computing power on the desktop. This would also involve establishment of networks that link the various but important subsystems of the enterprise. Our firm belief is that in order to gain a competitive edge the organisations need knowledge and sound strategy. This conviction is particularly true today, considering the pressures from international competition, environmental concerns and complicated ethical issues. This book, entitled A Manager's Primer on e-Networking, negotiates the hyper dimensions of the Internet through stories from myriad of Web sites with its fluent presentation and simple but chronological organisation of topics highlighting numerous opportunities and providing a solid starting point not only for inexperienced entrepreneurs and managers but anyone interested in applying information technology in the business. I sincerely hope the book will help as well many small and medium size companies and organisations to launch corporate networking successfully in order to attain their strategic objectives. Rajiv Jayashankar, Ph. D. Based on the popular Artech House classic, Digital Communication Systems Engineering with Software-Defined Radio, this book provides a practical

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approach to quickly learning the software-defined radio (SDR) concepts needed for work in the field. This up-to-date volume guides readers on how to quickly prototype wireless designs using SDR for real-world testing and experimentation. This book explores advanced wireless communication techniques such as OFDM, LTE, WLA, and hardware targeting. Readers will gain an understanding of the core concepts behind wireless hardware, such as the radio frequency front-end, analog-to-digital and digital-to-analog converters, as well as various processing technologies. Moreover, this volume includes chapters on timing estimation, matched filtering, frame synchronization message decoding, and source coding. The orthogonal frequency division multiplexing is explained and details about HDL code generation and deployment are provided. The book concludes with coverage of the WLAN toolbox with OFDM beacon reception and the LTE toolbox with downlink reception. Multiple case studies are provided throughout the book. Both MATLAB and Simulink source code are included to assist readers with their projects in the field.

Whether you are a student, a newly-minted engineer entering the field of power electronics, a salesperson needing to understand a customer's needs, or a seasoned power supply designer desiring to track down a forgotten equation, this book will be a significant aid. Beginning with the basic definition of a power supply, we will traverse through voltage regulation techniques and the components necessary for their implementation, and then move on to the myriad of circuit topologies and control algorithms prevalent in modern-day design solutions. Separate chapters on feedback-loop compensation and magnetic design principles will build on this foundation, along with in-depth descriptions for dealing with regulations for electromagnetic compatibility, human safety, and energy efficiency issues. Additional chapters will describe the value proposition for digital control and the practical aspects power supply construction.

The analysis and sorting of large numbers of cells with a fluorescence-activated cell sorter (FACS) was first achieved some 30 years ago. Since then, this technology has been rapidly developed and is used today in many laboratories. A Springer Lab Manual Review of the First Edition: "This is a most useful volume which will be a welcome addition for personal use and also for laboratories in a wide range of disciplines. Highly recommended." CYTOBIOS

Unfriendly to conventional electronic devices, circuits, and systems, extreme environments represent a serious challenge to designers and mission architects. The first truly comprehensive guide to this specialized field, Extreme Environment Electronics explains the essential aspects of designing and using devices, circuits, and electronic systems intended to operate in extreme environments, including across wide temperature ranges and in radiation-intense scenarios such as space. The Definitive Guide to Extreme Environment Electronics Featuring contributions by some of the world's foremost experts in extreme environment electronics, the book provides in-depth information on a wide array of topics. It begins by describing the extreme conditions and then delves into a description of suitable semiconductor technologies and the modeling of devices within those technologies. It also discusses reliability

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issues and failure mechanisms that readers need to be aware of, as well as best practices for the design of these electronics. Continuing beyond just the "paper design" of building blocks, the book rounds out coverage of the design realization process with verification techniques and chapters on electronic packaging for extreme environments. The final set of chapters describes actual chip-level designs for applications in energy and space exploration. Requiring only a basic background in electronics, the book combines theoretical and practical aspects in each self-contained chapter. Appendices supply additional background material. With its broad coverage and depth, and the expertise of the contributing authors, this is an invaluable reference for engineers, scientists, and technical managers, as well as researchers and graduate students. A hands-on resource, it explores what is required to successfully operate electronics in the most demanding conditions.

Is wind power the answer to our energy supply problems? Is there enough wind for everyone? Is offshore generation better than onshore generation? Can a roof-mounted wind turbine generate enough electricity to supply a typical domestic household? Electricity Generation Using Wind Power (2nd Edition) answers these pressing questions through its detailed coverage of the different types of electrical generator machines used, as well as the power electronic converter technologies and control principles employed. Also covered is the integration of wind farms into established electricity grid systems, plus environmental and economic aspects of wind generation. Written for technically minded readers, especially electrical engineers concerned with the possible use of wind power for generating electricity, it incorporates some global meteorological and geographical features of wind supply plus a survey of past and present wind turbines. Included is a technical assessment of the choice of turbine sites. The principles and analysis of wind power conversion, transmission and efficiency evaluation are described. This book includes worked numerical examples in some chapters, plus end of chapter problems and review questions, with answers. As a textbook it is pitched at the level of final year undergraduate engineering study but may also be useful as a textbook or reference for wider technical studies.

Until now, there has been a lack of a complete knowledge base to fully comprehend Low power (LP) design and power aware (PA) verification techniques and methodologies and deploy them all together in a real design verification and implementation project. This book is a first approach to establishing a comprehensive PA knowledge base. LP design, PA verification, and Unified Power Format (UPF) or IEEE-1801 power format standards are no longer special features. These technologies and methodologies are now part of industry-standard design, verification, and implementation flows (DVIF). Almost every chip design today incorporates some kind of low power technique either through power management on chip, by dividing the design into different voltage areas and controlling the voltages, through PA dynamic and PA static verification, or their combination. The entire LP design and PA verification process involves thousands of techniques, tools, and methodologies, employed from the register transfer level (RTL) of design abstraction down to the synthesis or place-and-route levels of physical design. These techniques, tools, and methodologies are evolving everyday through the progression of design-verification complexity and more intelligent ways of handling that complexity by engineers, researchers, and corporate engineering policy makers.

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This book describes in detail all required technologies and methodologies needed to create a comprehensive, functional design verification strategy and environment to tackle the toughest job of guaranteeing first-pass working silicon. The author first outlines all of the verification sub-fields at a high level, with just enough depth to allow an engineer to grasp the field before delving into its detail. He then describes in detail industry standard technologies such as UVM (Universal Verification Methodology), SVA (SystemVerilog Assertions), SFC (SystemVerilog Functional Coverage), CDV (Coverage Driven Verification), Low Power Verification (Unified Power Format UPF), AMS (Analog Mixed Signal) verification, Virtual Platform TLM2.0/ESL (Electronic System Level) methodology, Static Formal Verification, Logic Equivalency Check (LEC), Hardware Acceleration, Hardware Emulation, Hardware/Software Co-verification, Power Performance Area (PPA) analysis on a virtual platform, Reuse Methodology from Algorithm/ESL to RTL, and other overall methodologies.

Build safety-critical and memory-safe stand-alone and networked embedded systems
Key Features Know how C++ works and compares to other languages used for embedded development Create advanced GUIs for embedded devices to design an attractive and functional UI Integrate proven strategies into your design for optimum hardware performance
Book Description C++ is a great choice for embedded development, most notably, because it does not add any bloat, extends maintainability, and offers many advantages over different programming languages. Hands-On Embedded Programming with C++17 will show you how C++ can be used to build robust and concurrent systems that leverage the available hardware resources. Starting with a primer on embedded programming and the latest features of C++17, the book takes you through various facets of good programming. You'll learn how to use the concurrency, memory management, and functional programming features of C++ to build embedded systems. You will understand how to integrate your systems with external peripherals and efficient ways of working with drivers. This book will also guide you in testing and optimizing code for better performance and implementing useful design patterns. As an additional benefit, you will see how to work with Qt, the popular GUI library used for building embedded systems. By the end of the book, you will have gained the confidence to use C++ for embedded programming. What you will learn Choose the correct type of embedded platform to use for a project Develop drivers for OS-based embedded systems Use concurrency and memory management with various microcontroller units (MCUs) Debug and test cross-platform code with Linux Implement an infotainment system using a Linux-based single board computer Extend an existing embedded system with a Qt-based GUI Communicate with the FPGA side of a hybrid FPGA/SoC system Who this book is for If you want to start developing effective embedded programs in C++, then this book is for you. Good knowledge of C++ language constructs is required to understand the topics covered in the book. No knowledge of embedded systems is assumed.

This is the first book devoted to low power circuit design, and its authors have been among the first to publish papers in this area.· Low-Power CMOS VLSI Design· Physics of Power Dissipation in CMOS FET Devices· Power Estimation· Synthesis for Low Power· Design and Test of Low-Voltage CMOS Circuits· Low-Power Static Ram Architectures· Low-Energy Computing Using Energy Recovery Techniques· Software Design for Low Power

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The Maritime Engineering Reference Book is a one-stop source for engineers involved in marine engineering and naval architecture. In this essential reference, Anthony F. Molland has brought together the work of a number of the world's leading writers in the field to create an inclusive volume for a wide audience of marine engineers, naval architects and those involved in marine operations, insurance and other related fields. Coverage ranges from the basics to more advanced topics in ship design, construction and operation. All the key areas are covered, including ship flotation and stability, ship structures, propulsion, seakeeping and maneuvering. The marine environment and maritime safety are explored as well as new technologies, such as computer aided ship design and remotely operated vehicles (ROVs). Facts, figures and data from world-leading experts makes this an invaluable ready-reference for those involved in the field of maritime engineering. Professor A.F. Molland, BSc, MSc, PhD, CEng, FRINA. is Emeritus Professor of Ship Design at the University of Southampton, UK. He has lectured ship design and operation for many years. He has carried out extensive research and published widely on ship design and various aspects of ship hydrodynamics. * A comprehensive overview from best-selling authors including Bryan Barrass, Rawson and Tupper, and David Eyres * Covers basic and advanced material on marine engineering and Naval Architecture topics * Have key facts, figures and data to hand in one complete reference book

Many modern computer systems, including homogeneous and heterogeneous architectures, support shared memory in hardware. In a shared memory system, each of the processor cores may read and write to a single shared address space. For a shared memory machine, the memory consistency model defines the architecturally visible behavior of its memory system. Consistency definitions provide rules about loads and stores (or memory reads and writes) and how they act upon memory. As part of supporting a memory consistency model, many machines also provide cache coherence protocols that ensure that multiple cached copies of data are kept up-to-date. The goal of this primer is to provide readers with a basic understanding of consistency and coherence. This understanding includes both the issues that must be solved as well as a variety of solutions. We present both high-level concepts as well as specific, concrete examples from real-world systems. This second edition reflects a decade of advancements since the first edition and includes, among other more modest changes, two new chapters: one on consistency and coherence for non-CPU accelerators (with a focus on GPUs) and one that points to formal work and tools on consistency and coherence.

FPGA Prototyping Using Verilog Examples will provide you with a hands-on introduction to Verilog synthesis and FPGA programming through a "learn by doing" approach. By following the clear, easy-to-understand templates for code development and the numerous practical examples, you can quickly develop and simulate a sophisticated digital circuit, realize it on a prototyping device, and verify the operation of its physical implementation. This introductory text that will provide you with a solid foundation, instill confidence with rigorous examples for complex systems and prepare you for future development tasks.

This book provides an invaluable primer on the techniques utilized in the design of low power digital semiconductor devices. Readers will benefit from the hands-

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on approach which starts from the ground-up, explaining with basic examples what power is, how it is measured and how it impacts on the design process of application-specific integrated circuits (ASICs). The authors use both the Unified Power Format (UPF) and Common Power Format (CPF) to describe in detail the power intent for an ASIC and then guide readers through a variety of architectural and implementation techniques that will help meet the power intent. From analyzing system power consumption, to techniques that can be employed in a low power design, to a detailed description of two alternate standards for capturing the power directives at various phases of the design, this book is filled with information that will give ASIC designers a competitive edge in low-power design.

Primer on Multiple Sclerosis, 2nd Edition is an updated reference manual for the practicing clinician. It covers the range of information needed to treat persons with MS, beginning with basic science and immunopathology, thorough differential diagnosis, symptom management and disease modifying therapies. This essential book also includes material covering new and experimental strategies as well as a review of commonly used complementary and alternative modalities that are used by persons with MS. Multiple Sclerosis (MS) is the most common demyelinating disease of the CNS and the third most common cause of disability among young adults. The complex management issues that are often present in the care of individuals with MS may demand the participation of health care professionals from a variety of disciplines, although the team is usually led by a neurologist. It is therefore essential for the neurologist to have a thorough grounding in the basic science and clinical phenomenology of MS. In this second edition of Primer on Multiple Sclerosis, the latest updates on therapeutics are provided, including new medications that have been FDA- approved since the first edition. Includes new diagnostic criteria, as well as any advances made in current diagnostic techniques, e.g. new imaging metrics. Important new information in the basic sciences and pathophysiology of MS is provided as well as newer epidemiologic studies. Treatment algorithms for common symptoms will be expanded, as well as any new guidelines for switching medications for Disease Modifying treatment "failures". The chapter on alternative and complimentary therapies discusses new research on CCSVI. The chapter on legal issues includes information on the putative effect of the Affordable Health Care Act on access to neurologic care and treatments. Finally, there is expanded discussion of progressive forms of MS both from a basic science and treatment perspective.

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SystemC provides a robust set of extensions to C++ that enables rapid development of complex hardware/software systems. This book focuses on the practical uses of the language for modeling real systems. The wealth of examples and downloadable code methodically guide the reader through the

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finer points of the SystemC language. This work provides: - A step-by-step build-up of syntax - NEW features of SystemC 2.1 - Code examples for each concept, - Many resource references - Coding styles and guidelines - Over 52 downloadable code examples (over 8,000 lines) - Exercises throughout the book - How SystemC fits into the system design methodology - Why features are as they are Well known consultants in the EDA industry, both David Black and Jack Donovan have been involved in the adoption and teaching of new technologies and methodologies for a combined total of 42+ years. Recently, they jointly founded a consultancy, Eklectic Ally, focused on helping companies adopt SystemC methodologies.

This book is an excellent resource to get up to speed on the application of the various features of SystemVerilog per IEEE 1800-2009. The explanations of each feature is provided with examples and guidelines, where appropriate. This book is well organized and full of concrete examples that illustrates well on how to use SystemVerilog. It is a must primer for anyone who is beginning to learn SystemVerilog.

The chips in present-day cell phones already contain billions of sub-100-nanometer transistors. By 2020, however, we will see systems-on-chips with trillions of 10-nanometer transistors. But this will be the end of the miniaturization, because yet smaller transistors, containing just a few control atoms, are subject to statistical fluctuations and thus no longer useful. We also need to worry about a potential energy crisis, because in less than five years from now, with current chip technology, the internet alone would consume the total global electrical power! This book presents a new, sustainable roadmap towards ultra-low-energy (femto-Joule), high-performance electronics. The focus is on the energy-efficiency of the various chip functions: sensing, processing, and communication, in a top-down spirit involving new architectures such as silicon brains, ultra-low-voltage circuits, energy harvesting, and 3D silicon technologies. Recognized world leaders from industry and from the research community share their views of this nanoelectronics future. They discuss, among other things, ubiquitous communication based on mobile companions, health and care supported by autonomous implants and by personal carebots, safe and efficient mobility assisted by co-pilots equipped with intelligent micro-electromechanical systems, and internet-based education for a billion people from kindergarden to retirement. This book should help and interest all those who will have to make decisions associated with future electronics: students, graduates, educators, and researchers, as well as managers, investors, and policy makers. Introduction: Towards Sustainable 2020 Nanoelectronics.- From Microelectronics to Nanoelectronics.- The Future of Eight Chip Technologies.- Analog–Digital Interfaces.- Interconnects and Transceivers.- Requirements and Markets for Nanoelectronics.- ITRS: The International Technology Roadmap for Semiconductors.- Nanolithography.- Power-Efficient Design Challenges.- Superprocessors and Supercomputers.- Towards Terabit Memories.- 3D

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Integration for Wireless Multimedia.- The Next-Generation Mobile User-Experience.- MEMS (Micro-Electro-Mechanical Systems) for Automotive and Consumer.- Vision Sensors and Cameras.- Digital Neural Networks for New Media.- Retinal Implants for Blind Patients.- Silicon Brains.- Energy Harvesting and Chip Autonomy.- The Energy Crisis.- The Extreme-Technology Industry.- Education and Research for the Age of Nanoelectronics.- 2020 World with Chips. Over 100 practical recipes to make Apache Solr faster, more reliable and return better results.

Timing, timing, timing! That is the main concern of a digital designer charged with designing a semiconductor chip. What is it, how is it described, and how does one verify it? The design team of a large digital design may spend months architecting and iterating the design to achieve the required timing target. Besides functional verification, the timing closure is the major milestone which dictates when a chip can be released to the semiconductor foundry for fabrication. This book addresses the timing verification using static timing analysis for nanometer designs. The book has originated from many years of our working in the area of timing verification for complex nanometer designs. We have come across many design engineers trying to learn the background and various aspects of static timing analysis. Unfortunately, there is no book currently available that can be used by a working engineer to get acquainted with the details of static timing analysis. The chip designers lack a central reference for information on timing, that covers the basics to the advanced timing verification procedures and techniques.

The design of today's semiconductor chips for various applications, such as telecommunications, poses various challenges due to the complexity of these systems. These highly complex systems-on-chips demand new approaches to connect and manage the communication between on-chip processing and storage components and networks on chips (NoCs) provide a powerful solution. This book is the first to provide a unified overview of NoC technology. It includes in-depth analysis of all the on-chip communication challenges, from physical wiring implementation up to software architecture, and a complete classification of their various Network-on-Chip approaches and solutions. * Leading-edge research from world-renowned experts in academia and industry with state-of-the-art technology

implementations/trends * An integrated presentation not currently available in any other book * A thorough introduction to current design methodologies and chips designed with NoCs Salespeople, consultants, managers, executives, entrepreneurs. . . Influence is a crucial tool for absolutely anyone seeking success and prosperity. But how can everyday people actually become more influential? Maximum Influence unlocks the secrets of the master influencers. Now in an all-new edition, the book combines scientific research with real-world studies, presenting the most authoritative and effective arsenal of persuasion techniques ever. Author and renowned expert Kurt Mortensen reveals the 12 Laws of Persuasion, explaining why each law works, how to use it, and what to avoid. You will learn about the law of dissonance, the law of contrast, the law of expectation-and nine other proven principles that consciously and unconsciously propel people to act. You will also discover how to: * Read anyone instantly * Get people to trust you instinctively * Change minds easily * And convince anyone to give you almost anything With new case studies and cutting-edge influencing techniques, this is the ultimate guide to the art and science of getting exactly what you want-when you want.

Formal Verification: An Essential Toolkit for Modern VLSI Design presents practical approaches for design and validation, with hands-on advice to help working engineers integrate these techniques into their work. Formal Verification (FV) enables a designer to directly analyze and mathematically explore the quality or other aspects of a Register Transfer Level (RTL) design without using simulations. This can reduce time spent validating designs

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and more quickly reach a final design for manufacturing. Building on a basic knowledge of SystemVerilog, this book demystifies FV and presents the practical applications that are bringing it into mainstream design and validation processes at Intel and other companies. After reading this book, readers will be prepared to introduce FV in their organization and effectively deploy FV techniques to increase design and validation productivity. Learn formal verification algorithms to gain full coverage without exhaustive simulation Understand formal verification tools and how they differ from simulation tools Create instant test benches to gain insight into how models work and find initial bugs Learn from Intel insiders sharing their hard-won knowledge and solutions to complex design problems

An authoritative introduction to the exciting new technologies of digital money Bitcoin and Cryptocurrency Technologies provides a comprehensive introduction to the revolutionary yet often misunderstood new technologies of digital currency. Whether you are a student, software developer, tech entrepreneur, or researcher in computer science, this authoritative and self-contained book tells you everything you need to know about the new global money for the Internet age. How do Bitcoin and its block chain actually work? How secure are your bitcoins? How anonymous are their users? Can cryptocurrencies be regulated? These are some of the many questions this book answers. It begins by tracing the history and development of Bitcoin and cryptocurrencies, and then gives the conceptual and practical foundations you need to engineer secure software that interacts with the Bitcoin network as well as to integrate ideas from Bitcoin into your own projects. Topics include decentralization, mining, the politics of Bitcoin, altcoins and the cryptocurrency ecosystem, the future of Bitcoin, and more. An essential introduction to the new technologies of digital currency Covers the history and mechanics of Bitcoin and the block chain, security, decentralization, anonymity, politics and regulation, altcoins, and much more Features an accompanying website that includes instructional videos for each chapter, homework problems, programming assignments, and lecture slides Also suitable for use with the authors' Coursera online course Electronic solutions manual (available only to professors)

Ten Strategies of a World-Class Cyber Security Operations Center conveys MITRE's accumulated expertise on enterprise-grade computer network defense. It covers ten key qualities of leading Cyber Security Operations Centers (CSOCs), ranging from their structure and organization, to processes that best enable smooth operations, to approaches that extract maximum value from key CSOC technology investments. This book offers perspective and context for key decision points in structuring a CSOC, such as what capabilities to offer, how to architect large-scale data collection and analysis, and how to prepare the CSOC team for agile, threat-based response. If you manage, work in, or are standing up a CSOC, this book is for you. It is also available on MITRE's website, www.mitre.org.

The latest knowledge on mineral ore genesis and the exploration of ore deposits Global demand for metals has risen considerably over the past decade. Geologists are developing new approaches for studying ore deposits and discovering new sources. Ore Deposits: Origin, Exploration, and Exploitation is a compilation of diverse case studies on new prospects in ore deposit geology including atypical examples of mineral deposits and new methods for ore exploration. Volume highlights include: Presentation of the latest research on a range of ore deposit types Application of ore deposits to multiple areas of geology and geophysical exploration Emphasis on diverse methods and tools for the study of ore deposits Useful case studies for geologists in both academia and industry Ore Deposits: Origin, Exploration, and Exploitation is a valuable resource for economic geologists, mineralogists, petrologists, geochemists, mining engineers, research professionals, and advanced students in relevant areas of academic study.

••PCI EXPRESS is considered to be the most general purpose bus so it should appeal to a wide audience in this arena. •Today's buses are becoming more specialized to meet the needs

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of the particular system applications, building the need for this book. •Mindshare and their only competitor in this space, Solari, team up in this new book.

SystemVerilog is a Hardware Description Language that enables designers to work at the higher levels of logic design abstractions that match the increased complexity of current day integrated circuit and field-programmable gate array (FPGA) designs. The majority of the book assumes a basic background in logic design and software programming concepts. It is directed at: * students currently in an introductory logic design course that also teaches SystemVerilog, * designers who want to update their skills from Verilog or VHDL, and * students in VLSI design and advanced logic design courses that include verification as well as design topics. The book starts with a tutorial introduction on hardware description languages and simulation. It proceeds to the register-transfer design topics of combinational and finite state machine (FSM) design - these mirror the topics of introductory logic design courses. The book covers the design of FSM-datapath designs and their interfaces, including SystemVerilog interfaces. Then it covers the more advanced topics of writing testbenches including using assertions and functional coverage. A comprehensive index provides easy access to the book's topics. The goal of the book is to introduce the broad spectrum of features in the language in a way that complements introductory and advanced logic design and verification courses, and then provides a basis for further learning. Solutions to problems at the end of chapters, and text copies of the SystemVerilog examples are available from the author as described in the Preface.

This book provides a comprehensive overview of the VLSI design process. It covers end-to-end system on chip (SoC) design, including design methodology, the design environment, tools, choice of design components, handoff procedures, and design infrastructure needs. The book also offers critical guidance on the latest UPF-based low power design flow issues for deep submicron SOC designs, which will prepare readers for the challenges of working at the nanotechnology scale. This practical guide will provide engineers who aspire to be VLSI designers with the techniques and tools of the trade, and will also be a valuable professional reference for those already working in VLSI design and verification with a focus on complex SoC designs. A comprehensive practical guide for VLSI designers; Covers end-to-end VLSI SoC design flow; Includes source code, case studies, and application examples.

With this book, you can: - Start writing synthesizable Verilog models quickly. - See what constructs are supported for synthesis and how these map to hardware so that you can get the desired logic. - Learn techniques to help avoid having functional mismatches. - Immediately start using many of the models for commonly used hardware elements described for your own use or modify these for your own application.

The fundamental mathematical tools needed to understand machine learning include linear algebra, analytic geometry, matrix decompositions, vector calculus, optimization, probability and statistics. These topics are traditionally taught in disparate courses, making it hard for data science or computer science students, or professionals, to efficiently learn the mathematics. This self-contained textbook bridges the gap between mathematical and machine learning texts, introducing the mathematical concepts with a minimum of prerequisites. It uses these concepts to derive four central machine learning methods: linear regression, principal component analysis, Gaussian mixture models and support vector machines. For students and others with a mathematical background, these derivations provide a starting point to machine learning texts. For those learning the mathematics for the first time, the methods help build intuition and practical experience with applying mathematical concepts. Every chapter includes worked examples and exercises to test understanding. Programming tutorials are offered on the book's web site.

This book provides a practical guide for engineers doing low power System-on-Chip (SoC) designs. It covers various aspects of low power design from architectural issues and design

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techniques to circuit design of power gating switches. In addition to providing a theoretical basis for these techniques, the book addresses the practical issues of implementing them in today's designs with today's tools.

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